

ABL Software Environment
For Assembly Language Programming

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ABSTRACT

ABL software environment for
assembly language programming

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The development and implementation of a software environment using ABL (Alternative-Based Language) for TI-980 assembly language is described. The ABL concept and utilities, emphasizing structured design technique, modularization and top-down refinement, is found suited for assembly language programmers. The application of the ABL methodology and the software environment is illustrated with the design of a utility for debugging of computer graphics software.

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1. INTRODUCTION

With the emergence of software engineering, the importance of methodology on software quality and productivity has been widely recognized. While programming in high-level language has gradually changed and improved with the use of software techniques such as structured programming, top-down design, modularization..., and of structured languages such as PL/I, PASCAL, C, ADA ..., programming in assembly language still has not changed noticeably. Although high-level languages are used more in applications which until quite recently were written in assembly language, a very significant amount of software is still programmed in assembly language. Despite the impact of new software techniques and concepts on high-level environment, today's assembly language programming is still basically of free style and non-structured. Its quality and productivity leaves much to be desired.

It is observed that many techniques and concepts which are used in high-level language programming can also be used in assembly language programming. Contrary to the popular belief, it is possible and feasible to implement a system approach in assembly language programming.

The purpose of this project is to attempt to improve programming in TI-980 assembly language by implementing the programming concept ABL (alternative-based language).

2. ABL PRINCIPLES AND ENVIRONMENT

ABL is a programming concept and style which is remarkably different from conventional, sequential approach. In ABL technique, the program flow section is completely separated from the program components which perform actions on data objects. The separation is maintained during all stages of software activities (design, writing, testing and execution). A description of ABL concept can be found in JAWO81.

The ABL process of design and implementation of a software system consists of the following steps :

1. Divide the project into a number of tasks called abstract programs $PR(i)$ where $i := 1 \dots N$.

2. Each program $PR(i)$ is sub-divided into a set of sub-tasks, called clusters $C(j)$.

(labelled C_1, C_2, \dots, C_{10} in figure 1)

3. For each cluster $C(j)$ define a set of alternatives $AL(k)$
(labelled K_0, K_1, K_2, \dots)

4. For each alternative $AL(k)$

- 4.1 Define the set of predicates $P(l)$.

The predicates act like "guards" for its alternative, i.e. only if the predicates of an alternative in the present cluster are satisfied then the execution of its actions selected from actions A_1, A_2, \dots will proceed. Each of the predicates may be a simple or compound predicate which is a function of other variables, but always returns a

boolean value

Y (YES) = true

or

N (NO) = false

(predicates are labelled P0,P1,...,P6 in figure 1)

4.2 Define the set of actions to be executed AC(m) .

An action may be simple or compound. A simple action is a subroutine. A compound action is another abstract program PR(n) at the next lower abstraction level n.

(The actions are labelled A1 to A29. The entries in each alternative indicate the order of actions to be executed)

4.3 Define the next cluster N(k).

In the end of action execution, the control is transferred to the cluster indicated by N(k) ;

If N(k)=0, the current abstract program will be terminated and control is returned to calling program.

(labelled N)

The cluster, the predicates of alternatives and the next cluster completely determine the program flow. The interpreter of abstract programs selects next alternative by matching current cluster identifier and values of predicates to cluster and predicate entries respectively.

For example :

If current cluster = C5 and predicates P0,P1 = false then

- alternative K7 is selected

- actions A8, A11, A12 are executed

- C6 is selected as next-cluster

and whole process of selecting next alternative is repeated.

ALTERNATIVE IDENTIFIERS K0,....,K19		#	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	
CLUSTER MATRIX	C1	V	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	EDITOR INITIALIZATION
	C2	-	V	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	READ ACTION I/P LINE
	C3	-	-	V	V	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	PROCESS ACTION I/P LINE
	C4	-	-	-	V	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	INIT. FOR ALT. I/P LINE
	C5	-	-	-	-	V	V	V	-	-	-	-	-	-	-	-	-	-	-	-	-	ANALYZE ALT. I/P LINE
	C6	-	-	-	-	-	-	-	V	V	-	-	-	-	-	-	-	-	-	-	-	PROCESS CLUSTER SPEC.
	C7	-	-	-	-	-	-	-	-	V	V	V	V	V	-	-	-	-	-	-	-	PROCESS PRED. SPEC.
	C8	-	-	-	-	-	-	-	-	-	V	-	-	-	V	V	V	-	-	-	-	PROCESS ACTION SPEC.
	C9	-	-	-	-	-	-	-	-	-	V	-	-	-	-	-	-	-	-	V	-	PROCESS NXT CLTR
	C10	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	V	CLOSING
NEXT-CLUSTER VECTOR	N	2	3	2	4	5	4	A	6	7	5	7	7	8	8	8	8	9	9	4	#	NEXT CLUSTER
PREDICATE MATRIX	P0	-	-	-	-	Y	N	N	-	-	-	-	-	-	-	-	-	-	-	-	-	** IS 1ST CHAR
	P1	-	-	N	Y	-	N	Y	N	-	-	-	-	-	-	-	-	-	-	-	-	*S' IS 1ST CHAR
	P2	-	-	-	-	-	-	-	-	Y	N	N	-	Y	N	-	-	-	-	-	-	' IS NEXT SEPERATOR
	P3	-	-	-	-	-	-	-	-	N	Y	N	Y	-	Y	N	-	-	-	-	-	' IS NEXT SEPERATOR
	P4	-	-	-	-	-	-	-	-	N	N	Y	Y	-	N	Y	-	-	-	-	-	' IS NEXT SEPERATOR
	P5	-	-	-	-	-	-	-	-	N	N	N	N	Y	N	N	Y	-	-	-	-	CPTR=LAPTR (NULL)
P6	-	-	-	N	N	N	-	N	Y	N	N	N	N	N	N	N	N	N	N	N	-	INVALID CHAR.
ALTERNATIVE IDENTIFIERS K0,....,K19		#	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	
ACTION MATRIX	A1	1	INIT :INITIALIZATION
	A2	2	FNAME :GET FILENAME & OPEN
	A3	.	1	STACT :STORE ACTION
	A4	.	.	1	RPI :READ PROG INFO.
	A5	.	.	.	2	PINIT :INIT'N FOR PROG.
	A6	1	RPTRS :RESET CPTR,LAPTR
	A7	1	WCL :WRITE COMMENT LINE
	A8	1	.	2	PER :PROMPT FOR REPLAC.
	A9	1	.	1	RESTOR:RESTORE POINTERS
	A10	1	RAIL :READ ACTION I/P LINE
	A11	.	.	.	2	.	.	2	.	3	RIL :READ INPUT LINE
	A12	2	.	.	3	.	.	3	.	4	AFC :ANALYZE 1ST CHAR
	A13	1	ICPTR :INCREMENT CPTR
	A14	.	.	.	4	SAVEP :SAVE POINTERS
	A15	1	RPTRS :RESET PTRS TO CLTR
	A16	2	.	1	2	1	1	1	D2BCON:DEC-BIN CONVERSION
	A17	3	SACLS :SAVE CLUSTER NUMBER
	A18	2	.	2	SBVNP :SET BIT IN YNPBUF
	A19	3	3	3	2	SBMPR :SET BIT IN MPRBUF
	A20	4	3	1	WYN&M :WRITE YNPBUF & MPRBUF
	A21	2	2	PAILN :PLACE ACTION
	A22	3	1	PT :PLACE TERMINATOR
	A23	4	4	4	5	4	2	3	4	PCAL :POINT CPTR AFTER LAPTR
	A24	5	5	5	5	5	3	4	5	PNSC :NXT SEP. & CLASS.
	A25	1	WNCN :WRITE NEXT CLUSTER NO.
	A26	2	WCYM :WRITE CL.YN,MP
	A27	3	SABICB:SET BIT IN CLR BUFFER
	A28	1	WCM :WRITE TO CLR MATRIX
	A29	2	CPF :CREATE PERMANENT FILE
ABSTRACT PROGRAM											ABSTRACT MACHINE											

FIGURE 1. Abstract program in matrix form :
 - Cluster matrix : C1-C10
 - Next-cluster vector : N
 - Predicate matrix : P0-P6
 - Action matrix : A1-A29

3. IMPLEMENTATION OF ABL IN TI-980 ASSEMBLY LANGUAGE

3.1 HOST SYSTEM

The ABL assembly language environment was implemented on the following system configuration :

HARDWARE :

- TI-980 mini-computer with 64K words of primary memory
- 2 DIVA disk drives, each 80 mega words of random access secondary storage
- TI SILENT 700 ASR console
- ANN ARBOR CRT terminal,
- VERSATEC printer/plotter

SOFTWARE :

- CAE's operating system SIMTOS (Simulator Iterative Multi-Terminal Operating System)
- TI-980 assembler
- Various CAE-developed software utilities

3.2 DESCRIPTION OF THE IMPLEMENTED SYSTEM

Under the ABL assembly environment, a program consists of a pair : abstract program (or program) and abstract machine (or machine) . Abstract program contains all the informations in cluster matrix , next-cluster vector, predicate matrix and action matrix, and represents the control flow. Abstract machine is a set of subroutines each of which carries out a specific action.

The ABL environment consists of the following utilities :

- ABL editor
- Text editor
- ABL interpreter
- Assembler
- Linker

Figure 2 illustrates the steps during software development in ABL environment. To develop and run a software system, the ABL editor is first used to create a file containing abstract program in TI-980 assembly language format. The text editor is used to create abstract machine (subroutines) file. These files, in assembly language format, are passed through the assembler which will produce object code files. The linker will link the object code files to the ABL interpreter which at run time will execute actions of the abstract machine as specified in the abstract program.

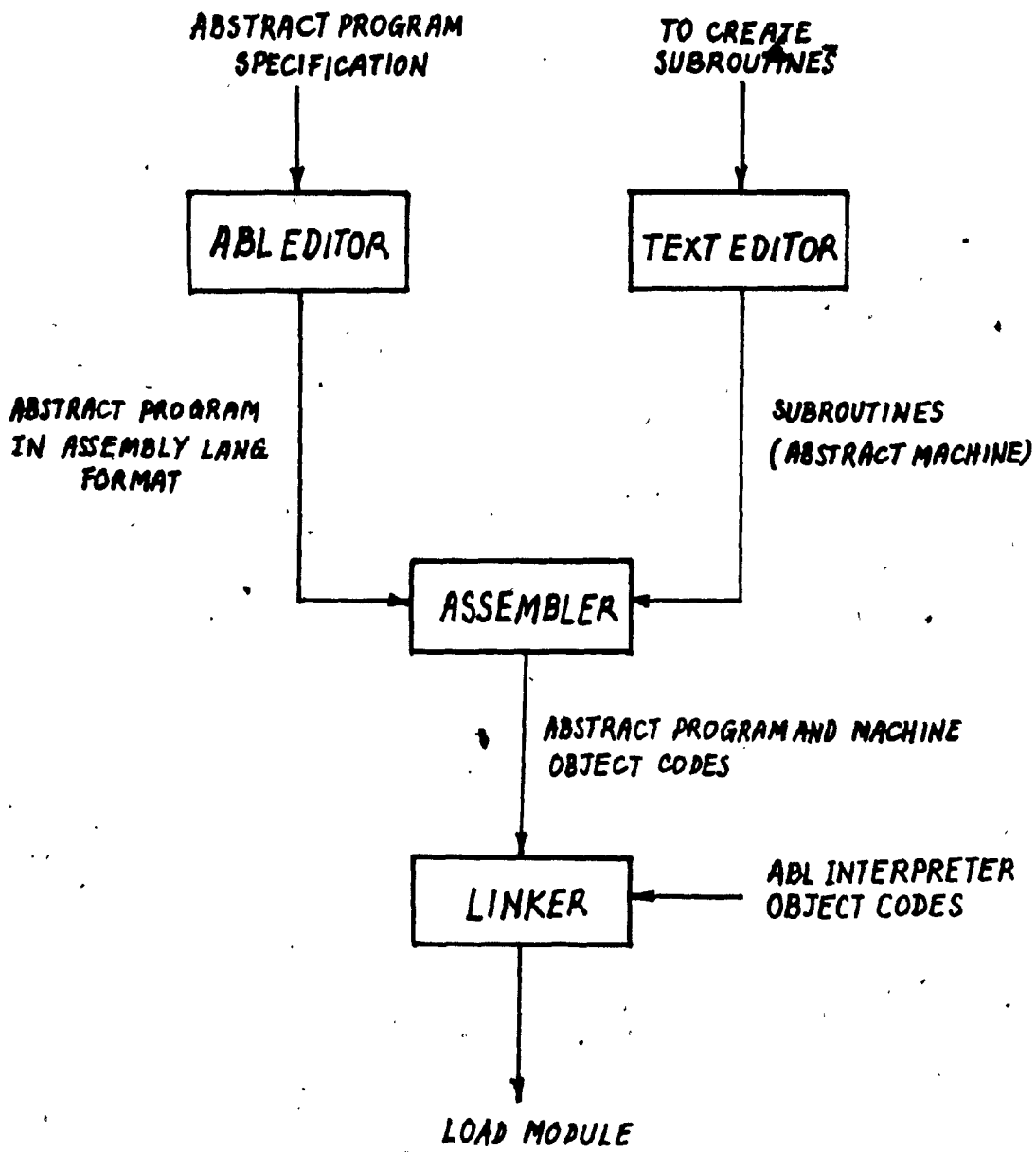


FIGURE 2. Development stages in ABL assembly environment.

3.3 REPRESENTATION OF ABSTRACT PROGRAM IN TI-980 ASSEMBLY LANGUAGE

Figure 3 shows the general structure of abstract program in assembly language, which consists of the following parts :

- Program number : for identification
- Abstract program status block. This status block represents the current status of abstract program at run time. The status block contains the current cluster, the current alternative, the action being executed within the alternative.
- Returning address and address of the calling abstract program if the present abstract program is called by another abstract program.
- Predicates of the state vector of abstract program.
- All matrices of abstract program (see figure 1) : cluster matrix, next-cluster vector, predicate matrix and action matrix.

To minimize memory requirement, the matrices are represented in binary form and there is one pointer pointing to beginning of each matrix. The size of abstract program varies depending on its complexity, therefore there is no unused memory.

A cluster is represented by 2 words, 32 bits. Each bit corresponds to one alternative. A bit in cluster is set to one if the alternative corresponding to that bit belongs to that cluster. Otherwise it is set to zero. For example, cluster 3 in figure 1 is represented as 3000 0000 (HEX) since it contains alternatives 2 and 3.

Predicate matrix is represented by two binary matrices : YES/NO matrix for true/false conditions and mask matrix for "don't

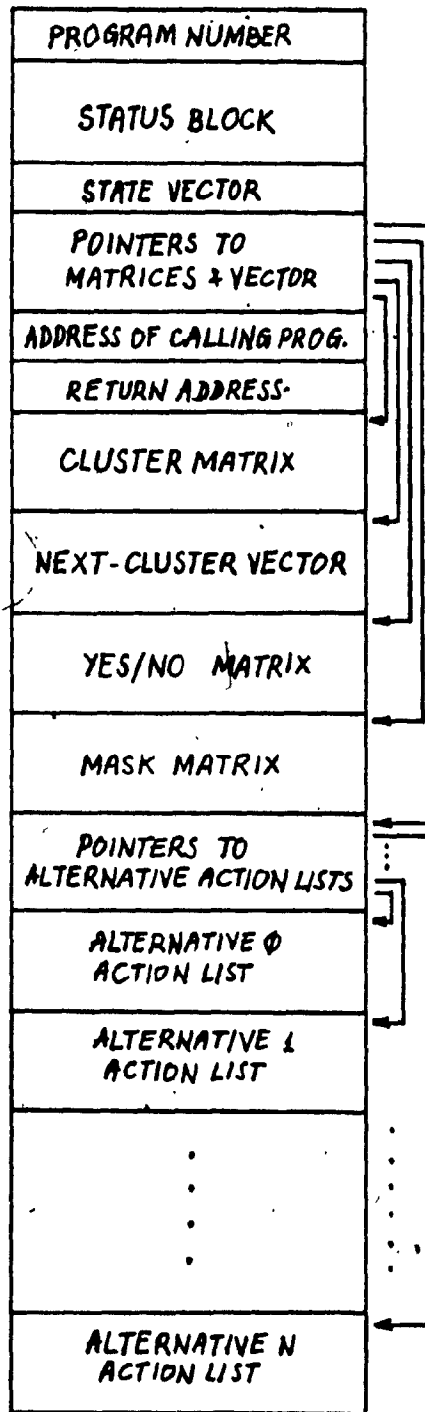


FIGURE 3. Structure of abstract program in assembly language.

care" conditions. A YES condition is represented by the corresponding bit set to one, whereas a NO condition to zero. In both cases, the corresponding bit in mask matrix is set to one. "Don't care" condition is represented by the corresponding bit in mask matrix set to zero.

Action matrix specifies actions of the alternatives. Each alternative has an action list which contains subroutine names which, when assembled and/or linked, will result in subroutine addresses. The action list is terminated by '0'. For each alternative, there is a pointer pointing to the beginning of its action list. These pointers are placed in increasing order, i.e. the pointer to the action list of alternative 0 is followed by the pointer to the action list of alternative 1 and so on. This structure allows an alternative to have any number of actions and at the same time makes efficient use of memory.

Shown in figure 4 is the abstract program from figure 1 represented in this structure :

- PROGNO contains program number.
- PCLUST, PALT, PACTOF, ACLST@ constitute the abstract program status block at run time.
- SVECTR is the predicates of the state vector at run time.
- CLSM@, NCLMA@, YNPMA@, MPRM@ are pointers to cluster matrix CLSMAT, next-cluster vector NCLMAT, predicate matrix YNPMAT, mask matrix MPRMAT respectively.
- APTR0@ is the pointer to the action list pointer area APTR00. APTR00 again contains pointers in order, each pointing to the action

list of an alternative. For example, the first word in APTR00
contains ACLS00 (refer to figure 4) which is the address of the
action list of alternative 0. The alternative 0 has actions INIT
and FNAME.

```

PROGNO DATA 3
*
PCLUST DATA #
PALT DATA #
PACT DATA #
PACTOF DATA #
ACLST# DATA #
*
SVECTR DATA #
*
CLSM# DATA CLSMAT
NCLM# DATA NCLMAT
YNPM# DATA YNPMAT
MPRM# DATA MPRMAT
APTR# DATA APTR#
*
BREG DATA #
LREG DATA #
*
*
PROGRAM NO.
PRESENT CLUSTER
" ALT
" ACTION
" ACTION OFFSET IN ALTERNATIVE
PTR TO ACTION LIST FORALT BEING EXE'D
STATE VECTOR.
*
POINTER TO CLUSTER MATRIX
" NEXT-CLUSTER VECTOR
" PREDICATE MATRIX
" PRED MASK MATRIX
" ACTION LIST POINTERS
*
(B)=ADDRESS OF PREVIOUS ABSTR. PROGRAM
(L)=RETURN ADDRESS
*
*
CLUSTER MATRIX BITS#-31
*
CLSMAT DATA >8000,>0000 C1
DATA >4000,>0000 C2
DATA >3000,>0000 C3
DATA >0000,>0000 C4
DATA >0700,>0000 C5
DATA >00C0,>0000 C6
DATA >007E,>0000 C7
DATA >0041,>C000 C8
DATA >0040,>2000 C9
DATA >0000,>1000 C10
*
*
NEXT CLUSTER
*
NCLMAT DATA 2 ALT #
DATA 3 1
DATA 2 2
DATA 4 3
DATA 5 4
DATA 4 5
DATA 10 6
DATA 6 7
DATA 7 8
DATA 5 9
DATA 7 10
DATA 7 11
DATA 8 12
DATA 8 13
DATA 8 14
DATA 8 15
DATA 9 16
DATA 9 17
DATA 4 18
DATA # 19
*
*
Y/N PREDICATE MATRIX
*
YNPMAT DATA >0000 ALT #
DATA >0000 1
DATA >0000 2
DATA >4000 3
DATA >0000 4
DATA >0000 5

```

FIGURE 4. Abstract program from figure 1
represented in TI-980 assembly language

```

DATA >4000      6
DATA >0000      7
DATA >0000      8
DATA >0200      9
DATA >2000     10
DATA >3000     11
DATA >0000     12
DATA >1000     13
DATA >0400     14
DATA >2000     15
DATA >0000     16
DATA >0400     17
DATA >0000     18
DATA >0000     19

```

MASK PREDICATE MATRIX

```

MPRMAT DATA >0000      ALT 0
DATA >0000      1
DATA >4000      2
DATA >4000      3
DATA >0200      4
DATA >C200      5
DATA >C200      6
DATA >C000      7
DATA >0200      8
DATA >0200      9
DATA >3E00     10
DATA >3E00     11
DATA >3E00     12
DATA >3E00     13
DATA >0600     14
DATA >2C00     15
DATA >2C00     16
DATA >0400     17
DATA >0200     18
DATA >0000     19

```

ACTION LIST POINTERS

```

APTR00 DATA ACLST0      PTR TO ACTION LIST OF ALT 1
DATA ACLST1      2
DATA ACLST2
DATA ACLST3
DATA ACLST4
DATA ACLST5
DATA ACLST6
DATA ACLST7
DATA ACLST8
DATA ACLST9
DATA ACLT10
DATA ACLT11
DATA ACLT12
DATA ACLT13
DATA ACLT14
DATA ACLT15
DATA ACLT16
DATA ACLT17
DATA ACLT18
DATA ACLT19

```

```

ACLST0 BSS 0      ALT 0 : INITIALIZATION
DATA INIT      ABL EDITOR INIT'N
DATA FNAME      GET FILE NAME & DO A/O
DATA 0

```

FIGURE 4. (continued)

```

ACLST1 BSS # ALT 1 : READ MACHINE I/P LINE
      DATA RAIL READ ACTION I/P LINE
      DATA AFC ANALYZE FIRST CHARACTER
      DATA #
*
ACLST2 BSS # ALT 2 : ACTION I/P LINE (SUB NAME)
      DATA STACT STORE ACTION INTO TEMP. FILE
      DATA #
*
ACLST3 BSS # ALT 3 : MACHINE TERMINATION
      DATA RPI READ INFO. ON PROGRAM SIZE
      DATA PINIT INIT'N FOR PROGRAM CREATION
      DATA #
*
ACLST4 BSS # ALT 4 : READ & ANALYZE I/P LINE FOR
      DATA CLRSV CLEAR STATE VERTOR
      DATA RPTRS RESET CPTR, LAPTR
      DATA RIL READ ALT I/P LINE
      DATA AFC ANALYZE 1ST CHAR
      DATA SAVEP SAVE POINTERS
      DATA #
*
ACLST5 BSS # ALT 5 : WRITE COMMENT LINE
      DATA WCL WRITE COMMENT LINE
      DATA #
*
ACLST6 BSS # ALT 6 : PROGRAM I/P TERMINATION
      DATA #
*
ACLST7 BSS # ALT 7 : ALT SPEC I/P LINE
      DATA PFR PROMPT FOR REPLACEMENT
      DATA RIL READ I/P LINE
      DATA AFC ANALYZE 1ST CHARACTER
      DATA #
*
ACLST8 BSS # ALT 8 : PROCESS CLUSTER SPEC
      DATA RPTRS RESET POINTERS TO CLUSTER POS.
      DATA D2BCON DEC TO BIN CONVERSION
      DATA SACLS SAVE CLUSTER
      DATA PCL POINT CPTR AFTER LAPTR
      DATA PNCS POINTER TO NEXT SEP & CLASSIFY
      DATA #
*
ACLST9 BSS # ALT 9 : INVALID I/P CHARACTER
      DATA RESTOR RESTORE POINTERS
      DATA PFR PROMPT FOR REPLACEMENT
      DATA RIL READ I/P LINE
      DATA AFC ANALYZE 1ST CHARACTER
      DATA #
*
ACLST10 BSS # ALT 10 : POSITIVE PREDICATE
      DATA D2BCON DEC TO BIN CONVERSION
      DATA SBYNP SET BIT IN YNP BUFFER
      DATA SBMPR SET BIT IN MPR BUFFER
      DATA PCAL POINT CPTR AFTER LAPTR
      DATA PNCS POINT TO NEXT SEP. & CLASSIFY
      DATA #
*
ACLST11 BSS # ALT 11 : NEGATIVE PREDICATE
      DATA ICPTR INCREMENT CPTR
      DATA D2BCON DEC TO BIN CONVERSION
      DATA SBMPR SET BIT IN MASK PREDICATE MATRIX
      DATA PCAL POINT CPTR AFTER LAPTR
      DATA PNCS POINT TO NEXT SEPERATOR & CLASSIFY
      DATA #

```

FIGURE 4. (continued) 642

```

ACLT12 BSS 0 ALT 12: POSITIVE PREDICATE, LAST PREDICATE
DATA D2BCON DEC TO BIN CONVERSION
DATA SBYNP SET BIT IN YNP BUFFER
DATA SBMPR SET BIT IN MASK PREDICATE MATRIX
DATA PCAL POINT CPTR AFTER LAPTR
DATA WYN&M WRITE YNPBUF & MPRBUF
DATA PNSC POINT TO NEXT SEP. & CLASSIFY
DATA 0

*
ACLT13 BSS 0 ALT 13: NEGATIVE PREDICATE, LAST PREDICATE
DATA D2BCON DEC TO BIN CONVERSION
DATA SBMPR SET BIT IN MASK PREDICATE MATRIX
DATA PCAL POINT CPTR AFTER LAPTR
DATA PNSC POINT TO NEXT SEPERATOR & CLASSIFY
DATA 0

*
ACLT14 BSS 0 ALT 14: NO PREDICATE SPECIFICATION
DATA PCAL POINT CPTR AFTER LAPTR
DATA PNSC POINT TO NEXT SEP. & CLASSIFY
DATA 0

*
ACLT15 BSS 0 ALT 15: ACTION NUMBER
DATA D2BCON DEC TO BIN CONVERSION
DATA PAILN PLACE ACTION IN ACTION LIST
DATA PCAL POINT CPTR AFTER LAPTR
DATA PNSC POINT TO NEXT SEPERATOR & CLASSIFY
DATA 0

*
ACLT16 BSS 0 ALT 16: LAST ACTION NUMBER
DATA D2BCON DEC TO BIN CONVERSION
DATA PAILN PLACE ACTION IN ACTION LIST
DATA PT PLACE TERMINATOR IN ACTION LIST
DATA PCAL POINT CPTR AFTER LAPTR
DATA PNSC POINT TO NEXT SEPERATOR & CLASSIFY
DATA 0

*
ACLT17 BSS 0 ALT 17: NO ACTION SPECIFICATION
DATA PT PLACE TERMINATOR
DATA ICPTR INCREMENT CPTR
DATA 0

*
ACLT18 BSS 0 ALT 18: PROCESS NEXT CLUSTER
DATA WNCN WRITE NEXT CLUSTER NUMBER
DATA WYM WRITE TO YN, MASK MATRIX
DATA SABICB SET ALT BIT IN CLS. BUFFER
DATA 0

*
ACLT19 BSS 0 ALT 19: CLOSING
DATA WCM WRITE TO CLUSTER MATRIX
DATA CPF CREATE PERMANENT FILE
DATA 0

```

FIGURE 4. (continued)

3.4 ABL EDITOR

3.4.1 Description

The process of transforming abstract program from its vertical form (Figure 1) into TI-980 assembly language format (Figure 4) is often very time-consuming and error-prone. Therefore, a software utility which handles this process automatically is naturally needed: the ABL editor accepts the specification of abstract program in a convenient and efficient way from the user and generates a corresponding source file, structured as discussed in section 3.3 (Figure 4), in TI-980 assembly language format.

The ABL editor creates programs by requesting the user to input information through the following steps :

- File name :

For the abstract program to be created. A file name, under SIMTOS, has the form AMNRRRT, where :

A : alphabet A,...,Z

M : alphanumeric A,...,Z,0,...,9

N : ASCII

RR: revision level 00,...,99

T : file type

The editor accepts three characters for AMN and opens a source file (type S) with highest revision.

- Abstract machine :

For the abstract machine on which the abstract program will

run on. The abstract machine is a set of subroutines. A subroutine in assembly language is symbolically represented by a label which is actually an address. A subroutine name is entered as a label of up to six characters and optionally comment part. Each subroutine, when entered, is numbered by the ABL editor. The subroutines of abstract machine are referenced in abstract program specification by its number. An abstract machine can have any number of subroutines.

- Abstract program :

Through this step, the user specifies the following information :

- Program number
- Number of clusters
- Number of alternatives
- Alternative specification

The user inputs alternative specification by inputting the specification for each alternative which consists of a comment part for the alternative and the actual specification itself :

- Cluster to which the alternative belongs
- Its relative order within the cluster
- Predicates (negative or positive) for the alternative
- Subroutines (actions) to be executed
- Next-cluster to transfer to when execution of actions is completed

Alternative specification is input sequentially : the first specification is for the first alternative, the second specification is for the second alternative, etc.

3.4.2 Description of ABL editor command language in

Backus-Naur form

This section describes the command language for the ABL editor in Backus-Naur form.

Notes :

1. The following three symbols belong to Backus-Naur formalism, and not of ABL editor.

::= denotes 'defined as'

[] denotes possible repetition of the enclosed symbol(s) zero or one more time.

! denotes "OR" condition

2. The following six symbols are of ABL editor command language :

\$ denotes termination of machine or program section

* denotes first character of the comment line

. denotes separator of cluster number and relative alternative number within cluster

/ denotes delimiter between predicate, action and next cluster sections

- denotes negative predicate condition

, denotes continuation of predicate or action section

<abstract program spec.> ::= <file name spec.>

 <machine spec.>

 <program spec.>

<file name spec.> ::= <alphabet> <alphanumeric> <ascii>

<alphabet> ::= A,...,Z

<alphanumeric> ::= A,...,Z,0,...,9

<ascii> ::= any ASCII character

<machine spec.> ::= [(subroutine)]

 <terminator>

<subroutine> ::= <subroutine name> [(comment)].

<subroutine name> ::= valid label in TI-980 assembly, up to
 6 characters

<comment> ::= <empty> ! <character string>

<empty> ::=

<character string> ::= any combination of ASCII, including blank
 terminated by carriage return

<terminator> ::= \$

<program spec.> ::= <program number>

 <number of clusters>

 <number of alternatives>

 <alternatives>

 <terminator>

<program number> ::= 0,...,99
 <number of clusters> ::= 1,...,99
 <number of alternatives> ::= 1,...,32
 <alternatives> ::= [<alternative>]
 <alternative> ::= [<alternative comment>]
 <alternative spec.>
 <alternative comment> ::= *(<comment>
 <alternative spec.> ::= <cluster> /<pred>/<action>/<next cluster>
 <cluster> ::= <cluster number>.<relative alternative number>
 <cluster number> ::= any number smaller or equal number of clusters
 <relative alt. number> ::= relative number of alternative within
 cluster, sequentially numbered
 <pred> ::= <empty> ! [<predicate description>]
 <pred description> ::= <sign> <pred #> [, <sign> <pred #>]
 <sign> ::= <empty> ! -
 <pred #> ::= 0,...,15
 <action> ::= <empty> ! [<action description>]
 <action description> ::= <action number> [, <action number>]
 <action number> ::= any number assigned to a subroutine by ABL
 editor during machine specification
 <next cluster> ::= any number smaller or equal to the number of
 clusters

3.4.3 Example

Figure 5 shows a session of ABL editor to create an abstract program. The user inputs are entered after the prompt '>' from the editor.

Figure 6 is the file in TI-980 assembly language format created by the ABL editor.

EH? 00:00:15 ABLEDI

ABL EDITOR

FILE NAME >V6#

MACHINE SPECIFICATION

ENTER SUBROUTINE NAMES

1 >INIT	1.INITIALIZATION
2 >RDCMD	2.READ COMMAND & SET PREDICATES
3 >A3	3.CHECK COMMAND SYNTAX
4 >GRTI1	4.READ GR7 INST. TO TI BUFFER 1
5 >A5	5.DISASSEMBLE GR7 INST. TO CONSOLE
6 >GRTI2	6.READ GR7 INST. TO TI BUFFER 2
7 >A6	7.DISASSEMBLE GR7 INST. TO L.P.
8 >MODD	8.MODIFY BUFFER
9 >WBTGR7	9.WRITE BUFFER TO GR7 MEMORY
10 >RLPCSP	10.RELEASE LP & CLOSE SPOOL FILE
11 >RQLOSP	11.REQUEST LP & OPEN SPOOL FILE
12 >RQCON	12.REQUEST CONSOLE FOR OUTPUT
13 >RLCON	13.RELEASE CONSOLE
14 >\$	

PROGRAM SPECIFICATION

PROGRAM NUMBER >1

NUMBER OF CLUSTERS >3

NUMBER OF ALTERNATIVES >6

ALTERNATIVE SPECIFICATION :

>*	- ALT 0 : INITIALIZATION
>1.1 //1/2	
>*	- ALT 1 : READ COMMAND & CHECK SYNTAX
>2.1 //2,3/3	
>*	- ALT 2 : 'L'
>3.1 /-0,-1,-2,3,-4/12,4,5,13/2	
>*	- ALT 3 : 'H'
>3.2 /-0,-1,2,-3,-4/4,6,11,7,10/2	
>*	- ALT 4 : 'M'
>3.3 /-0,-1,2,3,-4/8,5/2	
>*	- ALT 5 : 'W'
>3.4 /-0,1,-2,-3,-4/9/2	
>\$	

ABL PROGRAM CREATED IN FILE V6#32S

....S'LONG 00:11:46!

FIGURE 5. A session of ABL editor.

```

1 PROGNO DATA 1
2 PCLUST DATA #
3 PALT DATA #
4 PACT DATA #
5 PACTOF DATA #
6 ACLST# DATA #
7 SVECTR DATA #
8 CLSMA# DATA CLSMAT
9 NCLMA# DATA NCLMAT
10 YNPMA# DATA YNPMAT
11 MPRMA# DATA MPRMAT
12 APTR# DATA APTR#
13 BREG DATA #
14 LREG DATA #
15 *
16 CLSMAT BSS #
17 DATA >#0000
18 DATA >#0000
19 DATA >#4000
20 DATA >#0000
21 DATA >#3000
22 DATA >#0000
23 NCLMAT BSS #
24 DATA 2
25 DATA 3
26 DATA 2
27 DATA 2
28 DATA 2
29 DATA 2
30 YNPMAT BSS #
31 DATA >#0000
32 DATA >#0000
33 DATA >#1000
34 DATA >#2000
35 DATA >#3000
36 DATA >#4000
37 MPRMAT BSS #
38 DATA >#0000
39 DATA >#0000
40 DATA >#F000
41 DATA >#F000
42 DATA >#F000
43 DATA >#F000
44 APTR# BSS #
45 DATA ACLS#0
46 DATA ACLS#1
47 DATA ACLS#2
48 DATA ACLS#3
49 DATA ACLS#4
50 DATA ACLS#5
51 ACLS# BSS #
52 * - ALT # : INITIALIZATION
53 DATA INIT 1. INITIALIZATION
54 DATA #
55 *
56 ACLS#1 BSS #
57 * - ALT 1 : READ COMMAND & CHECK SYNTAX
58 DATA RDCMD 2. READ COMMAND & SET PREDICATES
59 DATA A3 3. CHECK COMMAND SYNTAX
60 DATA #
61 *

```

FIGURE 6. - An abstract program created by ABL editor.

```

62 ACLS#2 BSS #
63 * - ALT 2 : 'L'
64 DATA RQCON 12.REQUEST CONSOLE FOR OUTPUT
65 DATA GRTI1 4.READ GR7 INST. TO T1 BUFFER 1
66 DATA A5 5.DISASSEMBLE GR7 INST. TO CONSOLE
67 DATA RLCON 13.RELEASE CONSOLE
68 DATA #
69 *
70 ACLS#3 BSS #
71 * - ALT 3 : 'H'
72 DATA GRTI1 4.READ GR7 INST. TO T1 BUFFER 1
73 DATA GRTI2 6.READ GR7 INST. TO T1 BUFFER 2
74 DATA RQLOSP 11.REQUEST LP & OPEN SPOOL FILE
75 DATA A6 7.DISASSEMBLE GR7 INST. TO L.P.
76 DATA RLPCSP 10.RELEASE LP & CLOSE SPOOL FILE
77 DATA #
78 *
79 ACLS#4 BSS #
80 * - ALT 4 : 'M'
81 DATA MODD 8.MODIFY BUFFER
82 DATA A5 5.DISASSEMBLE GR7 INST. TO CONSOLE
83 DATA #
84 *
85 ACLS#5 BSS #
86 * - ALT 5 : 'W'
87 DATA WBTGR7 9.WRITE BUFFER TO GR7 MEMORY
88 DATA #
89 *
90 END

```

V6032S #2/03/84 13:03:14

FIGURE 6. (continued)

3.5 ABL INTERPRETER

At the heart of the ABL environment is the ABL interpreter. This interpreter runs abstract programs by comparing the predicates of the alternatives in the current cluster. It executes the actions of the alternative whose predicates match the state vector, and then goes to the next cluster to repeat the same process.

The predicates of the state vector are set or cleared in the action subroutines. The program flow is dependent on these action subroutines through the state vector predicates that they set or clear. The interpreter, acting like a state-driven driver, accepts the predicates and executes appropriate actions.

Hierarchical structure

Actions in abstract program can be a simple or a compound action, an abstract program. Therefore, an abstract program can call another abstract programs. The interpreter supports any number of abstract programs in this hierarchical structure. This is done by making use of the base relative addressing mode in TI-980 : the base register always contains the starting address of the abstract program that the interpreter is presently executing. When an action within a program calls another program, the starting address of the called program is passed to the interpreter. The interpreter responds to the call to execute a new program by saving the address of the current program, currently contained in the base register, into a pre-determined location in the called program, placing the address of the called program into the base register,

and then starts executing the called program. When execution is completed, the interpreter restores the previously saved address of the calling abstract program into the base register and resumes executing the calling program. This scheme allows the interpreter to execute any number of abstract programs in any level. Figure 7 illustrates this inter-program call feature.

Program tracing

The interpreter has the capability to trace the program flow of abstract programs. A program can be traced by its program number, alternative numbers within the program and the state vector at the end (or before) the alternative execution. This information completely determines the sequence of alternatives executed within each program as well as the sequence of program-to-program calls.

When tracing option is selected, the following information is output to the console :

- Program number to announce the beginning of the program execution.
- Alternative numbers and its resulting state vectors, in execution order.
- The completion of program execution by its number.

Detection of run-time unauthorized conditions

The interpreter also detects unauthorized conditions at run time. These conditions, arising when the interpreter cannot find a match between the state vector of the program and the predicates of any of the alternatives in the current cluster, will cause the

interpreter to stop executing. The interpreter will output the number of the last alternative executed and the final state vector. This information is useful for debugging.

Calling procedure

An abstract program is executed by calling the ABL interpreter with the M register containing its starting address.

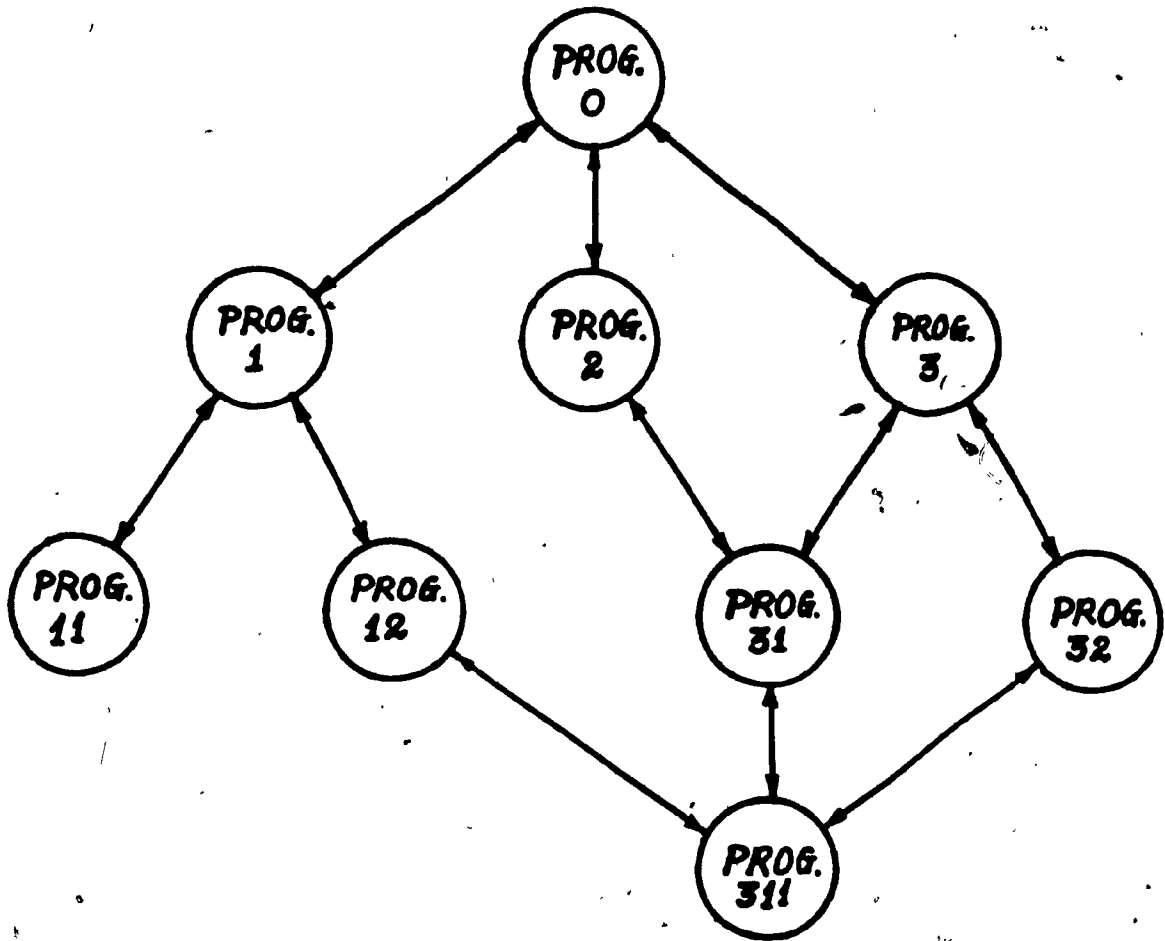


FIGURE 7. Hierarchical structure of abstract programs.

4. APPLICATION : A UTILITY FOR DEBUGGING OF REAL-TIME INTERACTIVE COMPUTER GRAPHICS

4.1 THE EXISTING ENVIRONMENT

HARDWARE :

A typical flight simulator system consists of the simulator itself and an instructor station. The instructor station consists of the facilities which provide the flight instructor means to control the simulator and to keep track of the pilot trainee's performance. This is done through various types of input/output devices such as keyboards, panel of special function buttons, graphic display units (DGU), hard copy units, etc. Of these devices, the DGU is the most important with its capability to provide information in different forms :

- alpha-numeric pages
- real-time interactive maps
- real-time interactive plottings...

In the early generation of flight simulators, the DGU had its display file stored in host-resident memory. Update of display file, which is generated by the host computer, is just a matter of updating host memory by host program. The debugging of the display file is done by examining and/or modifying the memory portion affected. Utilities for this type of debugging are simple and available.

With the evolution of the micro-processors, most of today's graphic display systems have its own processor(s) and its own

memory. In the current generation of flight simulators, one of the most widely used graphic systems is GRAPHIC-7 and GRAPHIC-8 series (Calcom, Sanders Associates Inc.).

GRAPHIC-7/8 is intelligent graphic system which has :

- Two separate 16-bit processors : a graphic processor and a graphic controller. The graphic processor, which emulates PDP-11 instructions, is used to handle the communication between the host and the GRAPHIC-7/8 unit, the input from position entry devices such as track ball, joystick, photopen, etc. The graphic controller is used to generate display images to the monitor from display file.

- Its own memory of 32K 16-bit words, expandable to 128K words. The graphic processor and the graphic controller share the same memory; each uses a different area.

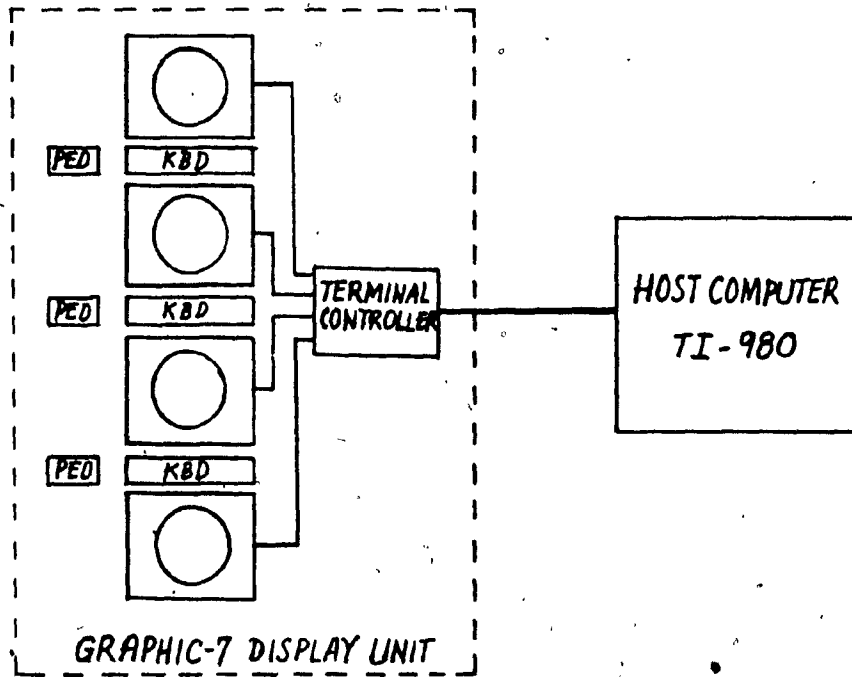
- GRAPHIC-7 is a vector display whereas GRAPHIC-8 is a pixel-scanned display.

- GRAPHIC-7 graphic instruction set is a subset of GRAPHIC-8 (upward compatibility).

Figure 8 shows a typical configuration between GRAPHIC-7/8 and its TI-980 host computer.

SOFTWARE :

For applications such as flight simulator, GRAPHIC-7/8 is used to display alpha-numeric pages, real-time interactive maps and plottings. Display files, containing GRAPHIC-7/8 instructions, are generated by the programs in the host computer and then sent to



KBD : KEYBOARD
 PED : TRACBALL - FORCESTICK
 DATA TABLET

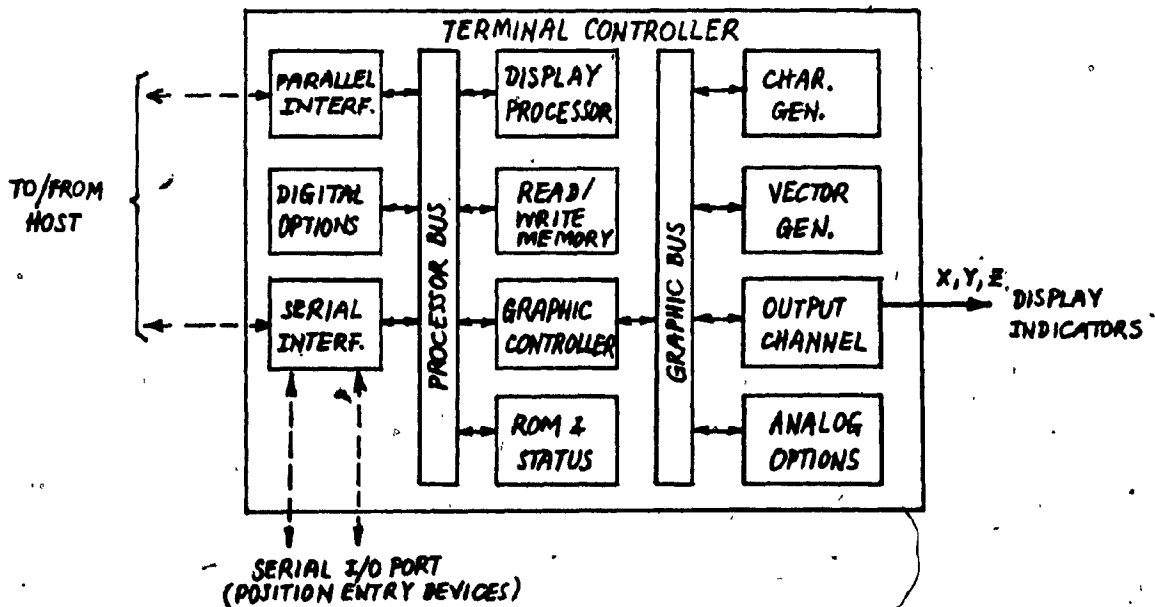


FIGURE 8. GRAPHIC-7/8 display unit and its TI-980 host computer.

GRAPHIC-7/8 memory which is used by the graphic controller to draw images on the monitor. Updating display files is done in the same way. The size of a typical display file is normally about 32K words.

Debugging is done through a special keyboard supplied by Sanders Inc.. This keyboard lets the user examine and modify any location in GRAPHIC-7/8 memory, one word at a time. The addresses and contents are specified or displayed in octal numbers. Image generation can be restarted from any location in display file.

Under this debugging facility, the user has to perform the following steps in order to examine and/or modify refresh display file :

- typing in the address (octal) of the location to be examined
- when the content is displayed, decode it from machine code to mnemonic code
- typing in the new content, if desired
- repeating the same process for every location to be examined and/or modified.

4.2 THE NEW DEBUGGING FACILITY

The above debugging facilities are clearly very time-consuming. In addition, TI-980 programs run on hexadecimal numbers whereas GRAPHIC-7/8 runs on octal numbers. The programmer, when debugging his/her software, must constantly perform conversion between the two number systems. This places another burden on an already inadequate debugging method.

A new software debugging facility is naturally desired. The Foreground Utility Program for Graphics (FUP/G) is to provide the programmers with the facility to debug display file on-line :

- FUP/G lets the user examine any portion of display file :
 - * the size of the portion to be examined varies from 1 to 76 words.
 - * two formats are supported when specifying the portion to be examined : in the first format, the user specifies the starting address and the number of bytes. In the second format, the user specifies the starting and ending address.
- The content of the portion of the display file is displayed in GRAPHIC-7/8 mnemonic form as well as in machine code in octal and hexadecimal representation. The addresses of the locations are displayed in decimal, octal and hexadecimal numbers.
- The user can modify any location(s) in the examined portion of display file.
- Debugging activities are carried out on-line : the image of modified display file is immediately generated on the monitor.

Command language :

At present, the utility FUP/G supports the following commands:

L : List the content of the specified portion of display file to console. This command reads the desired area from GRAPHIC-7/8 memory to a buffer in TI-980. The content is displayed in

machine code (octal and hexadecimal) and in mnemonic form.

Format :

L (start address) TO (end address)

or

L (start address) (number of bytes)

H : Give a Hard copy of display file to line printer. This command prints the content of the specified portion in machine and mnemonic form to line printer.

Format :

H (start address) TO (end address)

or

H (start address) (number of bytes)

M : Modify the content of display file previously brought to TI-980 buffer by 'L' command. More than one location can be modified by a single 'M' command.

Format :

M (start address) (new content) [(new content)]

W : Write the modified content of display file from TI-980 buffer to GRAPHIC-7/8 memory, i.e. update the display file to generate a new image on the monitor.

Format :

W

CR : Carriage return, to repeat the previous command

Format :

(carriage return key)

Q : Terminate utility.

Format :

Q

Future expansion and requirement on program structure

The above commands of the utility FUP/G satisfy the immediate debugging need. To provide more debugging power, more commands will gradually be developed and implemented in future. Therefore, it is required that the utility FUP/G be written in such a structure that the future expansion in the command language can be implemented easily.

4.3 DESCRIPTION OF IMPLEMENTATION OF THE UTILITY

USING THE ABL ENVIRONMENT

The utility FUP/G is designed, structured and created under the ABL environment. The ABL structure, with its highly modular, top-down structured characteristics, gives the utility the flexibility needed to satisfy the future expansion requirements.

The ABL editor was used to create abstract programs. The text editor was then used to create abstract machines, i.e. subroutines. These modules were linked to the ABL interpreter to create the load module which was named 'FUP/G' in the utility library.

The utility FUP/G presently consists of three programs :

- Main program (module V1#)
- Syntax check (module V3#), to perform syntax check on all commands.

- Disassembler (module V4#), to disassemble GRAPHIC-7/8 graphic instructions into mnemonic form. This module is used by 'L' and 'H' commands. Module V9# contains a number of action subroutines for the disassembler program.

In addition, modules VD# and MM# contain all common subroutines such as conversions, formatting shared by all action subroutines.

Appendix 1 shows a typical session of the utility FUP/G and the result.

5. CONCLUSION

During the design and implementation of the utility FUP/G, it was observed that the ABL approach gave a remarkable improvement on productivity over the conventional approach during the design, coding and testing stages. On the other hand, program space and execution time are two main advantages of assembly language programming. Therefore, let us examine the effects of the implemented ABL environment on these two factors :

- SPACE EFFICIENCY : instructions to control program flow such as 'test and skip if' each takes one or two words (16 bits/word). In ABL, each predicate takes only two bits. In a number of typical real-time assembly programs surveyed, it was found that the flow control instructions accounted for about ten percent of program size. The reduction of memory size by replacing flow control instructions by ABL binary predicates is significant.

- TIME EFFICIENCY : although there is an overhead during run time when the interpreter evaluates the predicates to select an alternative to execute, this overhead is negligible due to the fact that only the predicates of the alternatives in the current cluster are evaluated and only one instruction is used to compare the state vector to the predicates of each alternative. Time-wise, execution in ABL is as good as in conventional assembly programming.

The implemented ABL environment is a minimum configuration. Other features of ABL are found very useful which, when implemented,

should significantly enhance its power. Considered most important are :

- The ability of the interpreter to change program flow during run time by temporarily stopping execution when certain conditions in the state vector are met, changing the predicates of the state vector and/or of alternative(s) and next-cluster vector, and then resuming execution. This would be a very useful, dynamic debugging tool which effectively lets the user try different abstract programs on the same machine.

- The ability of the ABL editor to generate the matrix form (as in Figure 1) of the program created by the user.

The ABL concept and techniques are found very suited for assembly language programming. Although this project was implemented on TI-980 mini-computer, the techniques and design are basically transferrable to other assembly languages.

The implemented ABL environment offers a highly systematic approach and a great amount of flexibility in design and programming and at the same time it enhances and retains space and time efficiency respectively, which constitute the two most important advantages of assembly language programming. Its suitability and therefore potential use as tool for building software in assembly language is convincing and clear.

BIBLIOGRAPHY

- ACMS81 ACM SIGSOFT - 'NBS programming environment workshop report'
Software engineering notes, Vol 6 No. 4, August 1981.
- ANAS79 M.S. Anastas & R.F. Vaughau - 'Direct architectural implementation of a requirement-oriented computing structure',
Microprogramming workshop, AMC Sigmicro newsletter, 1979.
- FREE75 P. Freeman - 'Software system principles - A survey',
Science Research Associates Inc., 1975.
- JAWO81 W.M. Jaworski & H. Hinterger - 'Controlled program design
by use of the programming concept ABL', Applied Informatics,
July 1981.
- KRON82 M.N. Kronick - 'An ABL software environment for a mini-
computer', thesis for the degree of master of computer
science, Concordia University, March 1982.
- LINA82 J. Linares - 'Design of a comprehensive micro-programming
system', thesis for the degree of master of computer science,
Concordia University, July 1982.
- MITC79 J.G. Mitchell - 'The design and construction of flexible
and efficient interactive programming system', Garland
Publishing, New York 1979.
- MYER80 G.J. Myers - 'The art of software testing' - John Wiley &
Sons, Toronto 1980.
- NEWM79 W.M. Newman & R.F. Sproull - 'Principles of interactive

computer graphics', McGraw-Hill 1979.

SAND78 Sanders Ass. Inc., 'Graphic-7, computer graphic display system, technical description', Sanders Ass. Inc., New Hampshire, 1978.

SAND81 Sanders Ass. Inc., 'Graphic-8, computer graphic display system, series 8X00, programmer's reference manual', Sanders Ass. Inc., New Hampshire, 1981.

SCHN79 N.F. Schneidewind & H.M. Hoffman - 'An experiment in software data collection and analysis', IEEE transaction on software engineering, May 1979.

SHNE80 B. Shneiderman - 'Software psychology'- Human factors in computer and information systems', Winthrop Publishers Inc., 1980.

SHO083 M.L. Shooman - 'Software engineering - Design, Reliability, Management', McGraw-Hill 1983.

APPENDIX 1

A1.1 A typical session of utility FUP/G on a display file which contains GRAPHIC-7 instruction set, stored from location 3000 to 3072 (hex) in GRAPHIC-7 memory.

A1.2 Hard copy print-outs on line printer as result of 'H' commands in FUP/G session.

EH? 17:16:49 FUP/G

FUP/G V8

*FUP/G: L 3000 J4

<u>DEC</u> <u>ADDR</u>	<u>HEX</u> <u>ADDR</u>	<u>OCT</u> <u>ADDR</u>	<u>HEX</u> <u>VALUE</u>	<u>OCT</u> <u>VALUE</u>	
12288	3000	030000	0000	000000	HALT
12290	3002	030002	0200	001000	JUMP 1000
12292	3004	030004	1000	010000	
12294	3006	030006	0200	001000	*JUMP 1000
12296	3008	030010	9000	110000	
12298	300A	030012	0240	001100	JRMP 100
12300	300C	030014	0100	000400	
12302	300E	030016	0280	001200	JMPZ 2000
12304	3010	030020	2000	020000	
12306	3012	030022	0280	001200	*JMPZ 2000
12308	3014	030024	A000	120000	
12310	3016	030026	02C0	001300	JPRZ 100
12312	3018	030030	0100	000400	
12314	301A	030032	0400	002000	JMPM 1500
12316	301C	030034	1500	012400	
12318	301E	030036	0440	002100	CALL 9000
12320	3020	030040	9000	110000	
12322	3022	030042	0480	002200	CALR 1000
12324	3024	030044	1000	010000	
12326	3026	030046	04C0	002300	RTRN
12328	3028	030050	0600	003000	IZPR
12330	302A	030052	0800	004000	*LINK 0100
12332	302C	030054	8100	100400	
12334	302E	030056	0840	004100	SAVD 0
12336	3030	030060	0880	004200	RESD 0
12338	3032	030062	08C0	004300	ADDI 0,-100
12340	3034	030064	FF00	177400	
12342	3036	030066	0A00	005000	JMPR 0
12344	3038	030070	0C00	006000	LDRI 0,0,00FF
12346	303A	030072	00FF	000377	
12348	303C	030074	0C40	006100	LDDI 0,0500
12350	303E	030076	0500	002400	
12352	3040	030100	0C80	006200	LDSP 4000
12354	3042	030102	4000	040000	
12356	3044	030104	0E00	007000	WATE
12358	3046	030106	1000	010000	LDDZ 0
12360	3048	030110	1800	014000	LDDP 0
12362	304A	030112	2000	020000	LDXA 0
12364	304C	030114	2800	024000	LDXR 0
12366	304E	030116	3000	030000	DRYA 0
12368	3050	030120	3800	034000	DRXR 0
12370	3052	030122	4000	040000	DRYA 0
12372	3054	030124	4800	044000	DRYR 0
12374	3056	030126	5000	050000	MVXA 0

12376	3058	030130	5800	054000	MVXR	0
12378	305A	030132	6000	060000	MVYA	0
12380	305C	030134	6800	064000	MVYR	0
12382	305E	030136	7000	070000	LDKX	0
12384	3060	030140	7800	074000	DRKY	0
12386	3062	030142	8000	100000	DRSR	0,0
12388	3064	030144	8040	100100	MVSR	0,0
12390	3066	030146	C000	140000	PPLR	0,0
12392	3068	030150	C040	140100	LDTI	0
12394	306A	030152	9FC1	117701	CHAR	0
12396	306C	030154	C3C2	141702	TEXT	CB
12398	306E	030156	0E40	007100	INIT	
12400	3070	030160	0DC0	006700	CLRM	
12402	3072	030162	0E80	007200	MODE	0

*FUP/G: H 3000 TO 3072

*FUP/G: M 3008 1500

*FUP/G: M 302E 842 881 08C3 100

*FUP/G: W

*FUP/G: H 3000 74

*FUP/G: L 3000 TO 3072

12288	3000	030000	0000	000000	HALT	
12290	3002	030002	0200	001000	JUMP	1000
12292	3004	030004	1000	010000		
12294	3006	030006	0200	001000	JUMP	1000
12296	3008	030010	9000	110000		
12298	300A	030012	0240	001100	JRMP	100
12300	300C	030014	0100	000400		
12302	300E	030016	0280	001200	JMPZ	2000
12304	3010	030020	2000	020000		
12306	3012	030022	0280	001200	*JMPZ	2000
12308	3014	030024	A000	120000		
12310	3016	030026	02C0	001300	JPRZ	100
12312	3018	030030	0100	000400		
12314	301A	030032	0400	002000	JMPM	1500
12316	301C	030034	1500	012400		
12318	301E	030036	0440	002100	CALL	9000
12320	3020	030040	9000	110000		
12322	3022	030042	0480	002200	CALR	1000
12324	3024	030044	1000	010000		
12326	3026	030046	04C0	002300	RTRN	
12328	3028	030050	0600	003000	IZPR	
12330	302A	030052	0800	004000	*LINK	0100
12332	302C	030054	8100	100400		
12334	302E	030056	0840	004100	SAVD	2
12336	3030	030060	0880	004200	RESD	1
12338	3032	030062	08C0	004300	ADDI	3,100
12340	3034	030064	FF00	177400		

12342	3036	030066	0A00	005000	JMPR	.0
12344	3038	030070	0C00	006000	LDRI	0,0,00FF
12346	303A	030072	00FF	000377		
12348	303C	030074	0C40	006100	LDDI	0,0500
12350	303E	030076	0500	002400		
12352	3040	030100	0C80	006200	LDSP	4000
12354	3042	030102	4000	040000		
12356	3044	030104	0E00	007000	WATE	
12358	3046	030106	1000	010000	LDDZ	0
12360	3048	030110	1800	014000	LDDP	0
12362	304A	030112	2000	020000	LDXA	0
12364	304C	030114	2800	024000	LDXR	0
12366	304E	030116	3000	030000	DRXA	0
12368	3050	030120	3800	034000	DRXR	0
12370	3052	030122	4000	040000	DRYA	0
12372	3054	030124	4800	044000	DRYR	0
12374	3056	030126	5000	050000	MVXA	0
12376	3058	030130	5800	054000	MVXR	0
12378	305A	030132	6000	060000	MVYA	0
12380	305C	030134	6800	064000	MVYR	0
12382	305E	030136	7000	070000	LDKX	0
12384	3060	030140	7800	074000	DRKY	0
12386	3062	030142	8000	100000	DRSR	0,0
12388	3064	030144	8040	100100	MVSR	0,0
12390	3066	030146	C000	140000	PFLR	0,0
12392	3068	030150	C040	140100	LDTI	0
12394	306A	030152	9FC1	117701	CHAR	0
12396	306C	030154	C3C2	141702	TEXT	CB
12398	306E	030156	0E40	007100	INIT	
12400	3070	030160	0DC0	006700	CLRM	
12402	3072	030162	0E80	007200	MODE	0

*FUP/G: Q

FUP/G TERMINATES
S'LONG 17:27:21!

FUP/G	DEC ADDR	HEX ADDR	OCT ADDR	HEX VALUE	OCT VALUE	
	12288	3000	030000	0000	000000	HALT
	12290	3002	030002	0200	001000	JUMP 1000
	12292	3004	030004	1000	010000	
	12294	3006	030006	0200	001000	*JUMP 1000
	12296	3008	030010	9000	110000	
	12298	300A	030012	0240	001100	JRMP 100
	12300	300C	030014	0100	000400	
	12302	300E	030016	0280	001200	JMPZ 2000
	12304	3010	030020	2000	000000	
	12306	3012	030022	0280	001200	*JMPZ 2000
	12308	3014	030024	A000	120000	
	12310	3016	030026	02C0	001300	JPRZ 100
	12312	3018	030030	0100	000400	
	12314	301A	030032	0400	002000	JMPM 1500
	12316	301C	030034	1500	012400	
	12318	301E	030036	0440	002100	CALL 9000
	12320	3020	030040	9000	110000	
	12322	3022	030042	0480	002200	CALR 1000
	12324	3024	030044	1000	010000	
	12326	3026	030046	04C0	002300	RTRN
	12328	3028	030050	0600	003000	IZPR
	12330	302A	030052	0800	004000	*LINK 0100
	12332	302C	030054	0100	100400	
	12334	302E	030056	0840	004100	SAVD 0
	12336	3030	030060	0880	004200	RES0 0
	12338	3032	030062	08C0	004300	ADDI 0,-100
	12340	3034	030064	FF00	177400	
	12342	3036	030066	0A00	005000	JMPR 0
	12344	3038	030070	0C00	006000	LDRI 0,0,00FF
	12346	303A	030072	00FF	000377	
	12348	303C	030074	0C40	006100	LDDI 0,0500
	12350	303E	030076	0500	002400	
	12352	3040	030100	0C80	006200	LDSP 4000
	12354	3042	030102	4000	040000	
	12356	3044	030104	0E00	007000	WATE
	12358	3046	030106	1000	010000	LDDZ 0
	12360	3048	030110	1800	014000	LDDP 0

FUP/G	DEC ADDR	HEX ADDR	OCT ADDR	HEX VALUE	OCT VALUE	
	12362	304A	030112	2000	020000	LDXA 0
	12364	304C	030114	2800	024000	LDXR 0
	12366	304E	030116	3000	030000	DRXA 0
	12368	3050	030120	3800	034000	DRXR 0
	12370	3052	030122	4000	040000	DRYA 0
	12372	3054	030124	4800	044000	DRYR 0
	12374	3056	030126	5000	050000	MVXA 0
	12376	3058	030130	5800	054000	MVXR 0
	12378	305A	030132	6000	060000	MVYA 0
	12380	305C	030134	6800	064000	MVYR 0
	12382	305E	030136	7000	070000	LDKX 0
	12384	3060	030140	7800	074000	DRKY 0
	12386	3062	030142	8000	100000	DRSR 0,0
	12388	3064	030144	8040	100100	MVSR 0,0
	12390	3066	030146	C000	140000	PPLR 0,0
	12392	3068	030150	C040	140100	LDTI 0
	12394	306A	030152	9FC1	117701	CHAR A
	12396	306C	030154	C2C3	141303	TEXT BC
	12398	306E	030156	0E40	007100	INIT
	12400	3070	030160	0DC0	006700	CLRM
	12402	3072	030162	0E80	007200	MODE 0

FUP/G	DEC	HEX	OCT	HEX	OCT	
	ADDR	ADDR	ADDR	VALUE	VALUE	
	12288	3000	030000	0000	000000	HALT
	12290	3002	030002	0200	001000	JUMP 1000
	12292	3004	030004	1000	010000	
	12294	3006	030006	0200	001000	JUMP 1500
	12296	3008	030008	1500	012400	
	12298	300A	03000A	0240	001100	JRMP 100
	12300	300C	03000C	0100	000400	
	12302	300E	03000E	0200	001200	JMPZ 2000
	12304	3010	030010	2000	020000	
	12306	3012	030012	0200	001200	*JMPZ 2000
	12308	3014	030014	A000	120000	
	12310	3016	030016	02C0	001300	JPRZ 100
	12312	3018	030018	0100	000400	
	12314	301A	03001A	0400	002000	JMPM 1500
	12316	301C	03001C	1500	012400	
	12318	301E	03001E	0440	002100	CALL 9000
	12320	3020	030020	9000	110000	
	12322	3022	030022	0400	002200	CALR 1000
	12324	3024	030024	1000	010000	
	12326	3026	030026	04C0	002300	RTRN
	12328	3028	030028	0600	003000	IZPR
	12330	302A	03002A	0800	004000	*LINK 0100
	12332	302C	03002C	0100	100400	
	12334	302E	03002E	0842	004102	SAVD 2
	12336	3030	030030	0801	004201	RESD 1
	12338	3032	030032	08C3	004303	ADDI 3,100
	12340	3034	030034	0100	000400	
	12342	3036	030036	0A00	005000	JMPR 0
	12344	3038	030038	0C00	006000	LORI 0,0,00FF
	12346	303A	03003A	00FF	000377	
	12348	303C	03003C	0C40	006100	LDDI 0,0500
	12350	303E	03003E	0500	002400	
	12352	3040	030040	0C00	006200	LDSP 4000
	12354	3042	030042	4000	040000	
	12356	3044	030044	0E00	007000	WATE
	12358	3046	030046	1000	010000	LDDZ 0
	12360	3048	030048	1000	014000	LDDP 0

DEC	HEX	OCT	HEX	OCT	
ADDR	ADDR	ADDR	VALUE	VALUE	
12362	304A	03004A	2000	020000	LDXA 0
12364	304C	03004C	2000	024000	LDXR 0
12366	304E	03004E	3000	030000	DRXA 0
12368	3050	030050	3000	034000	DRXR 0
12370	3052	030052	4000	040000	DRYA 0
12372	3054	030054	4000	044000	DRYR 0
12374	3056	030056	5000	050000	MVXA 0
12376	3058	030058	5000	054000	MVXR 0
12378	305A	03005A	6000	060000	MVYA 0
12380	305C	03005C	6000	064000	MVYR 0
12382	305E	03005E	7000	070000	LDKX 0
12384	3060	030060	7000	074000	DRKY 0
12386	3062	030062	8000	100000	DRSR 0,0
12388	3064	030064	8040	100100	MVSR 0,0
12390	3066	030066	C000	140000	PPLR 0,0
12392	3068	030068	C040	140100	LDTI 0
12394	306A	03006A	9FC1	117701	CHAR A
12396	306C	03006C	C3C2	141702	TEXT CB
12398	306E	03006E	0E40	007100	INIT
12400	3070	030070	0DC0	006700	CLRM
12402	3072	030072	0E00	007200	MODE 0

FUP/G 09/03/84 17:21:39

APPENDIX 2

A2.1 Listings of ABL editor :

- Module V7# (ABL editor)
- Module V8# (common subroutines)

A2.2 Listing of ABL interpreter :

- Module T4#

V70085 19/03/84 16:07:33

980A OFF LINE ASSEMBLER

SHEET 1

ABL EDITOR
TI 980A
K. VO
REVISION #
IDT V70

THE ABL EDITOR ACCEPTS SPECIFICATION FOR ABSTRACT PROGRAM (SEE SHEET 3) AND CREATES A CORRESPONDING SOURCE FILE IN TI-980 ASSEMBLY LANGUAGE WHICH CAN BE RUN BY THE ABL INTERPRETOR (MODULE T40).

LUN ASSIGNMENT :

LUN # : CONSOLE
1 : SCRATCH FILE TO STORE ACTIONSO F ABL MACHINES
3 : SCRATCH FILE TO TEMPORARILY STORE PROGRAM
4 : SCRATCH FILE TO WHICH LUN 3 WILL BE COPIED
AT THE END OF SUCCESSFUL ABL PROGRAM CREATION

REF RFC, VTC, GETCHA, DEC281, ABLENT, B12HAS, HDER0, ACLS00
REF M12, ACLBSS, B12DAS, M13, HLABL1
DEF IPLNX, IPLNS, IPLN, OPBUF

###	A	EQU #
###1	E	EQU 1
###2	X	EQU 2
###3	H	EQU 3
###4	S	EQU 4
###5	L	EQU 5
###6	B	EQU 6
###7	P	EQU 7
###1	BR	EQU 1
###3	XB	EQU 3
CAES	SVC	OPD >CAES.3
8ABD	LFCR	EQU >8ABD
ASBD	BBCR	EQU >ASBD
4888	EMPTY	EQU >4888
###1	LU1	EQU 1
###3	LU3	EQU 3
###4	LU4	EQU 4

988A OFF LINE ASSEMBLER

SHEET 2

PEJ

0000 1000
P 0001, 0005
0002 7000
E 0003 0000
0004 CAE5

0LDM -PROGNO

M REG. CONTAINS ADDRESS OF PROGRAM

0BR1 ABLENT

CALL ABL INTERP. TO EXECUTE ABL EDITOR PROGRAM

SVC 5

TERMINATE ABL EDITOR UTILITY


```

SPECIFICATION FOR ABL EDITOR :
ALB EDITOR
ENTER FILE NAME : MMH
ENTER MACHINE :
1 >INIT      INITIALIZATION
2 >D2BCON   DEC-BIN CONVERSION
3 >
N >S
PROGRAM NUMBER >
ENTER NUMBER OF CLUSTERS : 2
ENTER NUMBER OF ALTERNATIVES : 1#
ENTER PROGRAM :
>*          INITIALIZATION & CLASSIFICATION
>1.1 //1/2
>*          L,H - FORMAT 1
>2.1 /# ,3,-4/2,3,2,4/#
. . .
>S

```

```

55 *
56 *
57 *
58 *
59 *
6# *
61 *
62 *
63 *
64 *
65 *
66 *
67 *
68 *
69 *
7# *
71 *
72 *
73 *
74 *
75 *
76 *
77 *
78 *
79 *
8# *
81 *
82 *

```

988A OFF LINE ASSEMBLER

SHEET 4

LINE	PEJ	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	DESCRIPTION
83																					EDITOR INITIALIZATION
84																					READ ACTION I/P LINE
85																					PROCESS
86																					INIT. FOR ALT. I/P LINE
87																					ANALYZE ALT. INPUT LINE
88																					PROCESS CLUSTER SPEC.
89																					PROCESS PRED. SPEC.
90																					PROCESS ACTION SPEC.
91																					PROCESS NXT CLTR
92																					V CLOSING
93																					
94																					
95																					
96																					
97																					
98																					
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139																					

982A OFF LINE ASSEMBLER
140 *
141 *
1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 SHEET 5

SBWA OFF LINE ASSEMBLER

SHEET 6

142	PEJ	
143		
144		
145		ABL EDITOR INITIALIZATION
146		READ MACHINE I/P LINE
147		ACTION I/P LINE (SUBROUTINE NAME)
148		MACHINE TERMINATION - ABL PROGRAM INIT'N
149		READ & ANALYZE I/P LINE FOR ALTER. SPEC.
150		COMMENT I/P LINE
151		PROGRAM I/P TERMINATION
152		ALTER. SPEC. I/P LINE
153		PROCESS CLUSTER SPECIFICATION
154		INVALID I/P CHARACTER
155		POSITIVE PREDICATE
156		NEGATIVE PREDICATE
157		POSITIVE PREDICATE, LAST PREDICATE
158		NEGATIVE
159		NO SPECIFICATION (CPTR=LAPTR)
160		ACTION NUMBER
161		LAST ACTION
162		NO SPECIFICATION (CPTR=LAPTR)
163		PROCESS NEXT CLUSTER
164		CLOSING

PEJ

ABL PROGRAM SECTION

```

0005 0003      PROGN0 DATA 3
0006 0000      PCLUST DATA #
0007 0000      PALT  DATA #
0008 0000      PACT  DATA #
0009 0000      PACTOF DATA #
000A 0000      ACLST0 DATA #
000B 0000      SVECTR DATA #
000C 0013      CLSM#0 DATA CLSMAT
000D 0027      NCLM#0 DATA NCLMAT
000E 0038      YNPM#0 DATA YNPMAT
000F 004F      MPRM#0 DATA MPRMAT
0010 0063      APTR#0 DATA APTRS
0011 0000      BREG  DATA #
0012 0000      LREC  DATA #
0013 0000      CLSMAT DATA >0000,0 C1
0014 0000      DATA >4000,0 C2
0015 0000      DATA >3000,0 C3
0016 0000      DATA >0000,0 C4
0017 0000      DATA >0700,0 C5
0018 0000      DATA >00C0,0 C6
0019 0000      DATA >007E,0 C7
001A 0000      DATA >0041,>0000 C8
001B 0000      DATA >0040,>2000 C9
001C 0000      DATA >0000,>1000 C10
001D 0000      DATA >0000,>1000 C10
001E 007E
001F 0000
0020 0041
0021 0000
0022 0000
0023 0040
0024 2000
0025 0000
0026 1000

0027 0002      NCLMAT DATA 2
0028 0003      DATA 3
0029 0002      DATA 2
002A 0004      DATA 4
002B 0005      DATA 5
002C 0004      DATA 4

```

```

PROGRAM NO.
PRESENT CLUSTER
  . ALT
  . ACTION
  . ACTION OFFSET IN ALTERNATIVE
PTR TO ACTION LIST FORALT BEING EXE'D

STATE VECTOR

POINTER TO CLUSTER MATRIX
NEXT-CLUSTER VECTOR
PREDICATE MATRIX
PRED MASK MATRIX
ACTION LIST POINTERS

```

```

(B)-ADDRESS OF PREVIOUS ABSTR. PROGRAM
(L)-RETURN ADDRESS

```

900A OFF LINE ASSEMBLER

002D	000A	212	DATA 15	6
002E	0006	213	DATA 6	7
002F	0007	214	DATA 7	8
0030	0004	215	DATA 4	9
0031	0007	216	DATA 7	10
0032	0007	217	DATA 7	11
0033	0008	218	DATA 8	12
0034	0008	219	DATA 8	13
0035	0008	220	DATA 8	14
0036	0008	221	DATA 8	15
0037	0009	222	DATA 9	16
0038	0009	223	DATA 9	17
0039	0004	224	DATA 4	18
003A	0008	225	DATA 8	19

Y/N PREDICATE MATRIX

003B	0000	228	Y/NP/MAT	ALT #
003C	0000	229	DATA >0000	1
003D	0000	230	DATA >0000	2
003E	0000	231	DATA >0000	3
003F	0000	232	DATA >4000	4
0040	0000	233	DATA >0000	5
0041	0000	234	DATA >0000	6
0042	0000	235	DATA >4000	7
0043	0000	236	DATA >0000	8
0044	0200	237	DATA >0000	9
0045	2000	238	DATA >0200	10
0046	3000	239	DATA >3000	11
0047	0000	240	DATA >0000	12
0048	1000	241	DATA >1000	13
0049	0400	242	DATA >0400	14
004A	2000	243	DATA >2000	15
004B	0000	244	DATA >0000	16
004C	0400	245	DATA >0400	17
004D	0000	246	DATA >0000	18
004E	0000	247	DATA >0000	19

MASK FOR PREDICATE MATRIX

004F	0000	248	Y/NP/MAT	ALT #
0050	0000	249	DATA >0000	1
0051	0000	250	DATA >0000	2
0052	0000	251	DATA >4000	3
0053	0200	252	DATA >0200	4
0054	C200	253	DATA >C200	5
0055	C200	254	DATA >C200	6
0056	C000	255	DATA >C000	7
0057	0200	256	DATA >0200	8
0058	0200	257	DATA >0200	9
0059	3E00	258	DATA >3E00	10
005A	3E00	259	DATA >3E00	11
005B	3E00	260	DATA >3E00	12
005C	3E00	261	DATA >3E00	13
005D	0600	262	DATA >0600	14
005E	2C00	263	DATA >2C00	15
005F	2C00	264	DATA >2C00	16

986A OFF LINE ASSEMBLER
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 324
 325

ACTION MATRIX

P 0063 0077
 P 0064 007A
 P 0065 007D
 P 0066 007F
 P 0067 0082
 P 0068 0088
 P 0069 008A
 P 006A 008B
 P 006B 008C
 P 006C 0092
 P 006D 0095
 P 006E 0098
 P 006F 00A1
 P 0070 00A7
 P 0071 00AD
 P 0072 00B0
 P 0073 00B5
 P 0074 00B8
 P 0075 00BE
 P 0076 00C2
 0077
 P 0077 0133
 P 0078 0157
 0079
 007A
 P 007A 018D
 P 007B 027C
 007C
 007D
 P 007D 01A8
 007E
 007F
 P 007F 0186
 P 0080 0206
 0081
 0082
 P 0082 01F4
 P 0083 01ED
 P 0084 01F7
 P 0085 027C
 P 0086 040A
 P 0087 0000
 0088
 P 0088 020D
 0089

DATA >0400
 DATA >0200
 DATA >0000

DATA ACLST0
 DATA ACLST1
 DATA ACLST2
 DATA ACLST3
 DATA ACLST4
 DATA ACLST5
 DATA ACLST6
 DATA ACLST7
 DATA ACLST8
 DATA ACLST9
 DATA ACLT10
 DATA ACLT11
 DATA ACLT12
 DATA ACLT13
 DATA ACLT14
 DATA ACLT15
 DATA ACLT16
 DATA ACLT17
 DATA ACLT18
 DATA ACLT19

INITIALIZATION
 ABL EDITOR INIT'N
 GET FILE NAME & DO A/O

READ MACHINE I/P LINE
 READ ACTION I/P LINE
 ANALYZE FIRST CHARACTER

ACTION I/P LINE (SUB NAME)
 STORE ACTION INTO TEMP. FILE

MACHINE TERMINATION
 READ INFO. ON PROGRAM SIZE
 INIT'N FOR PROGRAM CREATION

READ & ANALYZE I/P LINE FOR ALT SPEC
 CLEAR STATE VECTOR
 RESET CPTR, LAPTR
 READ ALT I/P LINE
 ANALYZE 1ST CHAR
 SAVE POINTERS

WRITE COMMENT LINE
 WRITE COMMENT LINE

BSS #
 DATA INIT
 DATA FNAME
 DATA #
 BSS #
 DATA RAIL
 DATA AFC
 DATA #
 BSS #
 DATA STACT
 DATA #
 BSS #
 DATA RPI
 DATA PINIT
 DATA #
 BSS #
 DATA CLRSV
 DATA CLPTRS
 DATA RIL
 DATA AFC
 DATA SAVEP
 DATA #
 BSS #
 DATA VCL
 DATA #

325	ACLST6	BSS	#	PROGRAM I/P TERMINATION
327	ACLST6	DATA	#	
328				
329				
330	ACLST7	BSS	#	ALT SPEC I/P LINE
331	ACLST7	DATA	#	
332				
333	ACLST8	BSS	#	PROCESS CLUSTER SPEC
334	ACLST8	DATA	#	RESET POINTERS TO CLUSTER, POS.
335				CONVERT TO BINARY
336				SAVE CLUSTER
337				POINT CPTR AFTER LAPTR
338				POINT CPTR AFTER LAPTR
339				POINT TO NEXT SEP. & CLASSIFY
340				
341	ACLST9	BSS	#	INVALID I/P CHARACTER
342	ACLST9	DATA	#	RESTORE POINTERS
343	ACLST9	DATA	#	PROMPT FOR REPLACEMENT
344				
345				
346	ACLST10	BSS	#	POSITIVE PREDICATE
347	ACLST10	DATA	#	DEC-BIN CONVERSION
348	ACLST10	DATA	#	SET BIT IN YNP BUFFER
349	ACLST10	DATA	#	SET BIT IN MPR
350	ACLST10	DATA	#	POINT CPTR AFTER LAPTR
351	ACLST10	DATA	#	POINT TO NEXT SEP. & CLASSIFY
352				
353				
354	ACLST11	BSS	#	NEGATIVE PREDICATE
355	ACLST11	DATA	#	INCREMENT CPTR
356	ACLST11	DATA	#	DEC-BIN CONVERSION
357	ACLST11	DATA	#	SET BIT IN MASK MATRIX
358	ACLST11	DATA	#	POINT CPTR AFTER LAPTR
359	ACLST11	DATA	#	POINT TO NEXT SEP. & CLASSIFY
360				
361				
362	ACLST12	BSS	#	POSITIVE PREDICATE, LAST PREDICATE
363	ACLST12	DATA	#	DEC-BIN CONVERSION
364	ACLST12	DATA	#	SET BIT IN YNP BUFFER
365	ACLST12	DATA	#	SET BIT IN MPR
366	ACLST12	DATA	#	POINT CPTR AFTER LAPTR
367	ACLST12	DATA	#	POINT TO NEXT SEP. & CLASSIFY
368				
369				
370	ACLST13	BSS	#	NEGATIVE PREDICATE, LAST PREDICATE
371	ACLST13	DATA	#	INCREMENT CPTR
372	ACLST13	DATA	#	DEC-BIN CONVERSION
373	ACLST13	DATA	#	SET BIT IN MASK MATRIX
374	ACLST13	DATA	#	POINT CPTR AFTER LAPTR
375	ACLST13	DATA	#	POINT TO NEXT SEP. & CLASSIFY
376				
377				
378	ACLST14	BSS	#	NO PREDICATE SPECIFICATION
379	ACLST14	DATA	#	POINT CPTR AFTER LAPTR
380	ACLST14	DATA	#	POINT TO NEXT SEP. & CLASSIFY
381				
382				

908A OFF LINE ASSEMBLER

383	ACL115	BSS	DATA D28CON	ACTION NUMBER	DEC-BIN CONVERSION
384		DATA	DATA PAILN		WRITE SUB NAME TO ACTION LIST
385		DATA	DATA PCAL		POINT CPTR AFTER LAPTR
386		DATA	DATA PMSB		POINT TO NEXT SEP. & CLASSIFY
387		DATA			
388		DATA			
389					
390	ACL116	BSS	DATA D28CON	LAST ACTION NUMBER	
391		DATA	DATA PAILN		DEC-BIN CONVERSION
392		DATA	DATA PT		WRITE SUB NAME TO ACTION LIST
393		DATA	DATA PCAL		PLACE TERMINATOR
394		DATA	DATA PMSB		POINT CPTR AFTER LAPTR
395		DATA			POINT TO NEXT SEP. & CLASSIFY
396		DATA			
397					
398	ACL117	BSS		NO ACTION SPECIFICATION	
399		DATA	DATA PT		PLACE TERMINATOR
400		DATA	DATA PCAL		PLACE CPTR AFTER LAPTR
401		DATA			
402					
403	ACL118	BSS	DATA VNCN	PROCESS NEXT CLUSTER	
404		DATA	DATA VYH		WRITE NEXT CLUSTER NUMBER
405		DATA	DATA SABICB		WRITE YN, MASK MATRIX
406		DATA			SET ALT BIT IN CLS. BUFFER
407					
408					
409	ACL119	BSS	DATA VCM	CLOSING	
410		DATA	DATA CPF		WRITE TO CLUSTER MATRIX
411		DATA			CREATE PERMANENT FILE KV
412		DATA			

PEJ

DATA SECTION

SIZE OF HEADER,
'S' TYPE RECORD LENGTH
LENGTH OF SOURCE LINE, EXCLUDING CR

HSIZE EQU 15
'S'RLNTH EQU 36
LLNTH EQU 78/2

RFCO DATA RFC
WTCO DATA WTC
GETCHO DATA GETCHA
DE2B10 DATA DEC2B1
B12HA0 DATA B12HAS
B12DA0 DATA B12DAS

OF CLUSTERS
OF ALTERNATIVES
OF ACTIONS
OF CHARACTERS IN I/P BUFFER
THE ALTERNATIVE CURRENTLY PROCESSED
THE CLUSTER WHICH THE CURRENT ALT BELONG

CLSMAT OFFSET
MCLMAT OFFSET, FROM SOURCE LINE NO 1
YNPMAT
MPRMAT
APTR#
ACLST1

BINARY VALUES OF ALT I/P SPECIFICATIONS

BUFFER FOR YNPMAT
MPRMAT ENTRY
ACTIONS FOR ALTERNATIVE

CLS 1
2
3
4
5
6

413	###F	NOCLS	DATA	#
414	###F	NOALTS	DATA	#
415	###F	NOACTS	DATA	#
416	###F	NOCHRS	DATA	#
417	###F	ALT#	DATA	#
418	###F	CLUS#	DATA	#
419	###F	COFFS	DATA	HSIZE
420	###F	MOFFS	BSS	1
421	###F	YOFFS	BSS	1
422	###F	MOFFS	BSS	1
423	###F	AOFFS	BSS	1
424	###F	ACOFFS	BSS	1
425	###F	OFFS	BSS	#
426	###F			
427	###F			
428	###F			
429	###F			
430	###F			
431	###F			
432	###F			
433	###F			
434	###F			
435	###F			
436	###F			
437	###F			
438	###F			
439	###F			
440	###F			
441	###F			
442	###F			
443	###F			
444	###F			
445	###F	NCLBUF	BSS	1
446	###F	YNPBUF	DATA	#
447	###F	MPRBUF	DATA	#
448	###F	ACTMS#	DATA	ACTNS
449	###F	ACTNS	BSS	16
450	###F	ACTNSE	BSS	#
451	###F			
452	###F	CLSBUF	DATA	#,#
453	###F			
454	###F			
455	###F			
456	###F			
457	###F			

980A OFF LINE ASSEMBLER

0107	0000	DATA	0,0	7
0108	0000	DATA	0,0	8
0109	0000	DATA	0,0	9
010A	0000	DATA	0,0	10
010B	0000	DATA	0,0	11
010C	0000	BSS	1	
010D	0000	BSS	1	
010E	0000	OCPTR	1	
010F	0000	OLAPTR	1	
0110	0000	BV	1	
0111	0000	IPLMX	DATA S+1	
0112	0000	DATA	EMPTY+00	
0113	0000	DATA	0,0	
0114	0000	IPLMS	0	
0115	0000	IPLN	40	
0116	0000	IPLME	0	
0117	0A00	DATA	LFCR	

CURRENT POINTER, BYTE OFFSET
 LOOK-AHEAD POINTER, BYTE OFFSET
 OLD CPTR
 OLD LAPTR
 CONVERTED BINARY VALUE

INPUT BUFFER

900A OFF LINE ASSEMBLER

477	PEJ				
478		ACTION SECTION			
479		-----			
480					
481		BRS PROGNO			
482		-- ABL EDITOR INITIALIZATION			
483					
484		BSS			
485		INIT			
486		RMO			
487		L-A			
488		STA			
489		INITRE			
490		LDM		SKIP A LINE	
491		BRL		'VTCC	
492		LDM		-2	
493		BRL		'VTCC	
494		LDM		-0	
495		BRL		'VTCC	
496		LDM		-0	
497		BRL		'VTCC	
498				OPEN FILES	
499					
500					
501		0LDM		-PRBLU1	OPEN TEMP. FILE FOR INPUT ACTIONS
502		SVC		0	
503		0LDM		-PRBLU3	
504		SVC		0	
505		BRU		*INITRE	RETURN
506		INITRE		BSS	RETURN ADDR
507		PRBLU1		DATA 1	PRB TO A/O SCRATCH FILE TO TEMPORARILY STORE INPUT ACTIONS
508				DATA 1	LUN
509				DATA 'DS'	DISK FILE OP-CODE
510				DATA >032#	DISK MNEH.
511				DATA 'ABL/ED'	O/P FILE TYPE; CATEG (2) & TYPE (N/A)
512					FILE NAME
513					
514					
515		DATA		5##	EXTENT IN SECTORS
516		DATA		128	RECORD LENGTH
517					PRB TO OPEN SCRATCH FILE TO TEMPORARILY STORE PROGRAM TO BE CREDITED
518		PRBLU3		LU3	LUN
519		DATA		1	DISK FILE OP-CODE
520		DATA 'DS'			DISK MNEH.
521		DATA >032#			CAT (2), TYPE (N/A)
522		DATA			FILE NAME
523					
524		DATA		3##	EXTENT IN SECTOR
525		DATA		128	RECORD LENGTH IN WORD

988A OFF LINE ASSEMBLER

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-- GET FILE NAME IN FORM 'MMI' AND OPEN/ASSIGN
   A SCRATCHB FILE TO LUN 4. THIS SCRATCH FILE WILL
   BE COPIED INTO PERMANENT PARTITION AREA UNFER
   FILE NAME 'MHIRRS' WHERE RR IS HIGHEST REVISION

RSS #
RMO L.A
STA FNARET
SAVE RETURN

LDM #3
BRL -VTCO
BRL -RFCO
SNO M
BRU FMS
RMO H.A
CPA #3
SCE
BRU FMS
ODLD IPLN
ODST AOPRB+4
OLDM -AOPRB

SVC B
LDA AOPRB
OARD ->FMS

SIZE A
BRU FMS
OLDM -PRBFA

SVC #
LDM #
BRL -VTCO
LDM -4
BRL -VTCO
LDM -14
BRL -VTCO
BRU -FNARET

LDM #8
BRL -VTCO
BRU FN2

DATA E04
DATA 1
DATA 'DS'
DATA >D33B
BSS 2
DATA 'MS'
DATA 4BBS
DATA 128

P #157 C55B
P #158 8B33
P #159 1FB3
P #160 75C1
P #161 75CB
P #162 CCA2
P #163 781A
P #164 C53B
P #165 5FB3
P #166 CDBB
P #167 7816
P #168 84BB
P #169 81BA
P #170 817F
P #171 1BBS
P #172 CAEB
P #173 75C1
P #174 1FB4
P #175 75C1
P #176 1FBE
P #177 75C1
P #178 1FB8
P #179 75C1
P #17A 78DE
P #17C 8BB4
P #17D C4D3
P #17E D33B
P #17F 8BD3
P #180 8FAJ
P #183 8BBB

```

900A OFF LINE ASSEMBLER

578	PRBFA
579	DATA 4
580	DATA >A
581	BSS 1
582	BSS 1
583	BSS 1
584	BSS 3
585	FNARET BSS 1
586	

PRB TO GET FILE ATTRIBUTES OF LUN 4 SHEET 16

LUN	OPCODE
CURRENT REC	
RECORD LENGTH	
FILE EXTENT	
FILE NAME	

800A OFF LINE ASSEMBLER

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587 PEJ
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-- READ ACTION INPUT LINE INTO INPUT BUFFER

SAVE RETURN

ACT #
1ST ACTION IS 1
CONVERT BINARY VALUE TO DEC.
PLACE 2 LS DIGITS IN MESSAGE

INPUT BUFFER IS FIRST BLANKED OUT
..TO CLEAR ALL LEFT OVER INFOS

READ I/P LINE INTO BUFFER
READ ERROR ?
YES -- READ NEW I/P
NO
SAVE # OF CHARS IN I/P LINE
RETURN

IMPROPER INPUT LINE

RETURN ADDRESS
4 BLANKS

SBSA OFF LINE ASSEMBLER

SHEET 18

PEJ

-- STORE ACTION SUBROUTINE IN INPUT BUFFER
 TO TEMPORARY FILE (LUN 1) AND BUMP NO. OF ACTIONS
 EACH ACTION IS STORED IN A RECORD STARTING FROM RECORD 1.

51A8	51C8	BSS	B	BUMP # OF ACTIONS
51A9	51C8	IMO	MOACTS	ACTION #
51AA	5008	LDA	PRBSA+4	..AS RECORD #
51AB	1008	STDA	-PRBSA	
51AC	51AF	STACT		
51AD	CAER			STORE ACTION NAME INTO TEMP FILE
51AE	C557	SVC	F	
		RMO	L,P	
51AF	5001			
51B0	5002			
51B1	5000			
51B2	510A			
51B3	5000			
51B4	5000			
51B5	5023			

PE3
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-- READ VALID PROGRAM NUMBER, NUMBER OF CLUSTERS,
 AND NUMBER OF ALTERNATIVES

BSS #
 RMO L,A
 STA RPIRET

LDH -#
 BRL -WTC#
 LDH -15
 BRL -WTC#
 LDH -16
 BRL -WTC#
 BRL -RFC#
 RMO M,A
 CPA -2
 @LDA IPLNS

SNE
 BRU RPI4
 @AND ->FF#
 @ADD ->#B#
 @STA HLABLI+6

RP13
 RP14
 RP15
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 RP99
 RP100

'NUMBER OF CLUSTERS :'
 'PROGRAM SPEC.'
 'PROGRAM NUMBER'
 READ INPUT
 # OF CHARACTERS READ
 GET PROGRAM NUMBER
 2 DIGITS
 1 DIGIT
 BLANK FILLER

LDH -6
 BRL -WTC#
 BRL -RFC#
 RMO M,A
 CPA -2
 SGE
 BRU RPI5
 RMO M,X
 @LDA -IPLN
 LLA 1
 BRL -DE2B10
 SNO X
 BRU RPI5
 STA NOCLS
 LDH -7
 BRL -WTC#
 BRL -RFC#
 RMO M,A
 CPA -2
 SGE
 BRU RPI1#
 RMO M,X
 @LDA -IPLN
 LLA 1

'NUMBER OF CLUSTERS :'
 'PROGRAM SPEC.'
 'PROGRAM NUMBER'
 READ INPUT
 # OF CHARACTERS READ
 GET PROGRAM NUMBER
 2 DIGITS
 1 DIGIT
 BLANK FILLER

LDH -6
 BRL -WTC#
 BRL -RFC#
 RMO M,A
 CPA -2
 SGE
 BRU RPI5
 RMO M,X
 @LDA -IPLN
 LLA 1
 BRL -DE2B10
 SNO X
 BRU RPI5
 STA NOCLS
 LDH -7
 BRL -WTC#
 BRL -RFC#
 RMO M,A
 CPA -2
 SGE
 BRU RPI1#
 RMO M,X
 @LDA -IPLN
 LLA 1

'NUMBER OF CLUSTERS :'
 'PROGRAM SPEC.'
 'PROGRAM NUMBER'
 READ INPUT
 # OF CHARACTERS READ
 GET PROGRAM NUMBER
 2 DIGITS
 1 DIGIT
 BLANK FILLER

LDH -6
 BRL -WTC#
 BRL -RFC#
 RMO M,A
 CPA -2
 SGE
 BRU RPI5
 RMO M,X
 @LDA -IPLN
 LLA 1
 BRL -DE2B10
 SNO X
 BRU RPI5
 STA NOCLS
 LDH -7
 BRL -WTC#
 BRL -RFC#
 RMO M,A
 CPA -2
 SGE
 BRU RPI1#
 RMO M,X
 @LDA -IPLN
 LLA 1

'NUMBER OF CLUSTERS :'
 'PROGRAM SPEC.'
 'PROGRAM NUMBER'
 READ INPUT
 # OF CHARACTERS READ
 GET PROGRAM NUMBER
 2 DIGITS
 1 DIGIT
 BLANK FILLER

LDH -6
 BRL -WTC#
 BRL -RFC#
 RMO M,A
 CPA -2
 SGE
 BRU RPI5
 RMO M,X
 @LDA -IPLN
 LLA 1
 BRL -DE2B10
 SNO X
 BRU RPI5
 STA NOCLS
 LDH -7
 BRL -WTC#
 BRL -RFC#
 RMO M,A
 CPA -2
 SGE
 BRU RPI1#
 RMO M,X
 @LDA -IPLN
 LLA 1

SHEET 2

900A OFF LINE ASSEMBLER

01E3 75C3
01E5 CCA2
01E7 78F2
01E8 81C7
01E9 1F88
01EA 75CJ
01EB 7C88
01EC

01EL X
01EM RPI1W
01EN STA HOALTS
01EO -8
01EP 0UTC9
01EQ RPIRET
01ER
01ES RPIRET BSS 1

SUCCESSFUL CONVERSION 7
NO
YES - THEN SAVE IT
'ENTER PROGRAM'
RETURN
RETURN ADDR

988A OFF LINE ASSEMBLER

722	PEJ		
723			
724			
725			
726			
727	RIL		
728			
729			
730			
731			
732			
733	RIL5		
734			
735			
736			
737			
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740			
741			
742			
743	RILRET BSS 1		
744			

-- READ ALT. INPUT LINE INTO INPUT BUFFER

BSS #
RMO L.A
STA RILRET

SAVE RETURN

OLD BBBB
LDX --4B
@DST IPLNE.X

RIM X.X
BIX RIL5
LDM -1B
BRL *VTC0
RMO M.A
STA NOCHRS
BRU *RILRET

INPUT BUFFER IS FIRST BLANKED OUT
..TO CLEAR ALL LEFT OVER INFOS

READ I/P LINE INTO BUFFER
SAVE # OF CHARS IN I/P-LINE
RETURN
RETURN ADDRESS

980A OFF LINE ASSEMBLER

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-- INITIALIZATION :
1. CALCULATES OFFSETS
2. CREATE HEADER
3. CREATE LABELS (NCLMAT, YNPMAT....),
   POINTERS (NCL0, YNPO....)

PEJ
BSS #
PINIT #
CALCULATE OFFSETS
# OF CLUSTERS
2 LINES/CLUSTER
PLUS HEADER SIZE
FOR 'CLSMAT BSS #'
OFFSET TO 'NCLMAT'
FOR 'BSS #'
'YNPMAT'
FOR 'BSS #'
'MPRMAT'
FOR 'BSS #'
'APTR#'
FOR 'BSS #'
'ACLS#'
OFFSET TO 'ACLS#'

LDA NOCLS
LLA 1
ADD -HSIZE
STA MOFFS
ADD MOALTS
STA YOFFS
ADD MOALTS
STA MOFFS
ADD MOALTS
STA AOFFS
ADD MOALTS
STA ACOFFS
CREATE HEADER
LDX --15
@LDM -PRBHDR
SVC #
LDA PRBHDR+3
ADD -1#
STA PRBHDR+3
IMO PRBHDR+4
BIX PIN3
CREATE LABELS
LDX --5
LDA OFFS.X
STA PRB3+4
RIN A.A
STA OFFS.X
@LDM -PRB3
SVC #
LDA PRB3+3
ADD -7
STA PRB3+3
BIX PIN5

15 LINES IN HEADER
WRITE TO TEMP FILE
1# WORDS/LINE
NEXT LABEL
NEXT RECORD
LINE OFFSET
..AS RECORD OFFSET
POINT TO LINE AFTER '.... BSS #'
POINT TO NEXT LABEL

```

CREATE 'DATA ACLS00.ACLS01...' AFTER APTR

```

822C 81C7
822D 8023
822E 8108
822F 801E
8230 1808
P 8231 824A
8232 CAEB
8233 801A
8234 8018
8235 2787
8236 8016
8237 4819
8238 78F9

8239 8788
823A 81CA
823B C557

823C 8883
823D 8882
823E 8888
E 823F 8888
8240 8888
8241 8888
8242 888A

8243 8883
8244 8882
8245 8888
P 8246 8252
8247 8888
8248 8888
8249 8887

824A 8883
824B 8882
824C 8888
E 824D 8888
824E 8888
824F 8888
8250 8887

8251

8252 C3CC
8253 D3CD
8254 C1D4
8255 A8C2
8256 D3D3
8257 A888
8258 A88D

```

BUMP TO NEXT RECORD

RESET ALT # TO #

PRB TO WRITE HEADER LINES

STARTING FROM 1ST LINE (REC #)

PRB TO CREATE LABELS IN SCRATCH FILE (LUN 3)

WRITE OP-CODE

BUFFER RECORD#

WORD#

WORD COUNT

REC#

WD#

WD COUNT

INDEX

DATA 'CLSMAT BSS #',BBCC

980A OFF LINE ASSEMBLER
 847 DATA 'NCLMAT BSS #',BBGR
 #259 CEC3
 #25A CCCD
 #25B C1D4
 #25C AJC2
 #25D D3D3
 #25E AJBB
 #25F AJBD
 #260 D3CE
 #261 D3CD
 #262 C1D4
 #263 AJC2
 #264 D3D3
 #265 AJBB
 #266 AJBD
 #267 CDDF
 #268 D2CD
 #269 C1D4
 #26A AJC2
 #26B D3D3
 #26C AJBB
 #26D AJBD
 #26E C1D4
 #26F D4D2
 #270 BBAF
 #271 AJC2
 #272 D3D3
 #273 AJBB
 #274 AJBD
 #275 C1C3
 #276 CCD3
 #277 BBAF
 #278 AJC2
 #279 D3D3
 #27A AJBB
 #27B AJBD

848 DATA 'YNPHAT BSS #',BBGR
 849 DATA 'MPRMAT BSS #',BBGR
 850 DATA 'APTR# BSS #',BBGR
 851 DATA 'ACLS# BSS #',BBGR

PEJ

-- ANALYZE 1ST CHARACTER IN INPUT LINE

BSS #
RMO L.A
STA AFCRET
OLDM -IPLN

SAVE RETURN

GET CHARACTER AT CPTR
..INTO (E)
5 TYPES

MATCHED ?
YES
NO - TRY NEXT

SET APPROP. PRED. FOR CHAR TYPE

RETURN ADDR.

CR

NOT USED

ASSEMBLER

852

853

854

855

856 AFC

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#27C

#27D

#27E

#27F

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#28A

#28B

#28C

#28D

#28E

#28F

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#292

#293

#294

#295

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#297

#298

#299

#29A

#29B

#29C

#29D

#29E

#29F

LDA #GETCHO

RMO A.E

LDX #-5

LDA CHAR.X

REC E.A

QAND ->#7F

SNZ A

BRU AFC1#

RIX AFC#5

BRU AFC15

LDA PREDB.X

STA SETP

LDA SVECTR

BSS 1

STA SVECTR

BRU #AFCRET

BSS 1

DATA >#00

DATA . / .

DATA >FFFF

DATA . \$.

DATA . . .

BSS #

SABO 5

SABO 4

SABO 3

SABO 2

SABO 1

SABO #

BSS #

CHAR

PREDB

900A OFF LINE ASSEMBLER

SHEET 27

897	REJ		
898			
899			
900			
901	PNSC	BSS	FIRST CLEAR STATE VECTOR
902		RMO L.A	
903		STA PMSCRE	
904			
905		LDA -B	
906		STA SVECTR	
907			
908		OLDM -IFLN	
909			
910	PNSC#2	IMO LAPTR	
911		LDA LAPTR	
912		BRL *GETCHO	
913		RMO A.E	GET CHAR IN (E) 6 TYPES INCLUDING CR
914		LDX -B	
915	PNSC#5	LDA CHAR,X	
916		REQ E.A	
917		QAND ->B#7F	
918			
919		SNZ A	THIS TYPE ?
920		BRU PMSCL#	YES
921		BIX PMSC#5	NO - TRY NEXT TYPE
922		BRU PMSC#2	
923			
924	PNSC1#	LDA LAPTR	LDA PTR AT SAME POSITION AS CPTR ?
925		CPA CPTR	YES
926		SNE	NO
927		BRU PMSCL5	
928		LDA PRED#X	
929		STA SETPR	
930		LDA SVECTR	SET PREDICATE FOR THE SEPARATOR
931		BSS I	(M) - START OF IP BUFFER
932		STA SVECTR	
933		OLDM -IFLN	
934			
935		LDA CPTR	CURRENT POINTER
936		BRL *GETCHO	'-' AT CPTR ?
937		QCPA ->B#AD	
938			
939		SEQ PMS12	NO
940		BRU SMBO 3,SVECTR	YES - SET PREDICATE FOR -
941			
942		IMO CPTR	MOVE CPTR PAST '-'
943	PNS12	BRU *PMSCRE	RETURN
944			
945		PNSCL5	SET 'LAPTR=CPTR' PREDICATE
946		SMBO 5,SVECTR	
947		BRU *PMSCRE	RETURN
948			
949		PMSCRE BSS I	

PEJ

-- POINT CURRENT POINTER ONE WORD AFTER
LOOK-AHEAD POINTER

LDA LAPTR
RIN L.A.
STA CPTR
RMO L.P.

-- POINT CPTR & LAPTR TO START PROCESS PRED. SPEC.

LDA L.A.
LDA L.P.
STA CPTR
RMO L.P.

-- INCREMENT CPTR

IMO CPTR
RMO L.P.

-- PROMPT FOR REPLACEMENT

BSS #
RMO L.A.
STA PFRRET

'INVALID CHARACTER....'
>>

BSS I
PFRRET BSS I
RETURN ADDR.

945
946
947
948
949
950
951 PCAL

BZCA B1FD
BZCB C3BB
BZCC B1FC
BZCD C557

957
958
959 PCL

BZCE B7BN
BZCF B1FD
BZCG B7NS
BZDH B1FC
BZDI C557

963
964
965
966
967
968
969
970
971
972
973 PFR

BZD3 B1FC
BZD4 C557

974
975
976
977
978
979
980
981
982
983

BZD5 C55B
BZD6 B8B5
BZD7 B1FC
BZD8 B7C1
BZD9 B1FC
BZDA B7C1
BZDB B7C1
BZDC

980A OFF LINE ASSEMBLER

PEJ

SHEET 29

-- WRITE A COMMENT LINE TO ACTION MATRIX
-- AS RECORD
-- POINTER TO CURRENT ENTRY IN ACTION MAT.

B200 B1D1
B201 B009
B202 B009
B203 B2E4
B2E1 CAE8
B2E2 B1D1
B2E3 C857

BSS #
LDA ACOFFS
STA PRBVCL
@LDN--PRBVCL

SVC #
IMO ACOFFS
RMO L.P

DATA LU3
DATA 2
DATA #
DATA IPLN
DATA #
DATA #
DATA LLNTH

LUN
WRITE

BUFFER ADDR
RECORD #
WORD #
WORD COUNT

PRBVCL

B2E4 B883
B2E5 B882
B2E6 B888
B2E7 B16A
B2E8 B888
B2E9 B888
B2EA B823

PEJ

--- CONVERT DEC ASCII STRING INTO BINARY VALUE
START AND END OF STRING ARE DETERMINED BY
(CPTR) AND -(LAPTR)-1

RETURN : BINARY VALUE IN BV

02EB	C558	B	D2BCON	BSS	B	SAVE RETURN
02EE	0008	L.A	RMO	L.A		
02EC		STA	STA	CONRET		
02ED	01F0	LDA	LDA	LAPTR		GET LOOK-AHEAD PTR
02EE	29FC	SUB	SUB	CPTR		(X) = 0 OF ASCII
02EF	C502	RMO	RMO	A.X		START ADDR OF I/P LINE BUFFER
02F0	0000	OLDA	OLDA	=IPLN		
02F1	010A	LLA	LLA	1		CONVERT TO BYTE POINTER
02F2	C8C1	ADD	ADD	CPTR		(A) = BYTE POINTER TO 1ST ASCII
02F3	21FC	BRL	BRL	=DE2B10		DEC-TO-BIN CONVERSION
02F4	76C3	OSTA	OSTA	BV		PLACE RESULT IN BV
02F5	0400	BRU	BRU	=CONRET		RETURN
02F6	0105	CONRET	CONRET	BSS	1	RETURN ADDR
02F7	7C00					
02F8						

988A OFF LINE ASSEMBLER

```

1828 PEJ
1829
1830 * - SET PREDICATE BIT IN YMPBUF
1831 * BIT NO. IS DETERMINED BY BV
1832 *
1833 * INPUT : BV
1834 * OUTPUT : YMPBUF
1835
1836 SBYMP BSS # BIT NO. TO SET
1837 OLDX BV
1838
1839 * LDA SABOP,X
1840
1841 STA SETBIT (A)-YM BUFFER TO TO SET
1842 LDA YMPBUF LDA YMPBUF (A)-YM BUFFER TO TO SET
1843 STA YMPBUF STA YMPBUF 'SABO #. TO SET BIT IN (A)
1844 RMO L,P RETURN
1845
1846 SABOP
1847 SABO #
1848 SABO 1
1849 SABO 2
1850 SABO 3
1851 SABO 4
1852 SABO 5
1853 SABO 6
1854 SABO 7
1855 SABO 8
1856 SABO 9
1857 SABO 10
1858 SABO 11
1859 SABO 12
1860 SABO 13
1861 SABO 14
1862 SABO 15
1863
1864 * -- SAVE CLUSTER NUMBER
1865 SACL5 OLDA BV
1866
1867 STA CLUS#
1868 RMO L,P

```

988A OFF LINE ASSEMBLER

LINE NO.	ASSEMBLER	PEJ	DESCRIPTION	ACTION LIST
1068			- SET BIT IN MPRBUF	
1069			BIT NO. IS DETERMINED BY BV	
1070				
1071			INPUT : BV	
1072			OUTPUT : MPRBUF	
1073				
1074				
1075			BSS #	BIT TO BE SET
1076			OLDX BV	
1077				
1078			OLDA SABOX,X	INST. TO SET BIT IN (A)
1079			STA SBIT	(A)- MPR BUFFER
1080			LDA MPRBUF	INST. TO SET BIT
1081			BSS 1	
1082			STA MPRBUF	
1083			RMO L,P	RETURN
1084				
1085				
1086				
1087				
1088			PAILN	-- WRITE SUBROUTINE NAME INTO ACTION LIST
1089			BSS #	ACTION NUMBET
1090			OLDA BV	
1091			CPA NOACTS	VALID ACTION NUMBER ?
1092			SCE	NO
1093			RMO L,P	
1094			STA PRBRAC+4	YES - USE IT AS RECORD NUMBER
1095			OLDM -PRBRAC	
1096			SVC #	
1097				
1098			LDA ACOFFS	OFFSET TO CURRENT LINE IN ACTION LIST
1099			STA PRBVAC+4	..AS RECORD NUMBER
1100			OLDM -PRBVAC	
1101			SVC #	
1102			IMO ACOFFS	
1103			RMO L,P	BUMP TO NEXT LINE
1104				
1105				
1106			PRBRAC	READ
1107			DATA LUI	BUFFER ADDR
1108			DATA #	RECORD
1109			DATA ABUF	WORD #
1110			DATA #	WD COUNT
1111			DATA #	
1112			DATA LLNTH	WRITE
1113			DATA LU3	
1114			DATA 2	
1115			DATA #	
1116			DATA #	
1117			DATA #	
1118			DATA #	
1119			DATA #	

988A OFF LINE ASSEMBLER	
#33C	DATA 128
#33D	1120
#33E	1121
#33F	1122
#340	1123
ABC4	WABUF
C1D4	DATA
C1A5	DATA
BSS	120-3
BUFFER	

ADDRESS	OPERAND	OPERATION	DESCRIPTION
1124		PEJ	
1125		*	
1126		*	
1127		*	
1128		*	
1129	WCH		
1130	BSS		SAVE RETURN
1131	RMO L.A		
1132	STA WCMRET		
1133	LDA NOCLS		# OF CLUSTERS
1134	RAD A.A		2 WORDS / CLUSTER
1135	STA NOWD		
1136	LDA		RESET INDEX
1137	STA		
1138	LDA		
1139	LDX INDX		
1140	LDA CLSBUF,X		BINARY VALUE
1141	LDE		NO ZERO SUPPRESSION
1142	BRL		CONVERT BIN TO HEX
1143	@DST OPBUF+4		'HHHH'
1144	LDA COFFS		OFFSET CURRENT ENTRY IN CLSMAT
1145	@STA PRBVYN+4		..AS RECORD #
1146	@LDM -PRBVYN		
1147	SVC		
1148	IMO COFFS		BUMP CLUSTER ENTRY OFFSET
1149	IMO INDX		BUMP INDEX
1150	DMT NOWD		FINISHED ?
1151	BRU		NO
1152	BRU		RETURN
1153			
1154			
1155	WCMRET BSS 1		
1156	NOWD BSS 1		
1157	INDX BSS 1		
1158			

988A OFF LINE ASSEMBLER

PEJ

-- PLACE TERMINATOR DATA B INTO ACTION LIST

LDA ACOFFS
STA PRBT+4
OLDM -PRBT

CURRENT LINE

SVC B
IMO ACOFFS
LDA ACOFFS
STA PRBST+4
OLDM -PRBST

BUMP TO NEXT LINE
USE AS REC # IN PRB

SVC B
IMO ACOFFS
LDA PRBS+3
ADD -7
STA PRBS+3
LDA ACOFFS
STA PRBS+4
OLDM -PRBS

PLACE ... AFTER DATA B
BUMP TO NEXT LINE
POINT TO NEXT NEXT LABEL
CURRENT LINE

SVC B
IMO ACOFFS
RMO L.P
PRBT
DATA LUI3
DATA 2
DATA B
DATA TERMTR
DATA B
DATA B
DATA 5
TERMTR DATA : DATA B ,.BBCR

WRITE 'ACLS...'
RETURN
WRITE
BUFFER RECORD#
WORDS
WD COUNT

PRBST
DATA LUI3
DATA 2
DATA B
DATA ASTER
DATA B
DATA 2
DATA :. .,BPCR
PRBS
DATA LUI3
DATA 2
DATA B
DATA ACLSS

WRITE_
BUFFER ADDR
REC
WORD #
WORD COUNT

8308	81D1	1159	
8309	8018	1160	
830A	1000	1161	
830B	83EE	1162	
830C	CAE8	1163	PT
		1164	
		1165	
		1166	
		1167	
		1168	
830D	51D1	1169	
830E	81D1	1170	
830F	801E	1171	
830G	1000	1172	
		1173	
		1174	
83E3	51D1	1175	
83E4	8021	1176	
83E5	2787	1177	
83E6	801F	1178	
83E7	81D1	1179	
83E8	801E	1180	
83E9	1000	1181	
		1182	
		1183	
		1184	
		1185	
83EE	8003	1186	
83EF	8002	1187	
83F0	8000	1188	
83F1	83F5	1189	
83F2	8000	1190	
83F3	8000	1191	
83F4	8005	1192	
83F5	AMC4	1193	
83F6	C1D4		
83F7	C1A8		
83F8	80A8		
83F9	A000		
		1194	
83FA	8003	1195	
83FB	8002	1196	
83FC	8000	1197	
83FD	8401	1198	
83FE	8000	1199	
83FF	8000	1200	
8400	8002	1201	
8401	AAA8	1202	
8402	A000		
8403	8003	1203	
8404	8002	1204	
8405	8000	1205	
8406	8000	1206	
8407	8000		

SHEET 36

SUBA OFF LINE ASSEMBLER
8487 8888 1287 DATA 5
8488 8888 1288 DATA 5
8489 8887 1289 DATA 7

908A OFF LINE ASSEMBLER

1210	PEJ				
1211		---	SAVE POINTERS		
1212					
1213					
1214					
1215	SAVEP	BSS			
1216	SAVEP	LDX	-NOPTRS		-VE NO. OF POINTERS TO BE SAVED
1217	SAVEP	LDA	OFFS,X		GET VALUE OF POINTER
1218		STA	OPTRS,X		..SAVE IT
1219		BIX	SAVEP		
1220		RMO	L,P		
1221					
1222		---	RESTORE POINTERS		
1223					
1224					
1225	RESTOR	BSS			
1226	RESTOR	LDX	-NOPTRS		-VE NO. OF POINTERS TO SAVE
1227	RESTOR	LDA	OPTRS,X		GET OLD VALUE OF POINTER
1228	RESTOR	STA	OFFS,X		RESTORE
1229		BIX	RESTOR		SAVE RESTORE NEXT POINTER
1230		RMO	L,P		
1231					
1232	OPTRSB	BSS	1		FOR COFFS
1233	OPTRSB	BSS	1		NOFFS
1234	OPTRSB	BSS	1		YOFFS
1235	OPTRSB	BSS	1		MOFFS
1236	OPTRSB	BSS	1		AOFFS
1237	OPTRSB	BSS	1		ACOFFS
1238	OPTRSB	BSS	1		OLD VALUES OF POINTERS
1239	OPTRSB	EQU	OPTRSB-OPTRS		-VE NO. OF POINTERS
1240					
1241					
1242		---	WRITE TO NEXT CLUSTER MATRIX		
1243					
1244	VMCN	BSS			
1245	VMCN	RMO	L,A		
1246	VMCN	STA	VMCRET		SAVE RETURN
1247					
1248		OLDM	-IPLN		(M)= START OF I/P BUFFER
1249		LDA	CPTR		(A)= BYTE OFFSET
1250		BRL	*GETCH0		GET 1ST CHAR
1251		LLA	0		SHIFT TO LH BYTE
1252		STA	SAVE		
1253		LDA	LAPTR		
1254		SUB	CPTR		
1255		CPA	-1		0 OF CHARS
1256		SNE	VMC5		1 CHAR
1257		BRU	VMC5		2ND CHAR
1258		LDA	CPTR		
1259		RIN	A,A		
1260		BRL	*GETCH0		PLACE 2 CHARS TOGETHER
1261		ADD	SAVE		
1262		BRU	VMC7		
1263					

#40A 17FA
 #40B 855B
 P #40C 88D7
 #40D 828E
 #40E 48FC
 #40F C557

#41B 17FA
 #41C 828A
 #41D 868B
 P #41E 88D7
 #41F 48FC
 #420 C557

#416
 #417
 #418
 #419
 #41A
 #41B
 #41C
 FFFA

#41C
 #41D
 #41E
 #41F
 #420
 #421
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 #427
 #428
 #429
 #42A
 #42B
 #42C
 #42D

188B
 818A
 81FC
 75C2
 88C8
 8821
 81FD
 29FC
 6F81
 CD8J
 7885
 81FC
 C88B
 75C2
 2818
 7883

900A OFF LINE ASSEMBLER

B42E	B016	1264	UNCS	LDA SAVE	PLACE BLANK
B42F	2000	1265		OADD ->B0A5	
B430	B0A0			STA OPBUF2+3	CURRENT MCL ENTRY
B431	B011	1266	VNC7	LDA MOFFS	...AS RECORD*
B432	B1CD	1267		STA PRBUNC+4	
B433	B009	1268		OLDM -PRBUNC	
B434	1000	1269		SVC #	POINT TO NEXT ENTRY IN MCLMAT
B435	B435	1270		IMO MOFFS	RETURN
B436	CAES	1271		BRU -VMCRET	
B437	51CD	1272		PRBUNC DATA L03	WRITE
B438	7C0D	1273		DATA 2	
B439	B003	1274		DATA #	
B43A	B002	1275		DATA OPBUF2	REC*
B43B	B000	1276		DATA #	WORD*
B43C	B440	1277		DATA #	WORD COUNT
B43D	B000	1278		DATA #	
B43E	B000	1279		DATA 5	
B43F	B005	1280		OPBUF2 DATA	TEMP SAVE
B440	ABC4	1281		DATA MN*.B8CR	RETURN
B441	C104	1282			
B442	C1A0			SAVE DATA 1	
B443	CECE			VMCRET BSS 1	
B444	A00D				
B445	B001	1283			
B446		1284			

```

1285      FEJ
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1500

```

-- SET THE ALTERNATIVE BIT IN THE CLUSTER BUFFER
SABICB BSS L.A
RMO STA SABRET
LDA CLUS
RDE A.A
LLA I
STA CINDX
SAVE CLS DOUBLE INDEX
A.X
RMO CLSBUF.X
OLD DST VDI
LDA ALT
CPA =16
SLE VCS
BRU SUB -16
SUB -16
RMO A.X
@LDA SABOP.X
STA LOC3
LDA VDI
BSS I
STA VDI
@BRL RLOC
BRU WC7
RMO A.X
@LDA SABOP.X
STA LOC4
LDA VDI
BSS I
STA VDI
@BRL RLOC
LDX CINDX
OLD DST CLSBUF.X
IMO ALT
BRU =SABRET
SABRET BSS I
RETURN

PEJ

WRITE TO Y/M. MASK MATRIX:

B46A C558
 B46A B728
 B46B
 B46C
 B46D
 B46E
 B46F
 P B470
 B471
 B472
 P B473
 B474
 P B475
 B476
 B477
 P B478
 B479
 P B47A
 B47B
 P B47C
 B47D
 B47E
 B47F
 B480
 P B481
 B482
 B483
 P B484
 B485
 P B486
 B487
 B488
 B489

B1D3
 B1D4
 B1D5
 B1D6
 B1D7
 P B1D8
 B1D9
 B1DA
 P B1DB
 B1DC
 B1DD
 P B1DE
 B1DF
 B1DG
 P B1DH
 B1DI
 B1DJ
 P B1DK
 B1DL
 B1DM
 P B1DN
 B1DO
 B1DP
 B1DQ
 P B1DR
 B1DS
 B1DT
 B1DU
 P B1DV
 B1DW
 B1DX
 B1DY
 B1DZ

B1E1
 B1E2
 B1E3
 B1E4
 B1E5
 B1E6
 B1E7
 B1E8
 B1E9
 B1EA
 B1EB
 B1EC
 B1ED
 B1EE
 B1EF
 B1EG
 B1EH
 B1EI
 B1EJ
 B1EK
 B1EL
 B1EM
 B1EN
 B1EO
 B1EP
 B1EQ
 B1ER
 B1ES
 B1ET
 B1EU
 B1EV
 B1EW
 B1EX
 B1EY
 B1EZ

B1F1
 B1F2
 B1F3
 B1F4
 B1F5
 B1F6
 B1F7
 B1F8
 B1F9
 B1FA
 B1FB
 B1FC
 B1FD
 B1FE
 B1FF
 B1FG
 B1FH
 B1FI
 B1FJ
 B1FK
 B1FL
 B1FM
 B1FN
 B1FO
 B1FP
 B1FQ
 B1FR
 B1FS
 B1FT
 B1FU
 B1FV
 B1FW
 B1FX
 B1FY
 B1FZ

B1G1
 B1G2
 B1G3
 B1G4
 B1G5
 B1G6
 B1G7
 B1G8
 B1G9
 B1GA
 B1GB
 B1GC
 B1GD
 B1GE
 B1GF
 B1GG
 B1GH
 B1GI
 B1GJ
 B1GK
 B1GL
 B1GM
 B1GN
 B1GO
 B1GP
 B1GQ
 B1GR
 B1GS
 B1GT
 B1GU
 B1GV
 B1GW
 B1GX
 B1GY
 B1GZ

B1H1
 B1H2
 B1H3
 B1H4
 B1H5
 B1H6
 B1H7
 B1H8
 B1H9
 B1HA
 B1HB
 B1HC
 B1HD
 B1HE
 B1HF
 B1HG
 B1HH
 B1HI
 B1HJ
 B1HK
 B1HL
 B1HM
 B1HN
 B1HO
 B1HP
 B1HQ
 B1HR
 B1HS
 B1HT
 B1HU
 B1HV
 B1HW
 B1HX
 B1HY
 B1HZ

BSS B
 RMO L.A
 STA WCYRET

WRITE INTO YMHMAT

LDA YMPBUF
 LDE -B
 BRL -B12HAP
 @DST OPBUF+4

Y/M IN BINARY FORM
 NO ZERO SUPPRESSION
 CONVERT TO HEX

LDA YOFFS
 @STA PRBVM+4
 @LDH -PRBVM

CURRENT YMH ENTRY
 ..AS RECORD #

SVC B

SRF

DATA REGFIL

@BRL WSTR

LRF

DATA REGFIL

@TMO YOFFS

BUMP YMH ENTRY

WRITE INTO MPR MATRIX

LDA MPRBUF

LDE -B

BRL -B12HAP

@DST OPBUF+4

MASK IN BINARY FORM
 NO ZERO SUPPRESSION
 CONVERT TO HEX

LDA MOFFS

@STA PRBVM+4

@LDH -PRBVM

SVC B

TMO MOFFS

BRU WCYRET

RETURN

MASK ENTRY
 ..AS RECORD #

PRBVM

DATA LU3

DATA 2

DATA 3

DATA OPBUF

DATA 5

DATA 7

WRITE

RECORD #
 WORD #
 WORD COUNT

41

SHEET

CLS DOUBLE INDEX

CLS WORD 1

RETURN 2

988A OFF LINE ASSEMBLER
#491 0000 1379 CINDX DATA #
#492 1380 VD1 BSS 1
#493 1381 VD2 BSS 1
#494 1382 WCYRET BSS 1
1383 "
1384 "

PEJ	ASSEMBLER	LOC1	OPERATION	DESCRIPTION
	1385		PEJ	
	1386		*	
	1387		*	
	1388		*	
	1389		*	
	1390	CPF	BSS	-- CREATE PERMANENT SOURCE FILE BY COPYING LUN 3 TO LUN 4
	1391		RMO	SAVE RETURN
	1392		STA	
	1393		LDA	LAST RECORD USED IN LUN 3
	1394		RDE	DO NOT COUNT 'ACLS...' FOR NEXT ALT
	1395		RCD	..USED AS LOOP INDEX
	1397			
	1398	CPF3	OLDM	READ LUN 3 RECORD
	1399		SVC	
	1400		IMO	BUMP TO NEXT RECORD
	1401		BRL	COUNT # OF WORD
	1402		STA	PLACE IN PRB WORD COUNT
	1403		RMO	
	1404		CPA	LAST REC
	1405		SEQ	
	1406		BRU	NO
	1407		OLDA	YES
	1408		ADD	FORM VD CNT TO EOF
	1409		STA	
	1410		DLD	...TO BUFFER
	1411		DST	
	1412		IMO	
	1413		IMO	
	1414		LDA	4 MORE WORD
	1415		LDA	
	1416		STA	
	1417		LDA	
	1418		ADD	
	1419	WR	STA	WRITE INTO LUN 4
	1420	LOC1	BSS	
	1421		SVC	
	1422		SRF	
	1423		DATA	
	1424		BRL	
	1425		LRF	
	1426		DATA	
	1427			
	1428		LDA	FIND OUT WHERE TO START NEXT TIME
	1429		ADD	PAST TO NEXT 'S' RECORD ?
	1430		OCPA	
	1431		SGT	
	1432		BRU	YES
	1433		STA	NO - UPDATE WORD OFFSET
	1434		BRU	
	1435			
	1436	CPF5	OSUB	CALCULATE WORD OFF SET IN NEXT RECORD
	1437			

BUMP RECORD ON LUN 4
TRANSFER NEXT RECORD
MAKE FILE PERMANENT

980A OFF LINE ASSEMBLER

980A OFF	LINE	ASSEMBLER	STA	WD	
B4BE	8029	1437	IMO	REC	
B4BF	8027	1438	BIX	CPF3	
B4CB	80D9	1439	OLDM	-PRBCLO	
B4C1	1800	1440	SVC	8	
B4C2	84EA	1441	ODLD	PR8FA+8	
B4C3	CAE8	1442	ODST	HI2+15	
B4C4	8400	1443	OLDA	PR8FA+7	
B4C5	8189	1444	OSTA	HI2+17	
B4C6	8400	1445	LDM	-12	
B4C7	800F	1446	BRL	*WTC0	
B4C8	8400	1447	BRU	*CPFRET	RETURN
B4C9	8188	1448	CPFRET	BSS 1	RETURN
B4CA	8400	1449	REGFIL	BSS 8	
B4CB	8011	1450	ENDS	DATA >A/C5.>CEC4	
B4CC	1F0C	1451	EOF	DATA >#D80	
B4CD	75C1	1452	TPPTR	BSS 1	TEMP PTR
B4CE	7C00	1453	PRBL3	DATA LU3	LUN 3
B4CF		1454	DATA	#	READ
B4D0		1455	DATA	#	
B4D8	ABC5	1456	DATA	OPBUF	
B4D9	CEC4	1457	DATA	#	
B4DA	8080	1458	DATA	#	
B4DB		1459	DATA	#	
B4DC		1460	DATA	#	
B4DD		1461	DATA	#	
B4DE		1462	DATA	#	
B4DF	8556	1463	DATA	128	
B4E0		1464	DATA	LU4	LUN 4
B4E1		1465	DATA	2	WRITE
B4E2		1466	DATA	#	
B4E3		1467	DATA	OPBUF	
B4E4		1468	DATA	#	
B4E5		1469	REC	#	
B4E6	8556	1470	WD	#	
B4E7		1471	VDCNT	#	
B4E8		1472	PRBL4	DATA LU4	PRB TO CLOSE AND TO MAKE FILE PERMANENT
B4E9		1473	PRBCLO	DATA LU4	CLOSE
B4EA	8004	1474	DATA	4	N/A
B4EB	8004	1475	DATA	#	
B4EC	8000	1476	DATA	#	

1477 *
1478 *
1479 *
1480 *
1481 *
1482 *
1483 *
1484 *
1485 *
1486 CC
1487 *
1488 *
1489 *
1490 LOC2

PEJ
SUBROUTINE TO COUNT NUMBER OF WORDS IN LINE
A LINE IS TERMINATED (AND INCLUDING) BY CR
IF CR IS LOCATED IN LH BYTE, A BLANK IS INSERTED BEFORE IT

ENTRY : (M)= START ADDR OF BUFFER
RETURN : (A)= # OF WORDS (INCLUDING CR)

BSS #
RMO L.A
STA CCRET
@LDM =OPBUF (M)=START OF O/P BUFFER
LDA --1
STA CCNT
IMD CCNT
LDA CCNT
BRL @GETCH0
@AND =>@B7F
@CPA =>@BDD
CR ?

RESET CHAR COUNT

GET CHAR

NO - CHECK NEXT CHAR
YES
IS CR IS LH BYTE ?

NO
YES - THEN PLACE BLANK BEFORE CR

WD OFFSET FROM START OF BUFFER
FORM POINTER

PLACE BLANK + CR
TO COUNT THE BLANK
CHAR COUNT
ADJUST
WORD COUNT.
..OM RETURN

RETURN
CHAR COUNT
TEMP POINTER

#4ED C550
#4ED #556
#4EE #B1C
#4EF 1800
#4FF #57F
#4F2 #B19
#4F3 #518
#4F4 #B17
#4F5 75C2
#4F6 #B00
#4F7 #B7F
#4F8 #B00
#4F9 #BDD
#4FA CD20
#4FB 70F7
#4FC DB2F
#4FD #50C
#4FE 7008
#4FF #B0C
#500 #B00
#501 #BDD
#502 CB41
#503 #B00
#504 #B00
#505 #C07
#506 #B05
#507 #B04
#508 2701
#509 CB41
#50A 7C00
#50B
#50C
#50D

CC
CC5
CC7
CC10
CCRET
CCNT
TPTR

BRU CC10
LDA CCNT
@LDE =>@BDD
LRA 1
RAD H.A
STA TPTR
STE #TPTR
IMD CCNT
LDA CCNT
ADD -1
LRA 1
BRU #CCRET

BSS 1
BSS 1
BSS 1

BSS 1

980A OFF LINE ASSEMBLER

LINE	ASSEMBLER	PEJ	FOR DEBUGGING : WRITE CONTENT OF A LOCATION
1520			
1521			
1522			
1523			
1524	RLOC	BSS	
1525		SRF	
1526		DATA	REGFIL
1527		RMO	L.A
1528		STA	RLORET
1529		LDX	=LTAB-LTABE
1530			
1531		LDA	LTABE.X
1532		STX	INDEX
1533		STA	RLOPTR
1534		LDA	*RLOPTR
1535		LDE	*R
1536		BRL	*R12MA0
1537		DST	DEBUF+3
1538		OLDM	-PRBDE
1539		SVC	
1540		LDX	INDEX
1541		BIX	DEBS
1542		LRF	DATA
1543		BRU	*RLORET
1544			
1545		RLORET	BSS 1
1546		RLOPTR	BSS 1
1547			
1548		LTAB	DATA
1549			CLUS0
1550			DATA
1551			VDI
1552			VD2
1553			LOC3
1554			LOC4
1555			REGFIL
1556			REGFIL+2
1557			CLSBUF
1558			CLSBUF+1
1559		LTABE	BSS
1560			
1561		PRBDE	DATA
1562			2
1563			12
1564			S+1
1565		DEBUF	DATA
			>HHNN'.LFCR
1566		INDEX	BSS 1
1567			
1568			
1569			
1570			

GET CONTENT
NO ZERO SUPPRES.M

FOR DEBUGGING : WRITE STRING OF ASCII TO CONSOLE

9800A OFF LINE ASSEMBLER
 1571 WSTR
 1572
 1573 WS#5
 1574
 1575
 1576
 1577
 1578
 1579
 1580
 1581
 1582
 1583
 1584
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 1597
 1598
 1599
 1600
 1601
 1602
 1603
 1604
 1605
 1606
 1607
 1608

RESET INDEX
 GET STARTING ADDRESS
 FINISHED
 BUFFER ADDR
 # OF CHARS
 FOR LFCR
 WD OFFSET

BSS #
 LDA STRG,X
 SNZ A
 RMO L,P
 STA PRBAS+3
 LDA STRLTH,X
 ADD -2
 STA PRBAS+2
 SUB -2
 LRA 1
 ADD STRG,X
 STA TEMP
 @LDA -LFCR
 STA *TEMP
 @LDM -PRBAS
 SVC #
 RIM X,X
 BRU ,WS#5
 STRG DATA OPBUF
 DATA #
 STRLTH DATA 24
 DATA #
 PRBAS DATA #
 DATA 2
 DATA #
 DATA #
 BSS 1
 TEMP
 OPBUF DATA * DATA >HHHH',BBCC
 BSS 128-7
 END

1700
 #53A
 #53B
 #53C
 #53D
 #53E
 #53F
 #541
 #542
 #543
 #544
 #545
 #546
 #547
 #548
 #549
 #54A
 #54B
 #54C
 #54D
 #54E
 #54F
 #550
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 #559
 #55A
 #55B
 #55C
 #55D

RESET INDEX
 GET STARTING ADDRESS
 FINISHED
 BUFFER ADDR
 # OF CHARS
 FOR LFCR
 WD OFFSET

BSS #
 LDA STRG,X
 SNZ A
 RMO L,P
 STA PRBAS+3
 LDA STRLTH,X
 ADD -2
 STA PRBAS+2
 SUB -2
 LRA 1
 ADD STRG,X
 STA TEMP
 @LDA -LFCR
 STA *TEMP
 @LDM -PRBAS
 SVC #
 RIM X,X
 BRU ,WS#5
 STRG DATA OPBUF
 DATA #
 STRLTH DATA 24
 DATA #
 PRBAS DATA #
 DATA 2
 DATA #
 DATA #
 BSS 1
 TEMP
 OPBUF DATA * DATA >HHHH',BBCC
 BSS 128-7
 END

1700
 #53A
 #53B
 #53C
 #53D
 #53E
 #53F
 #541
 #542
 #543
 #544
 #545
 #546
 #547
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 #54A
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 #55A
 #55B
 #55C
 #55D

V00205 19/03/84 16:12:38

900A OFF LINE ASSEMBLER
ROUTINES

SHEET 1

1	•	SUBROUTINES
2	•	T198MA
3	•	K: VO
4	•	ISSUE #
5	•	REVISION #
6	•	VA0
7	•	ROUTINES
8	•	DEF B1ZDAS, B1ZHAS, VTC, HAS2B1, GETCHA, BFC, DEC2B1
9	•	DEF HDER0, ACLS#0, M12, ACLSS, M13, HLABL1
10	•	REF IPLN, IPLMS, IPLNX, MRKIP
11	•	
12	•	
13	•	
14	•	

988A OFF LINE ASSEMBLER
ROUTINES

SHEET 2

15	***	PEJ	EQU	1
16	***		EQU	2
17	***		EQU	3
18	A		EQU	4
19	E		EQU	5
20	X		EQU	6
21	M		EQU	7
22	S		EQU	1
23	L		OPD	>CAEB,3
24	B		EQU	>BARD
25	P		EQU	>AJDD
26	***			
27	BR		EQU	1
28	XB		EQU	3
29	SVC		OPD	>CAEB,3
30	LFGR		EQU	>BARD
31	BBCK		EQU	>AJDD

900A OFF LINE ASSEMBLER
ROUTINES

SHEET 3

Address	Code	Operation	Comments
32		PEJ	
33			
34			
35			
36			
37			
38			
39			
40			
41			
42			
43			
44	B12DAS	LDX --5	
45		LDE --5	
46		OLDM -->0000	
47			
48	COMP	OCPL DCONST,X	CONVERSION UNIT
49		SLE	UNIT .LE. VALUE ?
50		BRU NXUNIT	NO
51		OSUB DCONST,X	YES - SUBSTRACT
52		RIM E,E	AND COUNT
53		BRU COMP	KEEP GOING
54			
55			
56			
57			
58			
59			
60			
61			
62			
63			
64			
65			
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82			

980A. OFF LINE ASSEMBLER
ROUTINES

03 *
04 REGD BSS 1
05 VSRETH BSS 1

SHEET 4

980A OFF LINE ASSEMBLER
ROUTINES

SHEET 6

Address	Code	Label	Operation	Comments
06		PEU		
07				CONVERT FROM BINARY TO HEX ASCII
08				ENTRY : (A)-BIN VALUE
09				(E)-1 LEADING ZERO(S) SUPPRESSED
10				RETURN : (A)-2 MS ASCII DIGITS
11				(E)-2 LS ASCII DIGITS
12				(X)-' >
13				(M) DESTROYED
14	BSS	B12HAS		SAVE ZERO SUPPRESSION
15	STE	ZSUPPR		
16	LDA	--4		
17	LDE	--8		
18	OLDM	-->BBBB		
19				
20	HCOMP			CONVERSION UNIT
21	CPL	HCONST,X		UNIT .LE. VALUE ?
22	SLE			NO
23	BRU	B100		YES - SUBSTRACT
24	SUB	HCONST,X		AND: COUNT
25	RIN	E.E		KEEP GOING
26	BRU	HCOMP		
27				
28	REX	E.A		ASSUME (<= 9
29	OLDM	-->BBBB		DIG OR CHAR ?
30	CPA	--A		
31	SLE			DIGIT
32	BRU	B105		CHAR
33	SUB	--A		
34	OLDM	-->B0C1		
35				
36	B105			HEX ASCII
37	RAD	H.A		
38	REX	E.A		RESET (E)
39	STE	ASBUE,X		CONTINUE CONVERSION TO 4 DIGITS
40	LDE	--8		
41	BIX	HCOMP		SUPPRESS LEADING ZERO(S)
42				
43				
44	LDA	ZSUPPR		ZERO SUPPRESSION ?
45	SOU	A		NO
46	BRU	B108		YES
47	LDA	--4		
48	OLDA	-->BBBB		
49				
50	SUPPZE	CPA		ZERO ?
51	SEQ			NO - STOP SUPPRESSION
52	BRU	B108		YES - LAST DIGIT ?
53	SNO	X		YES - LAST ZERO IS NOT SUPPRESSED
54	BRU	B108		NO - SUPPRESS
55	OLDE	-->B0A5		
56				
57				
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152				

IBM OFF LINE ASSEMBLER

SHEET 6

ROUTINES

0053	0AD7	STE	ASBUF-X
0054	0BF7	BIX	SUPPZE
0055	00D3	LDA	ASBUF-2
0056	08C8	LLA	0
0057	20D2	ADD	ASBUF-1
0058	0501	RMO	A.E
0059	00CD	LDA	ASBUF-4
005A	08C8	LLA	0
005B	20CC	ADD	ASBUF-3
005C	0557	RMO	L.P
005D	1000	DATA	>1000
005E	0100	DATA	>100
005F	0010	DATA	>10
0060	0001	DATA	>1
0061		HCONST	BSS
0061		ZSUPPR	BSS 1

2 LS DIGITS

2 MS DIGITS

RETURN

-1 : ZERO SUPPR. REQUESTED

980A OFF LINE ASSEMBLER
ROUTINES

SHEET 7

155	PEJ		
156		CONVERT DEC ASCII STRING INTO BINARY VALUE.	
157		STRING LENGTH IN (X) OR WHEN CR OR BLANK IS ENCOUNTERED,	
158		WHICHEVER COMES FIRST.	
159		- ENTRY (A)= BYTE POINTER TO STRING	
160		(X)= # OF ASCII'S	
161		- RETURN (A)= CONVERTED BINARY VALUE	
162		(X)=-1 ON INVALID CHARACTER	
163			
164			
165			
166	DEC2BI	BSS	RESET ASCII COUNTER
167	LOE	-#	CLEAR RESULT
168	STE	RESULT	IS '1ST CHAR IN LH OR RH BYTE ?
169	TABZ	15	RH
170	LDE	-1	
171	STE	COUNT	FORM WORD OFFSET
172	LRA	1	
173	STA	TEMPTR	
174	RCO	X,X	
175	LDA	COUNT	
176	LRA	1	
177	ADD	TEMPTR	
178	STA	\$+2	
179	LDA	*\$+1	
180	BSS	1	
181	TMBO	15,COUNT	
182	LRA	\$	
183	WARD	>#FFF	
184	OCPA	->#00D	
185	SNE		
186	BRU	DEC#3	
187	OCPA	->#0A#	
188	SNE		
189	BRU	DEC#3	
190			
191		CHECK FOR LEGAL HEX ASCII	
192			
193	OCPA	->#0B#	
194	SLE		
195	BRU	DEC#4	
196	OCPA	->#0B#	
197	SGE		
198	BRU	DEC#4	
199	OSUB	->#0B#	
200	MPY	DCONST,X	
201	RMO	E,A	
202	ADD	RESULT	
203	STA	RESULT	

8862	8863	8864	8865	8866	8867	8868	8869	8870	8871	8872	8873	8874	8875	8876	8877	8878	8879	8880	8881	8882	8883	8884	8885	8886	8887	8888	8889	8890	
8891	8892	8893	8894	8895	8896	8897	8898	8899	8900	8901	8902	8903	8904	8905	8906	8907	8908	8909	8910	8911	8912	8913	8914	8915	8916	8917	8918	8919	8920

8921	8922	8923	8924	8925	8926	8927	8928	8929	8930	8931	8932	8933	8934	8935	8936	8937	8938	8939	8940	8941	8942	8943	8944	8945	8946	8947	8948	8949	8950
8951	8952	8953	8954	8955	8956	8957	8958	8959	8960	8961	8962	8963	8964	8965	8966	8967	8968	8969	8970	8971	8972	8973	8974	8975	8976	8977	8978	8979	8980

300A OFF LINE ASSEMBLER
ROUTINES

000B	003E	204	IMO	COUNT
000C	40DD	205	BIX	DEC.M
000D	003A	206	LDA	RESULT
000E	C057	207	RMO	L.P
000F	17FF	208	LDX	--1
000H	C057	209	RMO	L.P
000I		210		
000J		211		
000K		212		
000L	2710	213	DATA	10000
000M	03E8	214	DATA	1000
000N	0064	215	DATA	100
000O	000A	216	DATA	10
000P	0001	217	DATA	1
000Q		218	DCONST	BSS

GET FINAL RESULT
..RETURN

SET ERROR ON RETURN

Address	Label	Code	Comment
219		PEJ	
220			CONVERT FROM HEX ASCII TO BINARY VALUE
221			ENTRY : (A) POINTER TO ASCII STRING
222			(X) = # OF ASCII
223			
224			
225			RETURN : (A) = CONVERTED BINARY VALUE
226			(X) = -1 ON UNVALID HEX CHARACTER
227			
228	HAS2B1	BSS	
229	STA	TEMPTR	SAVE ASCII POINTER
230	LDA	-#	RESET ASCII COUNTER
231	STA	COUNT	
232	STA	RESULT	CLEAR RESULT
233	RCO	X,X	- # OF ASCII
234	RIM	X,X	
235	RIM	COUNT	
236	LDA	COUNT	FORM WORD OFFSET
237	LRA	1	
238	ADD	TEMPTR	
239	STA	\$+2	GET WORD CONTAINING THE ASCII
240	LDA	\$+1	IF IN LH BYTE
241	BSS	1	
242	TMBO	15,COUNT	
243	LRA	8	THEN SHIFT IT TO RH BYTE
244	ORAND	->#FFF	KEEP THE CURRENT ASCII
245			
246			CHECK FOR LEGAL HEX ASCII
247			'9' <= ASCII <= '9' ?
248	OCPA	->#BBB	
249	SLE	HASB1	NO
250	BRU	HASB1	
251	OCPA	->#BB9	
252	SGE	HASB1	NO
253	BRU	HASB1	YES - GET BINARY VALUE
254	OSUB	->#BBB	
255	BRU	HASB2	
256			
257	HASB1	OCPA	'A' <= ASCII <= 'F' ?
258	SLE	HASB4	NO
259	BRU	HASB4	
260	OCPA	->#BC6	
261	SGE	HASB4	NO
262	BRU	HASB4	YES - GET BINARY VALUE
263	OSUB	->#BC9	
264	ADD	#9	
265	MPY	HCONST,X	TO PLACE RESULT BETWEEN
266	LLA	15	
267			
268			
269			
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980A OFF LINE ASSEMBLER
ROUTINES

88BF	C698	267	RAD	E.A
88C8	2687	268	ADD	RESULT
88C1	8886	269	STA	RESULT
88C2	5887	270	JMO	COUNT
88C3	4808	271	BIX	HAS88
88C4	8883	272	LDA	RESULT
88C5	C567	273	RMO	L.P
		274		
88C6	17FF	275	HAS84	LDX --1
88C7	C557	276	RMO	L.P
		277		
88C8		278	RESULT	BSS 1
88C9		279	TEMPTR	BSS 1
88CA		280	COUNT	BSS 1

...8-FFFF INTO (A)

SET ERROR ON RETURN

RESULT OF CONVERSION
TEMP POINTER
COUNT OF ASCII

SOBA OFF LINE ASSEMBLER
ROUTINES

SHEET 11

281	PEJ		
282	*	GET A CHARACTER FROM A STRING	
283	*		
284	*		
285	*	ENTRY : (M)= START ADDR. OF STRING	
286	*	(A)= BYTE OFFSET WITHIN STRING	
287	*		
288	*	RETURN : (A)= THE CHARACTER, RH-JUSTIFIED	
289	*	ALL OTHER REGISTERS PRESERVED	
290	*		
291	GETCHA	BSS	SAVE BYTE OFFSET
292		STA BOFF	CONVERT TO WORD OFFSET
293		LRA I	FORM POINTER TO CHARACTER
294		RAD H.A	
295		STA TPTR	
296		LDA *TPTR	GET THE CHARACTER
297		TMBO 15,BOFF	...IN RH BYTE
298		LRA B	
299		QAND ->BOFF	
300		RMO L,P	RETURN
301	BOFF	BSS I	BYTE OFFSET
302	TPTR	BSS Y	TEMPORARY POINTER
303			

BBBA	
BBB	
BB41	
BBB	
BBCD	
BBC	
BBCF	
BBDF	
BB01	
BB02	
BB03	
BB04	
BB05	
BB06	
BB07	

980A OFF LINE ASSEMBLER
ROUTINES

SHEET 12

304	PEJ		
305			
306			
307			
308			
309			
310			
311			
312			
313			
314	RFC		
315			
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353			

SUBROUTINE RFC (READ FROM CONSOLE) SETS UP A PRB FOR READING FROM LUN # AND THEN ISSUES THE READ
 ON RETURN (M)-NO. OF CHARS. IN I/P STRING EXCLUSIVE OF THE CR --1 FOR A READ ERROR OR A LINE TOO LONG ALL OTHER REGISTERS ARE PRESERVED
 STA R9+4 SAVE ENTRY (A).(L).(X)
 RMD L.A
 STA R9+5
 STX R9+6
 LDA -80 RESET THE I/P CHAR. COUNT
 STA R9+2
 0LDM -R9 ISSUE THE READ
 SVC # READ ERROR?
 TMBZ 1.R9 YES:
 BRU R4 (A)=I/P CHAR. COUNT
 LDA R9+2 80 CHARS. READ IN?
 CPA -80 NO:
 RDE A.M
 SEQ
 BRU R7
 0LDA IPLNS+39
 CPL ->8D
 SEQ R6
 BRU R7
 LDA -15
 BRU S+2
 LDA -17
 LDH -8
 BRU -VTC000
 DATA VTC
 RMO A.M
 BRU -VTC000
 LDH --1
 RIN M.A
 0LDM IPLNX
 BRU -MRKIP0
 DATA MRKIP

980A OFF LINE ASSEMBLER

SHEET 13

ROUTINES

E	00FC	0000	354
	00FD	C703	355
	00FE	0006	356
	00FF	1007	357
	0100	7005	358
	0101	0400	* R9
	0102	0000	359
	0103	0000	360
	0104	0001	361
E	0105	0000	362
			363

RDE	A,M
LDA	R9+4
LDX	R9+6
BRU	*R9+5
DATA	>400
DATA	0
BSS	1
DATA	IPLMS
BSS	3

RESTORE ENTRY (A), (X)

#	LUN #
1	READ ASCII OP CODE
2	CHAR. COUNT
3	DATA BUFFER ADDR.
4-5	ENTRY (A), (L), (X)

88A OFF LINE ASSEMBLER
ROUTINES

SHEET 14

364	PEJ		
365			
366			
367			
368			
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371			
372			
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401			

SUBROUTINE TO WRITE A MESSAGE TO CONSOLE
 ENTRY: (M): MESSAGE INDEX - ALL REGS PRESERVED
 BSS # SAVE REG FILE
 @SRF REGF2
 REX M,X
 RMO B,S SAVE (B)
 LDA M0,X (A)-MESSAGE STRING ADDRESS
 RMO A,B (B) POINTS TO COUNT + MESSAGE
 LDA B,BR (A)-CHARACTER COUNT
 RIN B,E (E)-START OF MESSAGE
 DST PUTC+2
 /RMO M,X
 @LOM -PUTC CALL SUPERVISOR TO WRITE
 SVC #
 RMO S,B RESTORE (B)
 @LRF REGF2 RESTORE REG FILE
 RMO L,P
 P,R,B
 DATA #
 DATA 2
 BSS 1
 BSS 1
 BSS 8 REGISTER FILE
 DATA M0,M1,M2,M3,M4,M5,M6,M7,M8,M9,M10,M11
 DATA M12,M13,M14,M15,M16
 DATA 2
 DATA LFCR

980A OFF LINE ASSEMBLER

ROUTINES

#138	#002	DATA 2
#139	ABAB	DATA
#13A	#00C	DATA 12
#13B	C1C2	DATA 'ABL EDITOR',LFCR
#13C	CCAB	
#13D	C5C4	
#13E	C9D4	
#13F	CFD2	
#140	8A8D	
#141	#00C	DATA 12
#142	C6C9	DATA 'FILE NAME >
#143	CCC5	
#144	A9CE	
#145	C1CD	
#146	C5AB	
#147	BEAB	
#148	#035	
#149	CDC1	
#14A	C3C8	
#14B	C9CE	
#14C	C5AB	
#14D	D3D0	
#14E	C5C3	
#14F	C9C6	
#150	C9C3	
#151	C1D4	
#152	C9CF	
#153	CEAB	
#154	8A8D	
#155	#01C	DATA 28
#156	ABAB	DATA 'ENTER SUBROUTINE NAMES',LFCR
#157	A9C5	
#158	CE04	
#159	C5D2	
#15A	A9D3	
#15B	D5C2	
#15C	D2CF	
#15D	D5D4	
#15E	C9CE	
#15F	C5AB	
#160	CEC1	
#161	CDCS	
#162	D3FF	
#163	8A8D	
#164	#002	DATA 2
#165	ABAB	DATA
#166	#018	DATA 24
#167	ABAB	DATA
#168	A9CE	NUMBER OF CLUSTERS >
#169	D5CD	
#16A	C2C9	
#16B	D2AB	
#16C	CFC6	
#16D	A9C3	
#16E	CG05	

988A OFF LINE ASSEMBLER
ROUTINES

ROUTINES	417 M7	DATA 28 DATA :	NUMBER OF ALTERNATIVES >
#16F	D3D4		
#170	C5D2		
#171	D3A8		
#172	A8BE		
#173	#81C		
#174	A8A8		
#175	A8CE		
#176	D5CD		
#177	C2C5		
#178	D2A8		
#179	CFC6		
#17A	A8C1		
#17B	CCD4		
#17C	C5D2		
#17D	CEC1		
#17E	D4C9		
#17F	D6C5		
#180	D3A8		
#181	A8BE		
#182	#828		
#183	A8A8		
#184	A8C1		
#185	CCD4		
#186	C5D2		
#187	CEC1		
#188	D4C9		
#189	D6C5		
#18A	A8D3		
#18B	D6C5		
#18C	C3C9		
#18D	C6C9		
#18E	C3C1		
#18F	D4C9		
#190	CFCE		
#191	A88A		
#192	8A8D		
#193	#818		
#194	C5D2		
#195	D2CF		
#196	D2A8		
#197	CFCE		
#198	A8C6		
#199	C9CC		
#19A	C5A8		
#19B	CFD8		
#19C	C5CE		
#19D	C9CE		
#19E	C7A8		
#19F	8A8D		
#1A0	#804		
#1A1	A8A8		
#1A2	A8BE		
#1A3	#816		
#1A4	C9CD		
#1A5	D8D2		
#1A6	CFD8		

419 M8 DATA 32
420 DATA : ALTERNATIVE SPECIFICATION : ',LFCR

421 M9 DATA 24
422 DATA 'ERROR ON FILE OPENING ',LFCR

423 M10 DATA 4
424 DATA : >

425 M11 DATA 22
426 DATA 'IMPROPER INPUT LINE',LFCR

980A OFF LINE ASSEMBLER

SHEET 17

ROUTINES
 #1A7 C5D2
 #1A8 A#C9
 #1A9 CED#
 #1AA D5D4
 #1AB A#CC
 #1AC C9CE
 #1AD C5FF
 #1AE 8A8D
 #1AF #24
 #1B# C1C2
 #1B1 CCA#
 #1B2 D#D2
 #1B3 CFC7
 #1B4 D2C1
 #1B5 CDA#
 #1B6 C3D2
 #1B7 C5C1
 #1B8 DAC5
 #1B9 CAA#
 #1BA C9CE
 #1BB A#C6
 #1BC C9CC
 #1BD C5A#
 #1BE CDC#
 #1BF C9D2
 #1C# D2D3
 #1C1 8A8D
 #1C2 #B#6
 #1C3 A#A#
 #1C4 A#A#
 #1C5 A#BE
 #1C6 #18
 #1C7 D#D2
 #1C8 CFC7
 #1C9 D2C1
 #1CA CDA#
 #1CB D3D#
 #1CC C5C3
 #1CD C9C6
 #1CE C9C3
 #1CF C1D4
 #1D# C9CF
 #1D1 CE#
 #1D2 8A8D
 #1D3 #14
 #1D4 A#A#
 #1D5 A#D#
 #1D6 D2CF
 #1D7 C7D2
 #1D8 C1C0
 #1D9 A#CE
 #1DA D5CD
 #1DB C2C5
 #1DC D2A#
 #1DD 8E#
 #1DE 8A8D

427 M12 DATA 36
 428 DATA 'ABL PROGRAM CREATED IN FILE MMIRRS',LFCR

429 M13 DATA 6
 43# DATA '>'

431 M15 DATA 24
 432 DATA 'PROGRAM SPECIFICATION ',LFCR

433 M16 DATA 2#
 434 DATA ' PROGRAM NUMBER > ',LFCR

980A OFF LINE ASSEMBLER
ROUTINES

```

435 * PEJ
436 * HEADERS
437 *
438 *
439 * HDER# BSS #
440 * HLABL1 DATA 'PROGNO DATA #',BBCR

441 * DATA 'PCLUST DATA #',BBCR

442 * DATA 'PALT DATA #',BBCR

443 * DATA 'PACT DATA #',BBCR

444 * DATA 'PACTOF DATA #',BBCR

445 * DATA 'ACLSTP DATA #',BBCR

#1DF DDD2
#1E0 CFC7
#1E1 CECF
#1E2 AJC4
#1E3 C1D4
#1E4 C1A8
#1E5 BFA8
#1E6 AFA8
#1E7 AFA8
#1E8 AFA8
#1E9 DFC3
#1EA CCO5
#1EB D3D4
#1EC AFA8
#1ED C1D4
#1EE C1A8
#1EF BFA8
#1F0 AFA8
#1F1 AFA8
#1F2 AFA8
#1F3 DFC1
#1F4 CCO4
#1F5 AFA8
#1F6 AFA8
#1F7 C1D4
#1F8 C1A8
#1F9 BFA8
#1FA AFA8
#1FB AFA8
#1FC AFA8
#1FD DFC1
#1FE C3D4
#1FF AFA8
#200 AFA8
#201 C1D4
#202 C1A8
#203 BFA8
#204 AFA8
#205 AFA8
#206 AFA8
#207 DFC1
#208 C3D4
#209 CEC6
#210 AFA8
#211 C1C3

```

380A OFF LINE ASSEMBLER
ROUTINES

#212	CCD3	
#213	D4C#	
#214	A#C4	
#215	C1D4	
#216	C1A#	
#217	B#A#	
#218	A#A#	
#219	A#A#	
#21A	A#D	
#21B	D3D6	DATA 'SVECTR DATA #', .BB#CR
#21C	C5C3	
#21D	D4D2	
#21E	A#C4	
#21F	C1D4	
#22#	C1A#	
#221	B#A#	
#222	A#A#	
#223	A#A#	
#224	A#D	
#225	C3CC	DATA 'CLSM#O DATA CLSMAT', .BB#CR
#226	D3CD	
#227	C1C#	
#228	A#C4	
#229	C1D4	
#22A	C1A#	
#22B	C3CC	
#22C	D3CD	
#22D	C1D4	
#22E	A#D	
#22F	C#C3	DATA 'NCLM#O DATA NCLMAT', .BB#CR
#23#	C#C#	
#231	C1C#	
#232	A#C4	
#233	C1D4	
#234	C1A#	
#235	C#C3	
#236	C#C#	
#237	C1D4	
#238	A#D	
#239	D9CE	DATA 'Y#P#A#O DATA Y#P#A#T', .BB#CR
#23A	D#C#	
#23B	C1C#	
#23C	A#C4	
#23D	C1D4	
#23E	C1A#	
#23F	D9CE	
#24#	D#C#	
#241	C1D4	
#242	A#D	
#243	C#D#	
#244	D2CD	DATA 'H#P#A#O DATA H#P#A#T', .BB#CR
#245	C1C#	
#246	A#C4	
#247	C1D4	
#248	C1A#	
#249	C#D#	

988A OFF LINE ASSEMBLER

ROUTINES

#24A D2CD
 #24B C1D4
 #24C A5FD
 #24D C1D8
 #24E D4D2
 #24F B9CB
 #250 A5C4
 #251 C1D4
 #252 C1A8
 #253 C1D8
 #254 D4D2
 #255 B9AB
 #256 A5FD
 #257 C2D2
 #258 C5C7
 #259 A5AB
 #25A A5C4
 #25B C1D4
 #25C C1A8
 #25D B9AB
 #25E A5AB
 #25F A5AB
 #260 A5FD
 #261 CC02
 #262 C5C7
 #263 A5AB
 #264 A5C4
 #265 C1D4
 #266 C1A8
 #267 B9AB
 #268 A5AB
 #269 A5AB
 #26A A5FD
 #26C A5AB
 #26D A5AB
 #26E A5AB
 #26F A5AB
 #270 A5AB
 #271 A5AB
 #272 A5AB
 #273 A5AB
 #274 A5FD
 #275 A5C4
 #276 C1D4
 #277 C1A8
 #278 C1C3
 #279 CC03
 #27A B9AB
 #27B A5FD
 #27C A5C4
 #27D C1D4
 #27E C1A8
 #27F C1C3
 #280 CC03

451 DATA 'APTR00 DATA APTR0' ,BBCR

452 DATA 'BREG DATA B' ,BBCR

453 DATA 'LREG DATA L' ,BBCR

454 DATA '' ,BBCR

455
456 'ACLS00 DATA ' DATA ACLS00' ,BBCR

457 DATA ' DATA ACLS01' ,BBCR

988A OFF LINE ASSEMBLER
ROUTINES

#281	B/B1		
#282	A/B/D		
#283	A/C4	458	DATA DATA ACLS02',B
#284	C104		
#285	C1A/B		
#286	C1C3		
#287	CCD3		
#288	B/B2		
#289	A/B/D	459	DATA DATA ACLS03',BBCR
#28A	A/C4		
#28B	C104		
#28C	C1A/B		
#28D	C1C3		
#28E	CCD3		
#28F	B/B3		
#290	A/B/D		
#291	A/C4	460	DATA DATA ACLS04',BBCR
#292	C104		
#293	C1A/B		
#294	C1C3		
#295	CCD3		
#296	B/B4		
#297	A/B/D		
#298	A/C4	461	DATA DATA ACLS05',BBCR
#299	C104		
#29A	C1A/B		
#29B	C1C3		
#29C	CCD3		
#29D	B/B5		
#29E	A/B/D		
#29F	A/C4		
#2A0	C104	462	DATA DATA ACLS06',BBCR
#2A1	C1A/B		
#2A2	C1C3		
#2A3	CCD3		
#2A4	B/B6		
#2A5	A/B/D		
#2A6	A/C4		
#2A7	C104	463	DATA DATA ACLS07',BBCR
#2A8	C1A/B		
#2A9	C1C3		
#2AA	CCD3		
#2AB	B/B7		
#2AC	A/B/D		
#2AD	A/C4		
#2AE	C104	464	DATA DATA ACLS08',BBCR
#2AF	C1A/B		
#2B0	C1C3		
#2B1	CCD3		
#2B2	B/B8		
#2B3	A/B/D		
#2B4	A/C4		
#2B5	C104	465	DATA DATA ACLS09',BBCR
#2B6	C1A/B		
#2B7	C1C3		
#2B8	CCD3		

8080 OFF LINE ASSEMBLER
ROUTINES

#289	ASND		
#28A	AMC4		
#28B	C1D4	466	DATA DATA ACLS11',BBCR
#28C	C1A8		
#28D	C1C3		
#28E	CCD3		
#28F	B1B1		
#2C1	ASND	467	DATA DATA ACLS11',BBCR
#2C2	AMC4		
#2C3	C1D4		
#2C4	C1A8		
#2C5	C1C3		
#2C6	CCD3		
#2C7	B1B1		
#2C8	ASND		
#2C9	AMC4	468	DATA DATA ACLS12',BBCR
#2CA	C1D4		
#2CB	C1A8		
#2CC	C1C3		
#2CD	CCD3		
#2CE	B1B2		
#2CF	ASND		
#2D0	AMC4	469	DATA DATA ACLS13',BBCR
#2D1	C1D4		
#2D2	C1A8		
#2D3	C1C3		
#2D4	CCD3		
#2D5	B1B3		
#2D6	ASND		
#2D7	AMC4	470	DATA DATA ACLS14',BBCR
#2D8	C1D4		
#2D9	C1A8		
#2DA	C1C3		
#2DB	CCD3		
#2DC	B1B4		
#2DD	ASND		
#2DE	AMC4	471	DATA DATA ACLS15',BBCR
#2DF	C1D4		
#2E0	C1A8		
#2E1	C1C3		
#2E2	CCD3		
#2E3	B1B5		
#2E4	ASND		
#2E5	AMC4	472	DATA DATA ACLS16',BBCR
#2E6	C1D4		
#2E7	C1A8		
#2E8	C1C3		
#2E9	CCD3		
#2EA	B1B6		
#2EB	ASND		
#2EC	AMC4	473	DATA DATA ACLS17',BBCR
#2ED	C1D4		
#2EE	C1A8		
#2EF	C1C3		
#2F0	CCD3		

900A OFF LINE ASSEMBLER ROUTINES

#2F1	B1B7	
#2F2	A#D	
#2F3	A#C4	474 DATA ' DATA ACLS18',BBCR
#2F4	C1D4	
#2F5	C1A#	
#2F6	C1C3	
#2F7	CCD3	
#2F8	B1B8	
#2F9	A#D	
#2FA	A#C4	475 DATA ' DATA ACLS19',BBCR
#2FB	C1D4	
#2FC	C1A#	
#2FD	C1C3	
#2FE	CCD3	
#2FF	B1B9	
#3#	A#D	
#3#1	A#C4	476 DATA ' DATA ACLS2#',BBCR
#3#2	C1D4	
#3#3	C1A#	
#3#4	C1C3	
#3#5	CCD3	
#3#6	B2B#	
#3#7	A#D	
#3#8	A#C4	477 DATA ' DATA ACLS21',BBCR
#3#9	C1D4	
#3#A	C1A#	
#3#B	C1C3	
#3#C	CCD3	
#3#D	B2B1	
#3#E	A#D	
#3#F	A#C4	478 DATA ' DATA ACLS22',BBCR
#31#	C1D4	
#311	C1A#	
#312	C1C3	
#313	CCD3	
#314	B2B2	
#315	A#D	
#316	A#C4	479 DATA ' DATA ACLS23',BBCR
#317	C1D4	
#318	C1A#	
#319	C1C3	
#31A	CCD3	
#31B	B2B3	
#31C	A#D	
#31D	A#C4	48# DATA ' DATA ACLS24',BBCR
#31E	C1D4	
#31F	C1A#	
#32#	C1C3	
#321	CCD3	
#322	B2B4	
#323	A#D	
#324	A#C4	481 DATA ' DATA ACLS25',BBCR
#325	C1D4	
#326	C1A#	
#327	C1C3	
#328	CCD3	

900A OFF LINE ASSEMBLER
ROUTINES

#329	B2B5		
#32A	A99D		
#32B	A9C4	482	DATA ' DATA ACLS26',BBCR
#32C	C1D4		
#32D	C1A9		
#32E	C1C3		
#32F	CCD3		
#330	B2B6		
#331	A99D	483	DATA ' DATA ACLS27',BBCR
#332	A9C4		
#333	C1D4		
#334	C1A9		
#335	C1C3		
#336	CCD3		
#337	B2B7		
#338	A99D		
#339	A9C4	484	DATA ' DATA ACLS28',BBCR
#33A	C1D4		
#33B	C1A9		
#33C	C1C3		
#33D	CCD3		
#33E	B2B8		
#33F	A99D		
#340	A9C4	485	DATA ' DATA ACLS29',BBCR
#341	C1D4		
#342	C1A9		
#343	C1C3		
#344	CCD3		
#345	B2B9		
#346	A99D		
#347	A9C4	486	DATA ' DATA ACLS30',BBCR
#348	C1D4		
#349	C1A9		
#34A	C1C3		
#34B	CCD3		
#34C	B3B9		
#34D	A99D		
#34E	A9C4	487	DATA ' DATA ACLS31',BBCR
#34F	C1D4		
#350	C1A9		
#351	C1C3		
#352	CCD3		
#353	B3B1		
#354	A99D		
#355	C1C3	488	ACLBSS DATA 'ACLSB0 BSS #',BBCR
#356	CCD3		
#357	B3B9		
#358	A9C2		
#359	D3D3		
#35A	A99D		
#35B	A99D		
#35C	C1C3	489	DATA 'ACLS#1 BSS #',BBCR
#35D	CCD3		
#35E	B3B1		
#35F	A9C2		
#360	D3D3		

988A OFF LINE ASSEMBLER
ROUTINES

#361	AFBB	
#362	AFBD	
#363	C1C3	
#364	CCD3	
#365	BFB2	
#366	ASC2	
#367	D3D3	
#368	AFBB	
#369	AFBD	
#36A	C1C3	DATA 'ACLSB2 BSS #',BBCR
#36B	CCD3	
#36C	BFB3	
#36D	ASC2	
#36E	D3D3	
#36F	AFBB	
#370	AFBD	
#371	C1C3	DATA 'ACLSB3 BSS #',BBCR
#372	CCD3	
#373	BFB4	
#374	ASC2	
#375	D3D3	
#376	AFBB	
#377	AFBD	
#378	C1C3	DATA 'ACLSB5 BSS #',BBCR
#379	CCD3	
#37A	BFB5	
#37B	ASC2	
#37C	D3D3	
#37D	AFBB	
#37E	AFBD	
#37F	C1C3	DATA 'ACLSB6 BSS #',BBCR
#380	CCD3	
#381	BFB6	
#382	ASC2	
#383	D3D3	
#384	AFBB	
#385	AFBD	
#386	C1C3	DATA 'ACLSB7 BSS #',BBCR
#387	CCD3	
#388	BFB7	
#389	ASC2	
#38A	D3D3	
#38B	AFBB	
#38C	AFBD	
#38D	C1C3	DATA 'ACLSB8 BSS #',BBCR
#38E	CCD3	
#38F	BFB8	
#390	ASC2	
#391	D3D3	
#392	AFBB	
#393	AFBD	
#394	C1C3	DATA 'ACLSB9 BSS #',BBCR
#395	CCD3	
#396	BFB9	
#397	ASC2	
#398	D3D3	

988A OFF LINE ASSEMBLER

ROUTINES

#399	ASB		
#39A	ASD		
#39B	C1C3	498	DATA 'ACLS10 BSS #',BBGR
#39C	CCD3		
#39D	B1B		
#39E	ASC2		
#39F	D3D3		
#3A0	ASB		
#3A1	ASD	499	DATA 'ACLS11 BSS #',BBGR
#3A2	C1C3		
#3A3	CCD3		
#3A4	B1B1		
#3A5	ASC2		
#3A6	D3D3		
#3A7	ASB		
#3A8	ASD	500	DATA 'ACLS12 BSS #',BBGR
#3A9	C1C3		
#3AA	CCD3		
#3AB	B1B2		
#3AC	ASC2		
#3AD	D3D3		
#3AE	ASB		
#3AF	ASD	501	DATA 'ACLS13 BSS #',BBGR
#3B0	C1C3		
#3B1	CCD3		
#3B2	B1B3		
#3B3	ASC2		
#3B4	D3D3		
#3B5	ASB		
#3B6	ASD	502	DATA 'ACLS14 BSS #',BBGR
#3B7	C1C3		
#3B8	CCD3		
#3B9	B1B4		
#3BA	ASC2		
#3BB	D3D3		
#3BC	ASB		
#3BD	ASD	503	DATA 'ACLS15 BSS #',BBGR
#3BE	C1C3		
#3BF	CCD3		
#3C0	B1B5		
#3C1	ASC2		
#3C2	D3D3		
#3C3	ASB		
#3C4	ASD	504	DATA 'ACLS16 BSS #',BBGR
#3C5	C1C3		
#3C6	CCD3		
#3C7	B1B6		
#3C8	ASC2		
#3C9	D3D3		
#3CA	ASB		
#3CB	ASD	505	DATA 'ACLS17 BSS #',BBGR
#3CC	C1C3		
#3CD	CCD3		
#3CE	B1B7		
#3CF	ASC2		
#3D0	D3D3		

888A OFF LINE ASSEMBLER

ROUTINES			
#301	ASB#		
#302	AJ#D		
#303	C1C3	DATA 'ACLS18 BSS #',BB#C	
#304	CCD3		
#305	B1B8		
#306	AJ#C2		
#307	D3D3		
#308	AJ#B#		
#309	AJ#D		
#30A	C1C3	DATA 'ACLS19 BSS #',BB#C	
#30B	CCD3		
#30C	B1B9		
#30D	AJ#C2		
#30E	D3D3		
#30F	AJ#B#		
#3E#	AJ#D		
#3E1	C1C3	DATA 'ACLS20 BSS #',BB#C	
#3E2	CCD3		
#3E3	B2B#		
#3E4	AJ#C2		
#3E5	D3D3		
#3E6	AJ#B#		
#3E7	AJ#D		
#3E8	C1C3	DATA 'ACLS21 BSS #',BB#C	
#3E9	CCD3		
#3EA	B2B1		
#3EB	AJ#C2		
#3EC	D3D3		
#3ED	AJ#B#		
#3EE	AJ#D		
#3EF	C1C3	DATA 'ACLS22 BSS #',BB#C	
#3F#	CCD3		
#3F1	B2B2		
#3F2	AJ#C2		
#3F3	D3D3		
#3F4	AJ#B#		
#3F5	AJ#D		
#3F6	C1C3	DATA 'ACLS23 BSS #',BB#C	
#3F7	CCD3		
#3F8	B2B3		
#3F9	AJ#C2		
#3FA	D3D3		
#3FB	AJ#B#		
#3FC	AJ#D		
#3FD	C1C3	DATA 'ACLS24 BSS #',BB#C	
#3FE	CCD3		
#3FF	B2B4		
#4#	AJ#C2		
#4#1	D3D3		
#4#2	AJ#B#		
#4#3	AJ#D		
#4#4	C1C3	DATA 'ACLS25 BSS #',BB#C	
#4#5	CCD3		
#4#6	B2B5		
#4#7	AJ#C2		
#4#8	D3D3		

986A OFF LINE ASSEMBLER.
ROUTINES

SHEET 28

514	DATA 'ACLS26 BSS #',BBCR
515	DATA 'ACLS27 BSS #',BBCR
516	DATA 'ACLS28 BSS #',BBCR
517	DATA 'ACLS29 BSS #',BBCR
518	DATA 'ACLS30 BSS #',BBCR
519	DATA 'ACLS31 BSS #',BBCR
520	END

5409 ABB#
5410 ABB#
5411 ABB#
5412 C1C3
5413 CCD3
5414 B2B6
5415 ABB#
5416 ABB#
5417 ABB#
5418 ABB#
5419 C1C3
5420 ABB#
5421 ABB#
5422 ABB#
5423 ABB#
5424 ABB#
5425 ABB#
5426 ABB#
5427 C1C3
5428 CCD3
5429 B3B1
5430 ABB#
5431 ABB#
5432 ABB#
5433 ABB#
5434 ABB#

986A OFF LINE ASSEMBLER
ROUTINES

A 9866
 B 9867
 B12DAS 9868
 COUNT 9869
 DEC281 9870
 HAS281 9871
 HDER0 9872
 L 9873
 M10 9874
 M15 9875
 M5 9876
 M6 9877
 M125 9878
 PACK 9879
 R9 9880
 S 9881
 TPTR 9882
 R X8 9883

ACLS00 9275
 9100 9276
 BOFF 9277
 DECMA 9278
 GETCHA 9279
 HAS281 9280
 IPLN 9281
 R M 9282
 M12 9283
 M2 9284
 M7 9285
 MRKIP0 9286
 R4 9287
 RECFC2 9288
 SUPZER 9289
 WTC 9290
 91AF 9291
 91A3 9292
 91D3 9293
 9166 9294
 9173 9295
 92FC 9296
 92EF 9297
 911D 9298
 9113 9299
 9100 9300

ASBUF 9228
 9105 9229
 BR 9230
 DEC03 9231
 HAS00 9232
 HCOMP 9233
 IPLNX 9234
 M0 9235
 M13 9236
 M3 9237
 M8 9238
 NXUNIT 9239
 R6 9240
 RESULT 9241
 SVC 9242
 WTC000 9243
 9241 9244
 9280 9245
 929C 9246
 9232 9247
 9202 9248
 9136 9249
 91C2 9250
 9141 9251
 9182 9252
 920C 9253
 92F1 9254
 92C8 9255
 CAEM 9256
 92F4 9257
 9228 9258
 9241 9259
 9280 9260
 929C 9261
 9232 9262
 9202 9263
 9136 9264
 91C2 9265
 9141 9266
 9182 9267
 920C 9268
 92F1 9269
 92C8 9270
 CAEM 9271
 92F4 9272

SHEET 29

R ASBUFR 9225
 B108 9226
 COMP 9227
 DEC04 9228
 HAS01 9229
 HCONST 9230
 IPLNS 9231
 M1 9232
 M14 9233
 M4 9234
 M9 9235
 P 9236
 R7 9237
 RFC 9238
 TEMPTR 9239
 X 9240

NO ERRORS NO WARNINGS
 V00285 19/03/84 16:14:35

T4066 19/03/84 16:06:16

980A OFF LINE ASSEMBLER
ABL INTERPRETOR

SHEET 1

ABL INTERPRETOR
T1987A

K. VO

ISSUE #
REVISION #

T5#

HED ABL INTERPRETOR
DEF ABLENT,ABLRET

REF B12DAS,B12HAS,B12OAS,HAS2B1

BRS ABLPRO

DEBUG = # : ENABLE DEBUGGING MESSAGES
1 : DISABLE

EQU 1

- 1
- 2
- 3
- 4
- 5
- 6
- 7
- 8
- 9
- 10
- 11
- 12
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- 14
- 15
- 16
- 17
- 18
- 19
- 20

P #134

#001

988A OFF LINE ASSEMBLER
ABL INTERPRETOR

21	###	PEJ	
22	###	EQU	1
23	###	EQU	2
24	###	EQU	3
25	###	EQU	4
26	###	EQU	5
27	###	EQU	6
28	###	EQU	7
29	###	EQU	1
30	###	OPD	>CAER.3
31	###	EQU	>8ABD
32	###		
33	###		
34	###		
35	###		
36	###		

PEJ

ABL EXECUTIVE

THIS EXECUTIVE IS CALLED TO EXECUTE PAIR OF
ABL PROGRAM-MACHINE. AND EXIT FROM TO/FROM
ALL ABL PROGRAMS.

ENTRY : (B) POINT TO THE CALLING ABL PROGRAM
(M) POINT TO THE CALLED ABL PROGRAM

RETURN: (B), (S) ARE RESTORED
RETURN ADDRESS IS THE SAVED CONTENT OF (L)

ABSENT REX M,B (B) POINTS TO NEW PROGRAM

REX A.M SAVE CALLING PROGRAM

STA BREG

REX A.M

REX A.L

STA LREG

REX A.L

IF

LDA PROGNO

QBRL B12DAS

OSTE M2+6

LDM -2

QBRL VTC

LDA SVECTR

QBRL B12HAS

QDST M8+11

LDM -8

QBRL VTC

BSS

LDA

STA

STA

KDA

STA

BRU

37 *

38 *

39 *

40 *

41 *

42 *

43 *

44 *

45 *

46 *

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908A OFF LINE ASSEMBLER
ABL INTERPRETOR

SHEET 4

83	PEJ		
84			
85			
86		RETURN TO THE PREVIOUS ABL PROGRAM-MACHINE	
87		FROM THE CURRENT ONE.	
88		THIS ROUTINE IS CALLED WHEN ABL EXECUTION IS COMPLETED	
89		THIS ROUTINE RESTORES (B) & (S) TO THE PREVIOUS	
90		ABL PROGRAM-MACHINE WHICH ARE STORED IN THE CURRENT	
91		PROGRAM	
92			
93	ABLRET	BSS	
94		IF	DEBUG.1,MSGE2
95		LDA	PROGNO *FOR DEBUGGING
96		OBRL	B12DAS *
97		OSTE	M18+11 *
98		LDM	-1A *
99		OBRL	VTC *
100		BYS	
101	MSGE2	LDM	LREG RETURN ADDR
102		RMO	M,L
103			
104		LDM	BREG
105		RMO	M,B (B)
106			
107		RMO	L,P.
108			RETURN TO THE CALLING ABL

980A OFF LINE ASSEMBLER
ABL INTERPRETOR

SHEET - 5

Address	Code	Operation	Comments
109		PEJ	
110			
111			
112			
113			
114			
115			
116			
117	PRDEVA	BSS	
118	LDA	PALT	ALT JUST FINISHED
119	RMO	A,E	
120	LRA	1	FORM WORD OFFSET IN NEXT CLUSTER MAT
121	LDX	MCLMAG	START ADDR OF NEXT CL. MAT.
122	RAD	X,A	
123	STA	NXTCLR	
124	NXTCLR	BSS	
125	LDA	NXTCLR	WORD CONTAINING NEXT CLUSTER FOR ALT
126	STA	PCLUST	UPDATE NEW CLUSTER
127			
128			
129			
130			
131	CC98		EXIT FROM THIS ABL PROGRAM
132	78F2		IF NEXT CLUSTER #
133			
134	C788		
135	8987		
136	C8C1		
137	C898		
138	8888		
139	84FE		
140	8A5A		
141			
142			
143			
144			
145			
146	1788		
147	8858		
148	178F		
149	9888		
150			
151			
152	8854		
153	1855		
154	C882		
155	9881		
156	7888		
157			
158	7813		
159			
160			
161			
162	18F7		
163	8189		
164	C8A8		

PREDICATE EVALUATION

THIS ROUTINE EVALUATES CLUSTER, NEXT CLUSTER AND NEXT CLUSTER MATRICES TO FIND OUT THE NEXT ALTERNATIVE TO BE EXECUTED, IF ANY.

PRDEVA BSS
LDA PALT
RMO A,E
LRA 1
LDX MCLMAG
RAD X,A
STA NXTCLR
NXTCLR BSS
LDA NXTCLR
STA PCLUST

EXIT FROM THIS ABL PROGRAM
IF NEXT CLUSTER #

SNZ BRU
RDE A,A
LDE CLSMAG
LLA 1
RAD E,A
STA TPTER
BSS 1
DLD TPTER
DST CLWDI

ADJUSTMENT (1ST CLR IS 1)
START. ADR OF CLUSTER MAT
EACH CLUSTER TAKES 2 WORDS
ADDR OF NEXT CLUSTER

TPYER
DLD TPTER
DST CLWDI

FIND OUT ALL POSSIBLE ALTERNATIVES
IN THIS CLUSTER.

LDX SECHFT
STX -15
L858
STX POSCNT
BSS 1

RESET FLAG FOR 16 RH BITS
BITS 8-15
BIT POSITION

CLVDI
TABOB#
LDM M,X
STX TABONE
BRU S+1

(A) - BITS IN CLUSTER
TABO INSTRUCTION
SPECIFY BIT# IN TABO
TABO (POSCNT)

IS THIS ALT POSSIBLE IN THIS CLUSTER ?
NO - CHECK NEXT ALT
YES

CHECK IF PREDICATES MATCH THIS ALTERNATIVE

LDX POSCNT
LDA YNPMAG
RAD X,A

(X)-ALT #
START ADR OF Y/M PREDICATE MATRIX

988A OFF LINE ASSEMBLER
 ABL INTERPRETOR

165	TMNZ B,SECHFT	ADJUSTMENT FOR ALTS 16-31
166	ADD -16	POINT TO PREDICATE OF THIS ALT
167	STA YNPRP	START ADR OF PRED MASK MATR:IX
168	LDA MPRMAG	ADJUSTMENT FOR ALTS 16-31
169	RAD X,A	
170	TMNZ B,SECHFT	
171		
172	ADD -16	POINT TO PRED MASK FOR ALT
173	STA MPRPT	PREDICATES
174	LDA SVECTR	PREDICATES FOR THIS ALT
175	LDE YNPRP	COMPARE
176	REQ E,A	MASK OUT DONT CARE: 8
177	AND MPRPT	PREDICATES MATCHED ?
178	SNZ A	YES - ALT FOUND
179	BRU L12B	NO - CHECK NEXT ALT TO THE LH
180	DHT POSCNT	
181	BRU S+1	
182	LDX POSCNT	ALL BITS CHECKED ?
183	SOO X	NO
184	BRU WXTBIT	YES - ALL 32 BITS CHECKED ?
185	TMNZ B,SECHFT	
186	BRU L11B	YES
187	LDA CLVD2	NO - CHECK NEXT 16 BITS
188	STA CLVD1	
189	SMBO B,SECHFT	16 RH BITS BEING CHECKED
190	BRU L05B	GO CHECK

900A OFF LINE ASSEMBLER
ABL INTERPRETOR

191	PEJ				
192		ALTERNATIVE FOUND			
193					
194					
195	L12#				
196	BSS	DEBUG.1,MSCE3			
197	IF	SVECTE	'DEBUGGING		
198	OBRL	B1ZHAS			
199	ODST	M8+11			
200	LDM	-8	'STATE VECTOR....'		
201	OBRL	VTC			
202	BSS		CALCULATE .ALT#		
203	LDA	POSCMT			
204	TM8Z	#,SECHFT			
205	ADD	-16	ADJUSTMENT FOR ALT 16-31		
206	STA	PALT	EXECUTE ALTERNATIVE		
207	BRU	ALTEXE			
208			NO ALTERNATIVE FOUND - OUTPUT ERROR MESSAGE		
209					
210	LDM	-7	'NO ALTERNATIVE FOUND'		
211	OBRL	VTC			
212					
213	LDA	SVECTR	OUTPUT LAST STATUS OF PRED		
214	OBRL	B1ZHAS			
215	ODST	M8+11			
216	LDM	-8	'STATE VECTOR : NNNN'		
217	OBRL	VTC			
218	LDA	PALT	OUTPUT LAST ALT #		
219	BRL	#B1ZAS#			
220	DATA	B1ZDAS			
221	OSTE	M9+16			
222	LDM	-9	'LAST ALT EXECUTED NN'		
223	OBRL	VTC			
224	SVC	5	TERMINATE UTILITY		

Address	Label	Code	Comments
225		PEJ	EXECUTE ALTERNATIVE
226			INPUT : PALT
227			DEBUG, I, MSGE4 "FOR DEBUGGING"
228			PALT
229			OBRL B12DAS
230			OSTE M4+12
231	ALTEXE	BSS	LDM -4
232		IF	OBRL VTC
233		LDA	BSS
234		LDA	LDE PALT
235		OSTE	LDA APTRO
236		LDM	A E
237		OBRL	STE TEMR
238	MSGE4	BSS	BSS 1
239		LDE	LDE TEMR
240		LDA	LDE "TEMPR"
241		RAD	STE ACLST
242		STE	LDA -#
243	TEMPR	BSS	STA PACTOF
244		LDE	EXECUTE ACTIONS IN ALTERNATIVE
245		STE	ACTION OFFSET
246		LDA	START ADDR OF ACTION LIST
247		STA	EXECUTE 1ST ACTION IN ALT
248			
249			
250	L13#	LDX	START OF ACTION LIST
251		LDA	ACTION ADDRESS
252		LDA	END OF ACTION LIST ?
253		STA	YES - GO EVALUATE PREDICATES
254	ACTLST	BSS	NO - FOR INDIRECT ADDR.
255			"ORDER OF ACTION WITHIN ALT."
256			"FOR DEBUGGING"
257			"ENTER ALT #...."
258			"NO - BUMP ACTION OFFSET"
259			EXECUTE ACTION
260			GO PREPARE FOR NEXT ACTION
261			
262			
263			
264			
265			
266			
267			
268			
269			
270			
271			
272			
273			
274			
275	CLVD1	BSS	TEMPORARY DATA & POINTERS
276	CLVD2	BSS	16 LH BITS OF CLUSTER
277	TABO#	TABO	RH INSTRUCTION
278	SECHFT	DATA	BIT#-1 : 16 RH BITS OF CLUSTER PROCESSED
279	YNPRP	BSS	POINTER TO PREDICATE FOR ALT
280	MPRPT	BSS	PRED MASK
8869			
8870			
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887A			
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980A OFF LINE ASSEMBLER

ABL INTERPRETOR

0003

P 0000 0012

000C

281

ABLRF

BSS

0 PRDEV0 DATA

282 PRDEV0 DATA

PRDEVA

BSS

1

ACTADR

BSS

1

SHEET

9

ABL REGISTER FILE

ADDR. OF ACTION SUBROUTINE

900A OFF LINE ASSEMBLER
 ABL INTERPRETOR

SHEET 10

284	PEJ				
285					
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0800	D0E8				
0801	08A2				
0802	C7B2				
0803	C564				
0804	0218				
0805	C5B6				
0806	0100				
0807	C351				
0808	A0BA				
0809	C532				
0810	1800				
0811	0A9E				
0812	CAE8				
0813	C546				
0814	08A8				
0815	08A2				
0816	C557				
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0894					
0895					
0896					
0897					
0898					
0899					
0900					

SUBROUTINE TO WRITE A MESSAGE TO CONSOLE
 ENTRY: (M): MESSAGE INDEX - ALL REGS PRESERVED
 BSS #
 OSRF REGF2 SAVE REG FILE
 REX M,X
 RMO B,S SAVE (B)
 LDA M0,X (A)-MESSAGE STRING ADDRESS
 RMO A,B (B) POINTS TO COUNT + MESSAGE
 LDA #,BR (A)-CHARACTER COUNT
 RIN B,E (E)-START OF MESSAGE
 DST PUTC+2

RMO M,X
 OLDM -PUTC CALL SUPERVISOR TO WRITE
 SVC #
 RMO S,B RESTORE (B)
 OLRF REGF2 RESTORE REG FILE
 RMO L,P
 P.R.B

DATA #
 DATA 2
 DATA 2
 BSS 1
 BSS 1
 REGF2 #BSS B REGISTER FILE
 DATA #,M1,M2,M3,M4,M5,M6,M7,M8,M9,M10

DATA 2
 DATA LFCR
 DATA 4
 DATA #,LFCR
 DATA 14
 DATA # PROGRAM MN',LFCR

989A OFF LINE ASSEMBLER.
ABL INTERPRETOR

SHEET 11

9880 CFC7
 9881 D2C1
 988E CDAS
 988F CECE
 989C# CECE
 98C1 8A8D
 98C2 8814
 98C3 AAAA
 98C4 AACD
 98C5 C9D3
 98C6 D3C9
 98C7 CEC7
 98C8 ASD2
 98C9 D5CC
 98CA CSAA
 98C8 AAAA
 98CC 8A8D
 98CD 881A
 98CE A8A8
 98CF A8A8
 98D# C5CE
 98D1 D4C5
 98D2 D2A8
 98D3 C1CC
 98D4 D4CE
 98D5 D2CE
 98D6 C1D4
 98D7 C9D6
 98D8 CSAS
 98D9 CECE
 98DA 8A8D
 98DB 8818
 98DC A8A8
 98DD A8A8
 98DE A8A8
 98DF C5CE
 98E# D4C5
 98E1 D2A8
 98E2 C1C3
 98E3 D4C9
 98E4 CFCE
 98E5 A8A8
 98E6 CECE
 98E7 8A8D
 98E8 8822
 98E9 A8A8
 98EA A8A8
 98EB C5CE
 98EC D4C5
 98ED D2A8
 98EE C1C3
 98EF D4C9
 98F# CFCE
 98F1 A8CE
 98F2 CFAS
 98F3 CECE
 98F4 A8C9

325 M3 DATA 2#
326 DATA '***MISSING RULE***',LFCR

327 M4 DATA 26
328 DATA ' ENTER ALTERNATIVE MN',LFCR

329 M5 DATA 24
330 DATA ' ENTER ACTION MN',LFCR

331 M6 DATA 34
332 DATA ' ENTER ACTION NO MN IN ALT MN',LFCR

980A OFF LINE ASSEMBLER
 ABL INTERPRETOR

SHEET 12

00F5 C1CC
 00F6 C1CC
 00F7 D4A8
 00F8 CECE
 00F9 8A8D
 00FA 0018
 00FB A8A8
 00FC CECF
 00FD A8C1
 00FE CCD4
 00FF C5D2
 0100 CEC1
 0101 D4C9
 0102 D6C5
 0103 A808
 0104 CFB5
 0105 CEC4
 0106 8A8D
 0107 001A
 0108 A8A8
 0109 A8A8
 010A D3D4
 010B C1D4
 010C C5A8
 010D D6C5
 010E C3D4
 010F CFB2
 0110 A8BA
 0111 A8A8
 0112 CECE
 0113 CECE
 0114 8A8D
 0115 0022
 0116 A8A8
 0117 CCCI
 0118 D3D4
 0119 A8C1
 011A CCD4
 011B C5D2
 011C CEC1
 011D D4C9
 011E D6C5
 011F A8C5
 0120 D8C5
 0121 C3D5
 0122 D4C5
 0123 C4A8
 0124 8A88
 0125 CECE
 0126 8A8D
 0127 0018
 0128 A8A8
 0129 C5D8
 012A C9D4
 012B A8C6
 012C D2CF

333 M7 DATA 24 NO ALTERNATIVE FOUND',LFCR
 334 DATA :

335 M8 DATA 26 STATE VECTOR : MNNN',LFCR
 336 DATA :

337 M9 DATA 34 LAST ALTERNATIVE EXECUTED : NN',LFCR
 338 DATA :

339 M10 DATA 24 EXIT FROM PROGRAM NN',LFCR
 340 DATA :

SHEET 13

988A OFF LINE ASSEMBLER
ASL INTERPRETOR

#120 CDAG
#12E DWD2
#12F CFC7
#138 D2C1
#131 CDAG
#132 CECE
#133 BARD

985A OFF LINE ASSEMBLER
ABL INTERPRETOR

SHEET 14

0134	0003	341	PEJ
0134	0003	342	ABLPRO BSS
0135	0000	343	PROGNO DATA 3
0136	0000	344	
0137	0000	345	PCLUST DATA
0138	0000	346	PALT DATA
0139	0000	347	PACT DATA
		348	PACTOF DATA
		349	ACLSTO DATA
		350	
		351	SVECTR DATA
		352	
		353	CLSMAO DATA
		354	NCLMAO DATA
		355	YRPMAG DATA
		356	MPRMAO DATA
		357	APTRAO DATA
		358	
		359	BREG DATA
		360	LNEG DATA
		361	

PROGRAM NO.
PRESENT CLUSTER
ALT
ACTION
ACTION OFFSET IN ALTERNATIVE
PTR TO ACTION LIST FORALT BEING EXE'D
STATE VECTOR
POINTER TO CLUSTER MATRIX
NEXT-CLUSTER VECTOR
PREDICATE MATRIX
PRED MASK MATRIX
ACTION LIST POINTERS
(B)-ADDRESS OF PREVIOUS ABSTR. PROGRAM
(L)-RETURN ADDRESS

980A OFF LINE ASSEMBLER
ABL INTERPRETOR

362	PEJ				
363	*				
364	*				
365	*				
366	OPALTO			5. OUTPUT ALTERNATIVE NO.	
367	BSS				
368	RMO	L.A		SAVE RETURN	
369	STA	RETNI		ALT #	
370	LDA	PALT		CONVERT TO 2 ASCII DIGITS	
371	OBRL	B12DAS			
372	STE	M4+12		PLACE IN MESSAGE	
373	LDM	=4		WRITE MESSAGE TO CONSOLE.	
374	BRL	=WTC			
375	BRU	*RETNI			
376	BSS	1			
377	DATA	WTC			
378	*				
379	*				
380	*				
381	OPACT			6. OUTPUT ACTION OFFSET WITHIN ALT	
382	BSS				
383	RMO	L.A		RETURN ADDR	
384	STA	RETN2		ACTION OFFSET WITHIN ALTERNATIVE	
385	LDA	PACTOF			
386	OBRL	B12DAS			
387	STE	M6+11		PLACE IN MESSAGE	
388	LDA	PALT		ALT ##	
389	OBRL	B12DAS		CONVERT TO 2 ASCII DIGITS	
390	STE	M6+16			
391	LDM	=6			
392	BRL	=WTC		OUTPUT MESSAGE TO CONSOLE	
393	BRU	*RETN2			
394	BSS	1			
395	RETN2				
396	*				
397	*				
398	OPPRO			10. OUTPUT PROGRAM # TO CONSOLE	
399	RMO	L.A			
400	STA	RETURN			
401	LDA	PROGNO			
402	OBRL	B12DAS			
403	STE	M2+6			
404	LDM	=2			
405	BRL	=WTC			
406	BRU	*RETURN			

988A OFF LINE ASSEMBLER
ABL INTERPRETOR 486 RETURN BSS 1
8168 487
488 END

SHEET 16

SB8A OFF LINE ASSEMBLER
 ABL INTERPRETOR

A ABLR 0000
 B ABLR 0001
 C ABLR 0002
 D ABLR 0003
 E ABLR 0004
 F ABLR 0005
 G ABLR 0006
 H ABLR 0007
 I ABLR 0008
 J ABLR 0009
 K ABLR 0010
 L ABLR 0011
 M ABLR 0012
 N ABLR 0013
 O ABLR 0014
 P ABLR 0015
 Q ABLR 0016
 R ABLR 0017
 S ABLR 0018
 T ABLR 0019
 U ABLR 0020
 V ABLR 0021
 W ABLR 0022
 X ABLR 0023

ABLPRO 0134
 ACTLST 0075
 B12DAS 0000
 CLSMA0 0138
 HAS281 0003
 L120 004F
 M0 0085
 M1 0086
 M2 0087
 M3 0088
 M4 0089
 M5 0090
 M6 0091
 M7 0092
 M8 0093
 M9 0094
 MSCE2 0016
 NXCCLR 0137
 PACT 0088
 PRDEV0 014C
 RETN1 CAE0
 SVC 0020
 TPTER 0020
 YNPHA0 0130

ABLRET 0000
 ALTEXE 0001
 B12HAS 0002
 CLWD01 0070
 L130 0072
 M1 0073
 M2 0074
 M3 0075
 M4 0076
 M5 0077
 M6 0078
 M7 0079
 M8 0080
 M9 0081
 MSCE3 004F
 OPACT 0138
 PACTOF 0138
 PRDEVA 0012
 RETN2 013A
 SVECTR 013A
 WTC 0080
 YNPRP 0081

SHEET 17

R ABLR 0003
 R APTR 0013F
 R B12OAS 0002
 CLWD2 007E
 L050 0025
 LFCR 008D
 M10 0127
 M6 00E8
 MPRMA0 013E
 MSGE4 0069
 OPALT 0142
 PALT 0135
 PROGNO 0134
 RETURN 0168
 TABOB0 007F
 WTC0 0140

T4085 19/03/84 16:07:33

NO ERRORS NO WARNINGS

APPENDIX 3

A3. Listings of utility FUP/G :

- V1# : main module
- V3# : syntax check
- V4# : disassembler - part 1
- V9# : disassembler - part 2
- VD# : common subroutines

VI0155 19/03/84 16:14:36

988A OFF LINE ASSEMBLER
FUP/G

FUP/G (GRAPHIC 7 FOREGROUND UTILITY PROGRAM)
T1988A

SHEET 1

K. VO

ISSUE #
REVISION #

IDT
HED

VI0
FUP/G

FUP/G IS USED TO DEBUG GRAPHIC 7 DISPLAY FILE.
IT ALLOWS THE USER TO EXAMINE AND MODIFY CONTENTS
OF GRAPHIC 7 DISPLAY MEMORY, TO GET A HARD COPY
ON LINE PRINTER OF DISPLAY MEMORY, LINK/UNLINK
SOME PORTION OF DISPLAY MEMORY...

REF A3.A5.A6,INIT,UNPACK,MRKIP
REF ABLRET,B12DAS,B12HAS,B12OAS,VTC,PAGENO

DEF MPRED,CMDLN4,CMDLNS,ASSTR1,ASSTR2,ASSTR3,ASSTR4,ASSTR5
DEF ASSTR6,ASSTR7,IPVAL2,IPVAL3,IPVAL4,IPVAL5
DEF IPVAL6,IPVAL7,NOVORD,NOVDP,NOSTRS
DEF GBUFF,CBUFF,SADDR,EADDR,NVMOD,HBUF1,HBUF2
DEF BHRUF2,C1,RPRB

- 1
- 2
- 3
- 4
- 5
- 6
- 7
- 8
- 9
- 10
- 11
- 12
- 13
- 14
- 15
- 16
- 17
- 18
- 19
- 20
- 21
- 22
- 23
- 24
- 25
- 26

908A OFF LINE ASSEMBLER
FUP/G

27	0000	PEJ	EQU	1	0000
28	0001	EQU	2	0001	
29	0002	EQU	3	0002	
30	0003	EQU	4	0003	
31	0004	EQU	5	0004	
32	0005	EQU	6	0005	
33	0006	EQU	7	0006	
34	0007	EQU	8	0007	
35	0008	EQU	9	0008	
36	0009	EQU	10	0009	
37	000A	EQU	11	000A	
38	000B	EQU	12	000B	
39	000C	EQU	13	000C	
40	000D	EQU	14	000D	
41	000E	EQU	15	000E	
42	000F	EQU	16	000F	
43	0010	EQU	17	0010	
44	0011	EQU	18	0011	
45	0012	EQU	19	0012	
46	0013	EQU	20	0013	
47	0014	EQU	21	0014	
48	0015	EQU	22	0015	
49	0016	EQU	23	0016	
50	0017	EQU	24	0017	
51	0018	EQU	25	0018	
52	0019	EQU	26	0019	
53	001A	EQU	27	001A	
54	001B	EQU	28	001B	
55	001C	EQU	29	001C	
56	001D	EQU	30	001D	
57	001E	EQU	31	001E	
58	001F	EQU	32	001F	
59	0020	EQU	33	0020	
60	0021	EQU	34	0021	
61	0022	EQU	35	0022	
62	0023	EQU	36	0023	
63	0024	EQU	37	0024	
64	0025	EQU	38	0025	

980A OFF LINE ASSEMBLER
FUP/G

SHEET 3

P	0000	65	PEJ
	0001	66	BRS
	0002	67	PROGNO
	0003	68	
	0004	69	OLDA -PROGNO
P	0001	70	RMO A.M
	0002	71	OBRL ABLENT
E	0004	72	SVC 5
	0005		CAE5

H REG CONTAINS ABL PROGRAM ADDR.
CALL DRIVER TO EXECUTE FUP/G PROGRAM

• TERMINATE FUP/G

73	PEJ								
74	C1	V	V	V	V	V	V		INIT
75	C2								READ & CHECK COMMAND
76	C3								PROCESS COMMAND
77									
78	N	2	3	2	2	2	2	2	
79									
80	P5								COMMAND NO.
81	P1	-	N	N	N	N	N	-	
82	P2	-	N	N	N	N	N	-	
83	P3	-	N	N	N	N	N	-	
84	P4	-	N	N	N	N	N	-	
85									INVALID SYNTAX/PARAMETER
86									
87									
88									
89									
90									
91									
92									
93									
94									
95									
96									
97									
98									
99									
100									

ALT 1 : INITIALIZATION
 ALT 2 : CLEAR PRED. - READ VALID COMMAND
 ALT 3 : 'L'
 ALT 4 : 'H'
 ALT 5 : 'V'
 ALT 6 : 'O'
 ALT 7 : INVALID SYNTAX/PARAMETER(S)


```

99          PEU
100         *
101         *
102         *
103         *
104         *
105         *
106         *
107         *
108         *
109         *
110         *
111         *
112         *
113         *
114         *
115         *
116         *
117         *
118         *
119         *
120         *
121         *
122         *

          ABL PROGRAM SECTION
          -----
          *****

          PROGRAM NO.
          PRESENT CLUSTER
          . . . . . ALT
          . . . . . ACTION
          . . . . . ACTION OFFSET IN ALTERNATIVE
          PTR TO ACTION LIST FORALT BEING EXE'D

          STATE VECTOR
          TO CLUSTER MATRIX
          . . . . . NEXT-CLUSTER VECTOR
          . . . . . PREDICATE MATRIX
          . . . . . PRED MASK MATRIX
          . . . . . ACTION LIST POINTERS

          (B)-ADDRESS OF PREVIOUS ABSTN. PROGRAM
          (L)-RETURN ADDRESS

          PROGNO DATA 3
          PCLUST DATA 8
          PALT DATA 8
          PACT DATA 8
          PACTOF DATA 8
          ACLSTO DATA 8
          SVECTR DATA 8
          CLSMAG DATA CLSMAT
          NCLMAG DATA NCLMAT
          YNPMAG DATA YNPMAT
          MPRMAG DATA MPRMAT
          APTRBO DATA APTRBO
          BREG DATA 8
          LREG DATA 8
    
```

123	PEJ			
124				
125				
126				
127	CLSMAT	DATA >8000	C1	
128		DATA >9000		
129		DATA >4000	C2	
130		DATA >0000		
131		DATA >3F00	C3	
132		DATA >0000		
133				
134				
135				
136	NCLMAT	DATA 2	#	
137		DATA 3	1	
138		DATA 2	2	
139		DATA 2	3	
140		DATA 2	4	
141		DATA 2	5	
142		DATA #	6	
143		DATA 2	7	
144				
145				
146				
147	YNPMAT	DATA >0000	ALT #	
148		DATA >0000	1	
149		DATA >1000	2	
150		DATA >2000	3	
151		DATA >3000	4	
152		DATA >4000	5	
153		DATA >5000	6	
154		DATA >6000	7	
155				
156				
157				
158	MPRMAT	DATA >0000	ALT #	
159		DATA >0000	1	
160		DATA >F000	2	
161		DATA >F000	3	
162		DATA >F000	4	
163		DATA >F000	5	
164		DATA >F000	6	
165		DATA >0000	7	
166				
167				
168				
169	APTR00	DATA ACLST0		
170		DATA ACLST1		
171		DATA ACLST2		
172		DATA ACLST3		
173		DATA ACLST4		
174		DATA ACLST5		
175		DATA ACLST6		
176		DATA ACLST7		
0014	0000			
0015	0000			
0016	4000			
0017	0000			
0018	3F00			
0019	0000			
001A	0002			
001B	0003			
001C	0002			
001D	0002			
001E	0002			
001F	0002			
0020	0000			
0021	0002			
0022	0000			
0023	0000			
0024	1000			
0025	2000			
0026	3000			
0027	4000			
0028	5000			
0029	6000			
002A	0000			
002B	0000			
002C	F000			
002D	F000			
002E	F000			
002F	F000			
0030	F000			
0031	0000			
0032	003A			
0033	003C			
0034	003F			
0035	0042			
0036	0045			
0037	0047			
0038	0049			
0039	004B			

```

177 PEJ
178 * ACLST0 BSS # - ALT # : INITIALIZATION
179 *
180 * DATA INITI
181 * DATA #
182 *
183 *
184 * ACLST1 BSS # - ALT 1 : READ AND CHECK COMMAND
185 *
186 *
187 *
188 * DATA RDCMD
189 * DATA A3
190 *
191 *
192 * ACLST2 BSS # - ALT 2 : 'L'
193 * DATA CRT11
194 * DATA A5
195 *
196 *
197 * ACLST3 BSS # - ALT 3 : 'H'
198 * DATA CRT12
199 * DATA A6
200 *
201 *
202 *
203 * ACLST4 BSS # - ALT 4 : 'M'
204 * DATA MODD
205 * DATA #
206 *
207 *
208 * ACLST5 BSS # - ALT 5 : 'V'
209 * DATA VBTGR7
210 * DATA #
211 *
212 *
213 * ACLST6 BSS # - ALT 6 : 'O'
214 * DATA TERMES
215 * DATA #
216 *
217 *
218 * ACLST7 BSS # - ALT 7 : INVALID COMMAND
219 * DATA ERMESS
220 * DATA #
221 *

```

Address	Label	Description	Pointers	ASCII String	ASCI Strings	Previous Command
222	PEJ					
223		DATA SECTION				
224		DATA SECTION				
225		DATA SECTION				
226		DATA SECTION				
227	INIT0	DATA INIT				
228	MPRED	EQU SVECTR				
229						
230						
231	CMDLN	DATA CMDLN				
232	CMDLN	DATA EMPTY+8				
233	CMDLN	DATA S,S				
234	CMDLN	BSS 80/2				
235		DATA LFCR				
236						
237	ASSTR0	BSS 1				
238		DATA EMPTY+1				
239		DATA S,S				
240	ASSTR1	BSS 1			2	
241		DATA EMPTY+6				
242		DATA S,S				
243	ASSTR2	BSS 3			3	
244		DATA EMPTY+6				
245		DATA S,S				
246	ASSTR3	BSS 3			4	
247		DATA EMPTY+6				
248		DATA S,S				
249	ASSTR4	BSS 3			5	
250		DATA EMPTY+6				
251		DATA S,S				
252	ASSTR5	BSS 3			6	
253		DATA EMPTY+6				
254		DATA S,S				
255	ASSTR6	BSS 3			7	
256		DATA EMPTY+6				
257		DATA S,S				
258	ASSTR7	BSS 6/2				
259	NOSTRS	BSS 1				
260						
261	IPVAL2	BSS 1			2	
262	IPVAL3	BSS 1			3	
263	IPVAL4	BSS 1			4	
264	IPVAL5	BSS 1			5	
265	IPVAL6	BSS 1			6	
266	IPVAL7	BSS 1			7	
267						
268	PRECMD	BSS 1				

988A OFF LINE ASSEMBLER

FUP/G

BEAC				
BEAD				
BEAE				
BEAF				
BEBS				
269	MOVWD	BSS	1	
270	MOVDP	BSS	1	
271	SADDR	BSS	1	
272	EADDR	BSS	1	
273	MVMD	BSS	1	

SHEET 9

* OF WORD IN GR7 MEM TO BE PROCESSED
 * WORDS ALREADY PROCESSED
 START BYTE ADDR OF BUFFER
 END
 * OF WORDS TO MODIFY

980A OFF LINE ASSEMBLER
FUP/C

SHEET 12

00E8	C29B	348	REQ E.A	
00E9	300B	349	QAND ->7F0B	
00EA	07F0B			ONLY LH BYTE
00EB	CC8B	350	SMZ A	INSTRUCTION MATCHED ?
00EC	7005	351	BRU A215	YES
00ED	40F8	352	BIX A210	NO - TRY NEXT INSTRUCTION
		353		'UNVALID COMMAND'
00EE	1F0C	354	A2.12 LDH -12	
00EF	7400	355	BRL *S+1	
	000B	356	DATA WTC	
E	00F0	357	BRU A2.0	PROMPT FOR NEW COMMAND
	7001	358		
00F2	000B	359	A215 STA PRECMD	SAVE THE COMMAND FOR POSSIBLE CR NEXT TIME
00F3	0218	360	LDA PREDIC.X	GET PREDICATE CORRESPONDING TO THIS COMMAND
00F4	0106	361	STA SVECTR	
00F5	7C00	362	BRU *A2RETN	RETURN
00F6		363		
	0004	364	A2RETN BSS 1	
E	00F7	365	UNPCK0 DATA UNPACK	
	0000	366	BSS 10	

900A OFF LINE ASSEMBLER
 FWP/C

SHEET 13

367	PEJ		
368		COMMAND LIST	
369			
370			
371	BCOMMD	DATA 'L'	LIST
372		DATA 'H'	HARD COPY
373		DATA 'M'	MODIFY
374		DATA 'W'	WRITE
375		DATA 'Q'	QUIT
376	COMMD	BSS	
377	NOCOMM	EGU	BCOMMD-COMMD # OF COMMANDS
378			
379			PREDICATES FOR COMMANDS
380			
381	1000	DATA >1000	L
382	2000	DATA >2000	H
383	3000	DATA >3000	M
384	4000	DATA >4000	W
385	5000	DATA >5000	Q
386		PREDIC BSS	

980A OFF LINE ASSEMBLER
FUP/G

SHEET 14

387	PEJ			
388	--	READ GR7 MEMORY TO TI MEMORY		
389				
390		INPUT : IPVAL2, NOWORD		
391		OUTPUT : GBUFF		
392				
393				
394	GRTI1	BSS	STARTING BYTE ADDR	
395		LDA IPVAL2	# OF WORDS	
396		LDE NOWORD		
397		BRU GRTI		
398				
399		READ 38 WORDS FROM GR7 TO TI (FOR 'H')		
400				
401	GRTI2	BSS		
402		LDA IPVAL2	START BYTE ADDR	
403	GRTI	OSTA GI+1	FOR 'GI' MESSAGE	
404		OLDM -RPRB	RECEIVE 'RI' FROM GR8	
405		SVC		
406		OHMO PAGENO	BUMP PAGE NO FOR 'H'	
407		RMO L.P	RETURN	
408				
409		GBUFF0 DATA GBUFF	POINTER	

988A OFF LINE ASSEMBLER
FUP/6

411	PEJ		
412	--	MODIFY GBUFF WITH THE CONTENTS SPECIFIED	
413			
414	MOOD		
415	BSS		BACK-UP (8)
416	RMD	B, X	
417	LDA	IPVAL2	
418	SUB	SADDR	
419	LRA	1	OFFSET IN WORDS FR. START OF GBUFF POINT TO LOC. IN BUFFER
420	QADD	-GBUFF	
421	RMO	A, E	
422	OLDA	-IPVAL3	START ADDR OF INPUT
423	RMO	A, B	
424	UPDATE	B, BR	NEW CONTENT
425	REX	E, B	
426	STA	B, BR	UPDATE INTO BUFFER
427	REX	E, B	
428	RIN	E, E	POINT TO NEXT WORD
429	RIN	B, B	
430	DMT	-NUMOD0	UPDATE ALL SPECIFIED LOCATIONS
431	BRU	UPDATE	
432	RMO	X, B	RESTORE (8)
433	RMO	L, P	
434			
435	NUMOD0	DATA	NUMOD
436			

OSMA OFF LINE ASSEMBLER
FUP/G

LINE	PEU	WRITE BUFFER TO GR7 MEMORY	NUMBER...	OF WORDS TO XFER	CHAR COUNT	WRITE TO GR7 MEMORY
437						
438						
439						
440						
441		WOTGR7 BSS #				
442		LDA S+1				
443		DATA >5355				
444		OSTA SUSU				
445						
446		OSTA SOSU+1				
447		LDA EADDR				
448		SUB SADDR				
449		LRA 1				
450						
451		STA SUSU+2				
452		LLA 1				
453		ADD -6				
454		OSTA SUPRB+2				
455		OLDM -SUPRB				
456		SVC #				
457		RMO L.P				

988A OFF LINE ASSEMBLER
FUP/G

SHEET 17

458	PEU				
459	--	OUTPUT 'INVALID SYNTAX' TO CONSOLE			
460					
461	ERMESG	BSS		SAVE RETURN	
462	RMO	L.A			
463	STA	A16RET			
464					
465	LDM	-15			
466	QBRL	WTC			
467					
468	BRU	"A16RET			
469	A16RET	BSS 1		RETURN	
470					
471	--	OUTPUT 'FUP/G TERMINATES' TO CONSOLE			
472					
473	TERMES	RMO		SAVE RETURN	
474	STA	TERRTH		SKIP A LINE	
475				'FUP/G TERMINATES'	
476	LDM	-8			
477	BRL	"WTCO			
478	LDM	-16			
479	BRL	"WTCO			
480	LDM	-16			
481	BRL	"WTCO			
482	DATA	WTC			
483	BRU	"TERRTH		RETURN	
484					
485	TERRTH	BSS 1		RETURN	

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535

0181 002C
0182 C560
0183 0028
0184 0028
0185 0750
0186 0025
0187 1000
0188 017A
0189 CA00
018A 0021
018B 017A
018C 7000
018D 001E
018E 0F50
018F C703
0190 CD20
0191 700F
0192 0400
0193 0079
0194 0700
0195 CD20
0196 7003
0197 7009
0198 070F
0199 7001
019A 0711
019B 1000
019C 7000
019D 0000
019E C503
019F 740D
0190 10FF
0191 C300
0192 1000
0193 004E
0194 7400
0195 0005
0196

PE0
SUBROUTINE RFC (READ FROM CONSOLE) SETS UP A PRB FOR READING FROM LUN # AND THEN ISSUES THE READ
ON RETURN (M)-NO. OF CHARS. IN I/P STRING EXCLUSIVE OF THE CR. --1 FOR A READ ERROR OR A LINE TOO LONG ALL OTHER REGISTERS ARE PRESERVED
STA R9+4 SAVE ENTRY (A),(L).(X)
RMO L,A
STA R9+5
STX R9+6
LDA -00
STA R9+2
OLDM -R9
SVC #
TH0Z 1,R9
BRU R4
LDA R9+2
CPA -00
RDE A,M
SEQ R7
BRU R7
OLDA CMDLMS+39
CPL ->8D
SE0 R6
BRU R7
LDA -15
BRU 0+2
LDA -17
LDM -0
BRL -VTC000
DATA VTC
RMO A,M
BRL -VTC000
LDM --1
RIN H,A
OLDM CMDLNX
BRL -MRKIP0
DATA MRKIP

RFC
R4
R6
R7
VTC000
MRKIP0

RESET THE I/P CHAR. COUNT
ISSUE THE READ
READ ERROR?
YES:
(A)-I/P CHAR. COUNT
00 CHARS. READ IN?
NO:
WAS 00-TH CHAR. A CR?
NO:
SET BAD LINE FLAG
(A)-NO. OF CHARS READ

900A OFF. LINE ASSEMBLER

SHEET 19

FUP/G											
#176	C783	536									
#177	8886	537	CODE	A.M							
#178	1887	538	LDA	R9+4							
#179	7C88	539	LDX	R9+6							
		540	BRU	R9+8							
		541	DATA	>488							
#17A	8488	542	DATA	#							
#17B	8888	543	BSS	1							
#17C		544	DATA	CMDLNS							
P #17D	8852	545	BSS	3							
#17E											

RESTORE ENTRY (A),(X)

LUN

1 READ ASCII OPCODE

2 CHAR. COUNT

3 DATA BUFFER ADDR.

4-5 ENTRY (A),(L),(X)

546	###2	DATA 2	GR8	WRITE OBJECT
547	###3	DATA 3	GR8	CHAR COUNT
548		BSS 1		BUFFER
549		DATA 1	GR8	READ OBJECT
550		DATA 2	GR8	CHAR COUNT FOR 'GI'
551	SUPR8	DATA 1		BUFFER ADDR
552		DATA 6		CHAR COUNT FOR 'RI'
553		DATA 12+38*2		BUFFER
554	RPR8	DATA R1		'GIVE IMAGE'
555		DATA >4749		STARTING BYTE ADDR
556		BSS 1		WORD COUNT
557		DATA 38		'RETURN IMAGE'
558		DATA >6249		STARTING BYTE ADDR
559		BSS 1		# OF WORDS
560	GI	DATA #		'VARIABLE LENGTH'
561		DATA >664C		# OF WORDS FOLLOWING
562		BSS #		GRAPHIC BUFFER - TO STORE GR7 INST.
563		DATA #		'H' SHARES THE SAME BUFFER AS 'L'
564	RI	BSS #		
565		BSS #		
566		BSS 38		
567	SUSU	BSS 38		
568	VL	BSS 38		
569		BSS 38		
570	CBUFF	BYTE HBUFF2		
571	HBUFF1	BYTE HBUFF2		
572		BYTE HBUFF2		
573	HBUFF2	BYTE HBUFF2		
574	HBUFF2	BYTE HBUFF2		
575		END		
576				

308A OFF LINE ASSEMBLER
FUP/7C

A 0000
 A2.2 0002
 A3 0000
 ACLST5 003A
 ACLST5 0047
 R APTR00 0011
 ASSTR5 0095
 BCOMMAND 0102
 BR 0001
 CMDLN4 004F
 EADDR 00AF
 CBUFF0 0118
 HBUF1 0194
 IPVAL2 00A5
 IPVAL7 00AA
 MODD 0119
 MRKIP0 0175
 NOWDP 00AD
 R P8 0000
 R P13 000D
 R P4 0004
 R P9 0009
 R PCLUST 0007
 R5 016A
 RFC0 00C8
 SUPRB 0181
 TERRTN 0150
 WBTGR7 012E
 X 0002
 A16RET 0147
 AZ00 00E5
 A5 0001
 ACLST1 003C
 ACLST6 0049
 ASSTR1 007F
 ASSTR6 0098
 HBUFF2 01E0
 BREG 0012
 R CMDLNS 0052
 EMPTY 0000
 GI 0188
 HBUF2 01BA
 IPVAL3 00A6
 L 0005
 HPRD 000C
 NCLMA0 000E
 NOWORD 00AC
 R P1 0001
 R P14 000E
 R P5 0005
 R PACT 0009
 PRECHD 00A8
 R7 0171
 R1 018E
 SUSU 0191
 UNPACK 0004
 WTC 0008
 R XB 0003

AIRETN 0008
 AZ10 00E6
 A6 0002
 ACLST2 003F
 ACLST7 0048
 ASSTR2 0083
 ASSTR7 00A1
 ASSTR8 0098
 R CLSMA0 0000
 R CMDM 0107
 ERMESG 0141
 CRT1 0118
 IPVAL4 00A7
 LFCR 008D
 R MPRMA0 0010
 NCLMAT 001A
 NVMOD 0009
 R P10 000A
 R P15 000F
 R P6 0006
 R PACTOF 000A
 R9 017A
 RPRB 0185
 SVC CAEM
 UNPCK0 00F7
 WTC00 00B5
 R YNPMA0 000F

AZ.0 00C3
 AZ15 00E6
 ABLENT 0006
 ACLST3 0042
 ACLST0 0008
 R ASSTR3 0089
 R ASSTR0 0078
 R B12MAS 0014
 CR CLSMAT 0009
 CR 0000
 R FULL 0000
 R GR11 010C
 R INIT0 00AD
 R IPVAL5 00A9
 R LREG 0013
 MPRMAT 002A
 NCOMM FFFB
 NVMOD0 012D
 R P11 000B
 R P2 0002
 R P7 0007
 R P12 000C
 R P3 0000
 R P8 0008
 R PALT 0168
 R4 RFC 0151
 R S SADDR 00AE
 SVECTR 0004
 UPDATE 0123
 WTC000 016D
 YNPMAT 0022

AZ.12 00EE
 AZRET0 00F6
 R ABLRET 0007
 ACLST4 0045
 APTR00 0032
 ASSTRA 000F
 B B12OAS 000A
 R CMDLN4 004E
 E 0001
 GBUFF 0194
 GR12 010F
 INITI 0081
 IPVAL6 00A9
 H 0003
 MRKIP 0005
 NOSTRS 00A4
 P 0007
 R P12 000C
 R P3 0000
 R P8 0008
 R R4 0168
 RFC 0151
 SADDR 00AE
 TERMES 0148
 R VL 0192
 WTCO 014E

SHEET 21

VI0155 19/03/84 NO ERRORS 16:16:10

V30045 19/03/84 15:57:28

900A OFF LINE ASSEMBLER
SYNTAX CHECK

1 *
2 *
3 *
4 *
5 *
6 *
7 *
8 *
9 *
10 *
11 *
12 *
13 *
14 *
15 *
16 *
17 *
18 *
19 *
20 *

SYNTAX CHECK FOR FUP/G
T1980A

K. VO

ISSUE #
REVISION #

IDT V30
HED SYNTAX CHECK

THIS MODULE CHECKS ON VALIDITY OF PARAMETERS
USED IN COMMANDS

REF B12HAS, ABLENT, ABLRET, ASSTR1, ASSTR2
REF ASSTR3, ASSTR4, ASSTR5, ASSTR6, ASSTR7
REF HAS281, NOWORD, MPRED, NOSTR5, SADDR, EADDR, NUMOD
REF IPVAL2, IPVAL3, IPVAL4, IPVAL5, IPVAL6, IPVAL7

DEF IPVAL0, A3, ASSTR0

SHEET 1

900A OFF LINE ASSEMBLER
SYNTAX CHECK

21	###	*	PEJ	EQU	#	1	>###
22	###	*	EQU	1			>###
23	###	A	EQU	2			>###
24	###	E	EQU	3			>###
25	###	X	EQU	4			>###
26	###	H	EQU	5			>###
27	###	S	EQU	6			>###
28	###	L	EQU	7			>###
29	###	B	EQU	1			>###
30	###	P	EQU	3			>###
31	###	*	OPD	>CAEN,3			>###
32	###	BR	EQU	>8A8D			>###
33	###	X8	BRS	PROGNO			>###
34	###	SVC	EQU	1			>###
35	CAEN	LFCR	EQU	2			>###
36	8A8D	*	EQU	3			>###
37	###	*	EQU	4			>###
38	###	*	EQU	5			>###
39	###	*	EQU	6			>###
40	###	P8	EQU	7			>###
41	###	P1	EQU	8			>###
42	###	P2	EQU	9			>###
43	###	P3	EQU	10			>###
44	###	P4	EQU	11			>###
45	###	P5	EQU	12			>###
46	###	P6	EQU	13			>###
47	###	P7	EQU	14			>###
48	###	P8	EQU	15			>###
49	###	P9	EQU	16			>###
50	###	P10	EQU	17			>###
51	###	P11	EQU	18			>###
52	###	P12	EQU	19			>###
53	###	P13	EQU	20			>###
54	###	P14	EQU	21			>###
55	###	P15	EQU	22			>###
56	###	BIT	EQU	23			>###
57	###	BIT	EQU	24			>###
58	###	BIT	EQU	25			>###
59	###	BIT	EQU	26			>###
60	###	BIT	EQU	27			>###
61	###	BIT	EQU	28			>###
62	###	BIT	EQU	29			>###
63	###	BIT	EQU	30			>###
64	###	BIT	EQU	31			>###
65	###	BIT	EQU	32			>###
66	###	BIT	EQU	33			>###
67	###	BIT	EQU	34			>###

985A OFF LINE ASSEMBLER
SYNTAX CHECK

SHEET 3

```

68 * * * * *
69 * * * * *
70 * * * * *
71 * * * * *
72 * * * * *
73 * * * * *
74 * * * * *
75 * * * * *
76 * * * * *
77 * * * * *
78 * * * * *
79 * * * * *
80 * * * * *
81 * * * * *
82 * * * * *
83 * * * * *
84 * * * * *
85 * * * * *
86 * * * * *
87 * * * * *
88 * * * * *
89 * * * * *
90 * * * * *
91 * * * * *
92 * * * * *

```

PEJ
A3* COMMAND SYNTAX CHECK

CHECK FOR VALIDITY OF ARGUMENTS ASSOCIATED WITH COMMAND
(EXCEPT FOR 'M' WITH 'I'). HEX ASCII STRINGS ARE
CONVERTED INTO BINARY VALUES
- FOR L,H,M,S : CALCULATES STARTING GR7 ADDR AND
AND # OF WORDS TO PROCESS. INDICATES IF MASK VALUE
IS USED IN S COMMAND

INPUTS : ASSTR1....ASSTR7
P#P1....P1#

OUTPUT : IPVAL2,...IPVAL7,IPVAL0,NOWORD

BSS #
RMO L.A
STA A3RETN
SAVE RETURN

OLDM -PROGNO
OBRL ABLENT
M REG CONTAINS PROGRAM ADDR.

CALL ABL DRIVER TO EXECUTE SYNTAX CHECK PROGRAM

BRU *A3RETN
A3RETN BSS 1
RETURN
RETURN

```

0000 C558
0001 8005
0002 1000
0003 0008
0004 7400
0005 0001
0006 0000
0007 7C00

```

988A OFF LINE ASSEMBLER
SYNTAX CHECK

SHEET 4

```

93          *
94          *
95          *
96          *
97          *
98          *
99          *
100         *
101         *
102         *
103         *
104         *
105         *
106         *
107         *
108         *
109         *
110         *
111         *
112         *
113         *
114         *
115         *
116         *
117         *
118         *

          A3 (SYNTAX CHECK)
          ALB PROGRAM SECTION
          *****
          PROGNO DATA 3
          PCLUST DATA #
          PALT DATA #
          PACT DATA #
          PACTOF DATA #
          ACLSTO DATA #
          SVECTR DATA #
          CLSMAP DATA CLSMAT
          NCLMAP DATA NCLMAT
          YNPMAP DATA YNPMAT
          MPRMAP DATA MPRMAT
          APTRES DATA APTRES
          BREG DATA #
          LREG DATA #

          PROGRAM NO.
          PRESENT CLUSTER
          .
          .
          .
          ACTION
          ACTION OFFSET IN ALTERNATIVE
          PTR TO ACTION LIST FORALT BEING EXE'D
          STATE VECTOR

          POINTER TO CLUSTER MATRIX
          .
          .
          .
          NEXT-CLUSTER VECTOR
          .
          .
          .
          PREDICATE MATRIX
          .
          .
          .
          PRED MASK MATRIX
          .
          .
          .
          ACTION LIST POINTERS

          (B)-ADDRESS OF PREVIOUS ABSTR. PROGRAM
          (L)-RETURN ADDRESS
    
```

980A OFF LINE ASSEMBLER
SYNTAX CHECK

SHEET 5

```

119 PEJ
120 C1 V V V V V V V V
121 C2 V V V V V V V V
122 N 2 2 2 2 2 2 2 2
123 P0 - Y Y - - - - -
124 P1 - - - - - L,H
125 P2 - - - - - M
126 P3 - - - - - S
127 P4 - N Y - - - C,U,K
128 P5 - - - - - I
129 P6 - - - - - TO IN 3RD ASCII STRG
130 P7 - - - - - MASK VALUE SPECIFIED IN 'S'
131 P8 - - - - - 'R','I','C'
132 - - - - - ALL 'M' PAR'S CHECKED OR NO ARG CMD
133 - - - - -
134 - - - - -
135 - - - - -
136 - - - - -
137 - - - - -
138 - - - - -
139 - - - - -
140 - - - - -
141 - - - - -
142 - - - - -
143 CLSMAT DATA >8888.S C1
144 DATA >7FC8.S C2
145 - - - - -
146 - - - - -
147 - - - - -
148 NCLMAT DATA 2 ALT 1
149 DATA 2 ALT 2
150 DATA 2 ALT 3
151 DATA 2 ALT 4
152 DATA 2 ALT 5
153 DATA 2 ALT 6
154 DATA 2 ALT 7
155 DATA 2 ALT 8
156 DATA 2 ALT 9
157 - - - - -
158 - - - - -
159 - - - - -
160 YNPHAT DATA >8888 ALT 1
161 DATA >8888 ALT 2
162 DATA >8888 ALT 3
163 DATA >4488 ALT 4
164 DATA >2288 ALT 5
165 DATA >2288 ALT 6
166 DATA >1888 ALT 7
167 DATA >8188 ALT 8
168 DATA >8888 ALT 9
169 - - - - -
170 - - - - -
171 - - - - -
172 - - - - -

```

CLUSTER MATRIX BITSS-31

CLSMAT DATA >8888.S C1
DATA >7FC8.S C2

NEXT CLUSTER

NCLMAT DATA 2 ALT 1
DATA 2 ALT 2
DATA 2 ALT 3
DATA 2 ALT 4
DATA 2 ALT 5
DATA 2 ALT 6
DATA 2 ALT 7
DATA 2 ALT 8
DATA 2 ALT 9

Y/N PREDICATE MATRIX

YNPHAT DATA >8888 ALT 1
DATA >8888 ALT 2
DATA >8888 ALT 3
DATA >4488 ALT 4
DATA >2288 ALT 5
DATA >2288 ALT 6
DATA >1888 ALT 7
DATA >8188 ALT 8
DATA >8888 ALT 9

MASK FOR PREDICATE MATRIX

908A OFF LINE ASSEMBLER
SYNTAX CHECK

SHEET 6

002E 0000	173 *	MPMAT	DATA >0000	ALT #	1
002F 0000	174 *		DATA >8000		2
0030 0000	175		DATA >8000		3
0031 4410	176		DATA >4410		4
0032 4400	177		DATA >4400		5
0033 2200	178		DATA >2200		6
0034 2200	179		DATA >2200		7
0035 1000	180		DATA >1000		8
0036 0100	181		DATA >0100		9
0037 0000	182		DATA >0000		

ACTION MATRIX

P 0038 0042	183 *		DATA ACLST0
P 0039 0044	184 *		DATA ACLST1
P 003A 004A	185 *		DATA ACLST2
P 003B 0051	186 *	APTR0	DATA ACLST3
P 003C 0055	187		DATA ACLST4
P 003D 0057	188		DATA ACLST5
P 003E 0058	189		DATA ACLST6
P 003F 0059	190		DATA ACLST7
P 0040 005A	191		DATA ACLST8
P 0041 005C	192		DATA ACLST9

980A OFF LINE ASSEMBLER
SYNTAX CHECK

SHEET 7

197	PEJ		
198			
199			
200	ACLST8	DATA	INIT
201			
202			
203			
204	ACLST1	DATA	HXS2B1
205			
206			
207			
208			
209			
210			
211			
212	ACLST2	DATA	HXS2B1
213			
214			
215			
216			
217			
218			
219			
220			
221	ACLST3	DATA	HXS2B1
222			
223			
224			
225			
226			
227			
228			
229			
230			
231	ACLST5	DATA	
232			
233			
234			
235	ACLST6	DATA	
236			
237			
238	ACLST7	DATA	
239			
240			
241	ACLST8	DATA	INVALID SYNTAX
242			
243			
244			
245	ACLST9	DATA	INVALID SYNTAX

- ALT 8 : INITIALIZATION - CLASSIFICATION
 INIT - CLASS.
 - ALT 1 : L, H FORMAT 1
 CONVERT HEX VALUE TO BINARY
 BUMP POINTERS BY 1
 CONVERT HEX VALUE TO BINARY
 CHECK MEM LOC 1 & CALC. # OF WORDS
 SAVE START & END ADDRESS
 - ALT 2 : L, H FORMAT 2
 CONVERT HEX VALUE TO BINARY
 CHECK VALID GR7 MEM LOC.
 BUMP POINTER BY 2
 CONVERT HEX VALUE TO BINARY
 CHECK MEM LOC 1 & LOC 2
 SAVE START & END ADDRESS
 - ALT 3 : M
 CONVERT HEX VALUE TO BINARY
 BUMP POINTERS BY 1
 CHECK IF ALL ARG'S ARE CONVERTED
 - ALT 4 : M WITH 1 ** TO BE DEVELOPED
 - ALT 5 : S ** TO BE DEVELOPED
 - ALT 6 : S WITH MASK ** TO BE DEVELOPED
 - ALT 7 : C.U.K ** TO BE DEVELOPED
 - ALT 8 : INVALID SYNTAX
 OUTPUT 'INVALID SYNTAX'
 - ALT 8 : EXIT FROM A3

980A OFF LINE ASSEMBLER
SYNTAX CHECK

SHEET 8

PEJ
246 *
247 *
248 *
249 *
250 *
251 IPVAL0 BSS 1
252 ASSTR0 BSS 1
253 MOARG0 BSS 1

DATA SECTION
.....

#55b
#55e
#55f

POINT TO IPVAL1, IPVAL2....
POINT TO ONE OF ASCII STRINGS
OF ARGUMENTS

986A OFF LINE ASSEMBLER
SYNTAX CHECK

SHEET 9

254	PEJ				
255					
256					
257					
258					
259					
260					
261					
262					
263					
264	INIT				
265					
266					
267					
268					
269					
270					
271					
272					
273					
274					
275					
276					
277					
278					
279					
280					
281					
282					
283					
284					
285					
286					
287					
288					
289					
290					
291					
292					
293					
294					
295					
296					
297					
298					

ACTION SECTION

RESET ASCII STRING POINTER TO 1ST STRING
DETERMINE CLASS OF COMMAND
CHECK FOR 'TO', 'I', MASK VALUE FOR 'M'

BSS #

RESET ASCII & IPVAL POINTERS

OLDA -ASSTR2 ASSTR0 = ASSTR2

OSTA ASSTR0

OLDA -IPVAL2

STA IPVAL0

RECOGNIZE CLASS OF COMMAND
CLEAR PREDICATES

LDE SVECTR

LDX -CMOPR-CMOPRE

OLDA MPRED MAIN PRED.

0AND ->F000 P0-P3 OF MAIN PRED : CMD NO.

RMO A,E

LDA CMOPRE,X

RCA E,A

SME A31.2 THIS CLASS ?

BRU A31.2 YES

BIX A31.2 NO - CHECK NEXT CLASS

SMBO 0.SVECTR COMMAND DOES NOT NEED ARGUMENT

RMO L,P

LDA CMDCLS,X

STA SVECTR

SET PRED

CHECK FOR CORRECT # OF STRGS IN COMMAND LINE

LDA SVECTR

TABO #

BRU A312# 'L','H'

OLDA NOSTRS

CPA -3

SME

CORRECT # OF STRINGS ?

988A OFF LINE ASSEMBLER

SYNTAX CHECK	LINE	ASSEMBLER	OPERATOR	OPERAND	OPERATION	OPERAND	OPERATION	OPERAND	OPERATION	OPERAND	OPERATION
299	CPA	L.P									
300	CPA	-4									
301	SEQ	A3129									
302	BRU	ASSTR3									
303	OLDA	ASSTR3									
304	CPA	+1									
305	DATA	PTO									
306	SEQ	A3129									
307	BRU	ASSTR3									
308	OLDA	SVECTR									
309	SABO	P4									
310	OSTA	SVECTR									
311	RMO	L.P									
312	TABO	1									
313	BRU	A3122									
314	OLDA	NOSTRS									
315											
316	SUB	-1									
317	STA	NOARGS									
318	SUB	-1									
319	OSTA	NMOD									
320	SPL	A									
321	BRU	A3129									
322	BRU	A31.3									
323	TABO	2									
324	BRU	A3124									
325	OLDA	NOSTRS									
326											
327	CPA	-6									
328	SNE										
329	RMO	L.P									
330	CPA	-7									
331	SEQ	A3129									
332	BRU	ASSTR3									
333	OLDA	SVECTR									
334	SABO	P6									
335	OSTA	SVECTR									
336	BRU	A31.3									
337	OLDA	NOSTRS									
338											
339	CPA	-2									
340	SNE										

S88A OFF LINE ASSEMBLER

SHEET 11

SYNTAX CHECK
 S88A C557
 S881 DB74
 S881 S88C
 E S882 S88B
 S883 S88B
 S885
 E S884 S88B
 S886 S88B
 S886 D4CF
 S887 CD2B
 S888 7856
 S889 S88B
 P S88A S88E
 S88B DB54
 S88C S88B
 P S88D S88E
 S88E 784B
 S88F S88B
 S88C S88F
 S881 C42B
 S882 S88B
 S883 A1FF
 S884 CD2B
 S885 C557
 S886 S88B
 P S887 S88E
 S888 DB55
 S889 S88B
 P S88C S88E
 S88C C557
 S88C S88B
 E S88D S88B
 S88D
 S88E S88B
 S88F 6F87
 S88C CD2B
 S88D C557
 S881 S88B
 P S882 S88E
 S883 DB56
 S884 S88B
 P S885 S88E
 S886 C557

341 RMO L.P
 342
 343 A3129 BSS B
 344 S88D P4.M.PRED SET 'INVALID' PREDICATE IN MAIN STATE VECTOR
 345
 346
 347
 348 A31.3 OLDA ASSTR3 TO OR 1 MUST BE IN STRING 3
 349
 350
 351
 352
 353
 354
 355
 356
 357
 358 A31.4 OLDE ->BFFF CHECK IF MASK VALUE SPECIFIED
 359
 360
 361
 362
 363
 364
 365
 366
 367
 368
 369 A31.6 OLDA NOSTRS RETURN
 370
 371
 372
 373
 374
 375
 376
 377

800A OFF LINE ASSEMBLER
SYNTAX CHECK

SHEET 12

378	PEJ		
379			
380			
381	DATA	>1000	L
382	DATA	>2000	H
383	DATA	>3000	M
384	BSS		
385			
386			
387	DATA		L: CLASS #
388	DATA		H: CLASS #
389	DATA		M: CLASS #
390	BSS		

391	DATA		
392	DATA		
393	DATA		
394	BSS		
395			
396			
397	DATA		
398	DATA		
399	DATA		
400	BSS		

980A OFF LINE ASSEMBLER
SYNTAX CHECK

```

391 * * * * * PEJ
392 * * * * *
393 * * * * *
394 * * * * *
395 * * * * *
396 * * * * *
397 * * * * *
398 BPTRB1 BSS B
399 @IMO IPVAL0
400
401 LDX --7
402 OLDA ASSTRO
403
404 CPL ASSTR.X
405 SNE --2
406 BRU @A32.B
407 BIX A32.B
408 RIN X.X
409 LDA ASSTR.X
410 @STA ASSTRO
411
412 RMO L.P
413
414 * * * * *
415 * * * * *
416 * * * * *
417 * * * * *
418 * * * * *
419 * * * * *
420 * * * * *
421 * * * * *
422 * * * * *
423 * * * * *
424 * * * * *
425 * * * * *
426 * * * * *
427 * * * * *
428 * * * * *
429 * * * * *
430 * * * * *
431 ASSTR BSS B
432 * * * * *
433 * * * * *
434 * * * * *
435 * * * * *
436 * * * * *

```

-- BUMP ASCII STRING PTR AND I/P VALUE PTR BY 1
INPUTS : ASSTRO, IPVAL0
OUTPUTS: ASSTRO, IPVAL0

-- BUMP ASCII STRING PTR AND I/P VALUE PTR BY 2
INPUTS : ASSTRO, IPVAL0
OUTPUTS: ASSTRO, IPVAL0

```

400 B550
401 B551
402 B552
403 B553
404 B554
405 B555
406 B556
407 B557
408 B558
409 B559
410 B560
411 B561
412 B562
413 B563
414 B564
415 B565
416 B566
417 B567
418 B568
419 B569
420 B570
421 B571
422 B572
423 B573
424 B574
425 B575
426 B576
427 B577
428 B578
429 B579
430 B580
431 B581
432 B582
433 B583
434 B584
435 B585
436 B586

```

--- SET INVALID PAR. IN MAIN PREDICATES
INVALID SMO P4.MPRD

SOMA OFF LINE ASSEMBLER
SYNTAX CHECK
E 00F9 0000
00FA C557

437

RNO L.P

RETURN

SHEET 14

308A OFF LINE ASSEMBLER
SYNTAX CHECK

SHEET 16

438	PEJ		
439	--	CHECK FOR HEX ASCII AND CONVERT TO BINARY VALUE	
440			
441	HXS2BI	BSS	SAVE RETURN
442	RMO	L,A	
443	STA	A34RET	
444			
445	OLDA	ASSTR0	
446			
447	SUB	#2	POINT TO OS
448	STA	TEMP	
449	BSS	1	
450	LDA	*TEMP	IS
451	AND	RHBYTE	...IN RH BYTE
452	RMO	A,X	OS
453	IMO	TEMP	...IN RH BYTE
454	LDA	*TEMP	OF ASCII IN (X)
455	AND	RHBYTE	BECAUSE UNPACK IS NOT WORKING AS CLAIMED
456	RSU	A,X	(A)-START OF ASCII STRING
457	RIN	X,X	
458	OLDA	ASSTR0	CONVERT ASCII STRING TO BINARY
459	OBRL	HAS2BI	
460			
461	S00	X	CONVERSION SUCCESSFUL ?
462	BRU	HX2BI#	YES
463	SMBO	4,MPRED	NO - SET 'INVALID IN MAIN PREDICATE
464	HX2BI#	OLDE	IPVAL0
465	STE	IPVLO	
466	BSS	1	SAVE CONVERTED BIN VALUE
467	STA	*IPVLO	
468	BRU	*A34RET	RETURN
469	A34RET	BSS	1

988A OFF LINE ASSEMBLER
SYNTAX CHECK

470	PEJ		
471	--	CHECK FOR VALID GR7 MEMORY LOCATION	
472		DETERMINED BY POINTER IPVAL0	
473			
474			
475	CHKMEM BSS		
476	OLDE IPVAL0		
477	STE IPVLO		
478	LDA *IPVLO		
479	CPL GR7LO		
480	SLE	GR7 LO < MEM LOC < GR7 HI ?	
481	BRU A35.B	NO - NO GOOD	
482	CPL GR7HI		
483	SGE		
484	BRU A35.B	NO	
485	RMO L.P	RETURN	
486	A35.B		
487	SMBO P4.MPRED	INVALID SYNTAX IN MAIN PRED.	
488	RMO L.P		
489	RMBYTE DATA >FFFF		
490	GR7LO DATA B		
491	GR7HI DATA >FFFF		
492	IPVA20 DATA IPVAL2		
493	IPVA30 DATA IPVAL3		
494	IPVA40 DATA IPVAL4		
495	IPVA50 DATA IPVAL5		
496			
497			

980K OFF LINE ASSEMBLER
SYNTAX CHECK

SHEET 17

498	PEJ		
499	--	CHECK MEM LOC 1 < MEM LOC 2	
500		CALCULATE # OF WORDS	
501			
502	CHN1&2		
503	BSS		
504	LDA	*IPVA40	
505	CPL	GR7HI	MEM LOC 2 <= GR7 HI ?
506	SGE		
507	BRU	A36.2	NO
508	SUB	=IPVA20	MEM LOC 1 < MEM LOC 2 ?
509	SPL	A	NO
510	BRU	A36.2	NO
511	SNZ	A	NO
512	BRU	A36.2	YES - CONVERT TO # OF WORDS
513	LRA	1	INCLUDE THE LAST WORD
514	ADD	=1	# OF WORDS TO UPDATE
515	OSTA	NOWORD	
516	RMO	L.P	RETURN
517			
518	A36.2	SMBO P4.MPRED	SET P13 (INVALID SYNTAX) IN MAIN PREDICATES
519	RMO	L.P	

988A OFF LINE ASSEMBLER
SYNTAX CHECK

SHEET 10

520	PEJ				
521	--		CHECK IF MEM LOC 1 + # OF WORDS <= GR7 HI		
522			CALCULATE # OF WORDS		
523					
524					
525	CHMEMV	BSS			
526		LDA	#IPVA20	BYTE LOC 1	
527		ADD	#IPVA30	# OF BYTES	
528		CPL	GR7HI	<= GR7 HI LIMIT ?	
529		SGT			
530		BRU	A37.2	NO	
531		LDA	#IPVA30	# OF BYTES	
532		LRA	1	CONVERT TO # OF WORDS	
533		OSTA	NOWORD		
534		RMO	L.P		
535		A37.2	SMBO P4,MPRED	SET P4 IN MAIN PRED.	
537		RMO	L.P		

813F	84EA
8140	24EA
8141	69E7
8142	CD48
8143	7885
8144	84E6
8145	C841
8146	8488
E 8147	8888
8148	C557
8149	DB74
E 814A	888C
814B	8888
	C557

S88A OFF LINE ASSEMBLER
SYNTAX CHECK

SHEET 19

538	PEJ	
539		
540	--	CHECK FOR VALID GRAPHIC UNIT FOR C.U.K CMDS
541		
542	CHGUNI @LDA IPVAL3	GRAPHIC UNIT
543	CPTA =5	VALID UNIT? 7
544	SLT	YES
545	RMO L.P	NO - SET ERROR PRED
546	SMBO P12,MPRED	
547	RMO L.P	

#14C #400
#112
E #14D #000
#14E 6F05
#14F C000
#150 C557
#151 087C
#00C
E #152 #000
#153 C557

8080A OFF LINE ASSEMBLER
SYNTAX CHECK

548	PEJ		
549	--	SAVE START & END GR7 ADDRESSES OF BUFFER	
550			
551			
552	BSS	OLDA IPVAL2	START BYTE ADDR
553			
554	OSTA	SADDR	
555	ADD	"MWORD"	
556	DATA	NOWORD	
557	ADD	"MWORD"	END BYTE ADDR
558	OSTA	EADDR	
559	RMO	L.P	
560	--	CHECK IF ALL WORDS IN 'M' ARE CONVERTED	
561		INTO BINARY VALUES	
562			
563	CHKALC	BSS	
564	DMT	"NOARG"	
565	BRU	CHKAL9	
566	SMBO	S.SVECTR	ALL CONVERTED
567			
568	CHKAL9	RMO	L.P
569			
570	NOARG	DATA	NOARGS
571			
572			END

980A OFF LINE ASSEMBLER
SYNTAX CHECK

SHEET 21

A31.4	000F	A31.3	0003
A31.5	000F	A31.4	0004
A32.1	0001	A35.1	0124
A36.2	013C	R ABLRET	0002
ACLST1	0044	ACLST4	0055
ACLST6	0058	ACLST9	005C
APTR1	0038	ASSTR1	0003
ASSTR2	0004	ASSTR6	0008
ASSTR7	0009	ASSTR6	0400
BIT6	0000	R BIT	
R BIT6	0200	R BIT4	
BPTR2	000B	BPTR81	000D
CHKALC	000E	CHKALS	0162
CLSMAT	0016	R E	
EADDR	000F	HX281	0112
HXS281	000F	IPVA30	012B
IPVA40	012C	IPVAL4	0113
IPVAL5	0014	IPVAL6	0115
L	0005	MPRED	000C
R MPRM0	0012	NOARGO	0163
NOARG5	000F	NOARGO	0159
P12	0007	P11	000B
P13	0003	R P2	0007
R P8	0000	R P7	
R PCLUST	0009	R PALT	000A
SAVE	0054	SADDR	000E
R XB	0003	X	

A31.6	000F	A31.2	0077
A32.1	000C	A31.2	009C
A32.2	0002	A34RET	0110
A37.2	0149	ABLENT	0001
ACLST1	0044	ACLST3	0051
ACLST6	0058	ACLST6	005A
APTR1	0038	ASSTR5	0007
ASSTR3	0005	ASSTR5	0007
ASSTR6	000E	R BIT2HAS	0000
BIT1	0000	R BIT3	1000
R BIT7	0100	R BIT9	0040
R		R CHGUNT	014C
CHKMEM	0119	CHMENV	013F
CHDCLS	000D	CHDPR	000A
GR7H1	0129	HAS281	000A
INIT	0000	IPVA20	012A
R IPVA50	012D	IPVAL3	0012
R IPVAL6	0015	IPVAL6	005D
R LFCR	0A8D	R M	0003
MPRMT	002E	R NCLMAT	001A
NOSTRS	000D	NWMOD	0010
R P8	0000	R P15	000A
R P13	0000	R P16	000F
R P14	0004	R P6	0006
R P9	0009	R PACTOF	000C
R PROGNO	0000	R S	
R SVC	CAE5	TEMP	0004
R YNPM0	0011		0101

NO ERRORS 19/03/84
V3004S 15:58:07

V40155 19/03/84 15:59:07

980A OFF LINE ASSEMBLER
GR7 DISASSEMBLER

SHEET 1

GR7 DISASSEMBLER
T1980A

K. VO

ISSUE #
REVISION #

IDT V40
HED GR7 DISASSEMBLER

DEF	A5.A6.PACENO
REF	ABLENT.B12HAS.B12DAS.B12OAS
REF	ASSTR1.ASSTR2.ASSTR3.ASSTR4.ASSTR5.ASSTR6.ASSTR7
REF	IPVAL2/IPVAL3.IPVAL4/IPVAL5/IPVAL6/IPVAL7
REF	NOWORD/CBUFF.CBUFF0
REF	OPEFLD.B0003.B07FF.B07FF.B07FF.B07FF.B07FF.B07FF
REF	B7F07.B007F.B07FF.B07FF.B07FF.B07FF.B07FF.B07FF
REF	GRMASK.GRINST.GRFORM.GRAME.VTC
REF	RPRB.GI.H0UF1.H0UF2.H0UF2.NOINS

- 1
- 2
- 3
- 4
- 5
- 6
- 7
- 8
- 9
- 10
- 11
- 12
- 13
- 14
- 15
- 16
- 17
- 18
- 19
- 20

988A OFF LINE ASSEMBLER
CR7 DISASSEMBLER

21	###	PEJ	EQU	1	EQU	1
22	###	EQU	2	EQU	2	EQU
23	###	EQU	3	EQU	3	EQU
24	###	EQU	4	EQU	4	EQU
25	###	EQU	5	EQU	5	EQU
26	###	EQU	6	EQU	6	EQU
27	###	EQU	7	EQU	7	EQU
28	###	EQU	1	EQU	1	EQU
29	###	EQU	3	EQU	3	EQU
30	###	OPD	>CAEM,3	OPD	>CAEM,3	OPD
31	###	EQU	>8A8D	EQU	>8A8D	EQU
32	###	BRS	PROGNO	BRS	PROGNO	BRS
33	###	EQU	1	EQU	1	EQU
34	###	EQU	2	EQU	2	EQU
35	###	EQU	3	EQU	3	EQU
36	###	EQU	4	EQU	4	EQU
37	###	EQU	5	EQU	5	EQU
38	###	EQU	6	EQU	6	EQU
39	###	EQU	7	EQU	7	EQU
40	###	EQU	8	EQU	8	EQU
41	###	EQU	9	EQU	9	EQU
42	###	EQU	10	EQU	10	EQU
43	###	EQU	11	EQU	11	EQU
44	###	EQU	12	EQU	12	EQU
45	###	EQU	13	EQU	13	EQU
46	###	EQU	14	EQU	14	EQU
47	###	EQU	15	EQU	15	EQU
48	###	EQU	16	EQU	16	EQU
49	###	EQU	17	EQU	17	EQU
50	###	EQU	18	EQU	18	EQU
51	###	EQU	19	EQU	19	EQU
52	###	EQU	20	EQU	20	EQU
53	###	EQU	21	EQU	21	EQU
54	###	EQU	22	EQU	22	EQU
55	###	EQU	23	EQU	23	EQU

988A OFF LINE ASSEMBLER
CR7 DISASSEMBLER

SHEET 3

56	PEJ			
57	A6	DISASSEMBLE CR7 INSTRUCTIONS INTO MNEMONIC FORMS AND OUTPUT TO LINE PRINTER		
58	BSS		OPEN LP SPOOL ON LUN 3	
59	OLDM -PRBA0			
60	SVC 8		FLAG TO DIRECT OUTPUT TO LP	
61	LDA -1			
62	OSTA LORH			
63	OLDA -HBUF1		INIT POINTERS TO H BUFFER	
64	OSTA IGBUF0			
65	OSTA ACBUF0			
66	LDA -1		RESET PAGE NO. TO 1ST PAGE	
67	OSTA PAGENO			
68	OLDM -HD1PRL			
69	SVC #			
70	OLDM -HD2PRL			
71	SVC #			
72	OLDM -HD3PRL			
73	SVC #			
74	BRU L007		'H' SHARES THE SAME ABL WITH 'L'	
75	A5	DISASSEMBLE CR7 INSTRUCTIONS INTO MNEMONIC FORM AND OUTPUT TO CONSOLE		
76	BSS #			
77	LDA -#			
78	OSTA HD1PRB			
79	OSTA HD2PRB			
80	OSTA HD3PRB			
81	OSTA OPCPRB			
82	OSTA LORH			
83	A5			
84	L005			
85	PEJ			
86	PEJ			
87	PEJ			
88	PEJ			
89	PEJ			
90	PEJ			
91	PEJ			
92	PEJ			
93	PEJ			
94	PEJ			
95	PEJ			

988A OFF LINE ASSEMBLER
GR7 DISASSEMBLER

SHEET 4

P 0028 042E	96	SVC #	2ND	
0029 CAEB	97	OLDM -HD2PRB		
002A 1800				
P 002B 044B	98	SVC #	3RD	
002C CAEB	99	OLDM -HD3PRB		
002D 1800				
P 002E 0469	100	SVC #	RESET 1ST 'L' FLAG	
002F CAEB	101	SMBO #.FSTL		
0030 0B70				
P 0031 00A7	102		INIT BUFFER POINTER	
0032 0000	103	OLDA -CBUFF		
0033 0012				
E 0034 0400	104	OSTA ACBUF0		
0035 00A2				
P 0036 0400	105	OSTA ICBUF0		
P 0037 00A1	106			
0038 C550	107	RMO L.A	SAVE RETURN	
0039 0016	108	STA ASRETN		
003A 0700	109			
003B 0400	110	LDA -S	RESET # OF WORDS PROCESSED	
P 003C 00A4	111	STA -S+1		
003D 0400	112	DATA MOVDP		
P 003E 00A5	113	OSTA LINENO	RESET LINE #	
003F 0400	114			
0040 0000	115	OLDA IPVAL2	INIT GR7 MEM LOCATION	
E 0041 0400	116	OSTA GRLOC1		
P 0042 00A0	117	OLD 0000	BLANK OUT LINE BUFFER	
0043 000E	118	LDX --80/2		
0044 1708	119	ODST OPBUF+40,X		
0045 A600				
P 0046 007C	120	RIM X,X		
0047 C322	121	BIX A51.2		
0048 00FC				

988A OFF LINE ASSEMBLER
CR7 DISASSEMBLER

SHEET 5

122	PEJ		
123	*		
124	*		
125	*		
126	*		
127	*		
128	*		
129	DISAS#	BSS	INPUT : NOWORD
130	LDA	#	
131	STA	SVECTR	CLEAR PREDICATES FOR ABL PROGRAM
132	OLDM	-PROGNO	ABL PROGRAM
133	OBRL	ABLENT	
134	BRU	*ASRETM	YES - RETURN
135	ASRETM	BSS	
136	VTCC	DATA	
137	VTCC	DATA	
138	BBBB	DATA	
139	OFBUF	BSS	OUTPUT BUFFER
140	OFBUF	DATA	LECR

0049	0070	
004A	0038	
004B	1800	
P 004C	0070	
004D	7400	
	0000	
E 004E	0000	
004F	7C00	
0050		
E 0051	0025	
0052	0000	
0053	0000	
0054	0000	
007C	0000	

988A OFF LINE ASSEMBLER
 CR7 DISASSEMBLER

SHEET 6

141	PEJ				
142					
143					
144					
145					
146					
147					
148					
149					
150					
151					
152					
153					
154					
155					
156					
157					
158					
159					
160					
161					
162					
163					

ABL PROGRAM SECTION

0070	0003	PROGNO	DATA 3	PROGRAM NO.	
007E	0000	PCLUST	DATA 0	PRESENT CLUSTER	
007F	0000	PALT	DATA 0	ALT	
0080	0000	PACT	DATA 0	ACTION	
0081	0000	PACTOF	DATA 0	ACTION OFFSET IN ALTERNATIVE	
0082	0000	ACLSTO	DATA 0	PTR TO ACTION LIST FORALTY BEING EXE'D	
0083	0000	SVECTR	DATA 0	STATE VECTOR	
0084	00A8	CLSMAG	DATA	CLSMAT	POINTER TO CLUSTER MATRIX
0085	00AE	NCLMAG	DATA	NCLMAT	NEXT-CLUSTER VECTOR
0086	00C4	YNPMAG	DATA	YNPMAT	PREDICATE MATRIX
0087	00DA	MPRMAG	DATA	MPRMAT	PRED MASK MATRIX
0088	00F0	APTR00	DATA	APTR00	ACTION LIST POINTERS
0089	0000	BREG	DATA 0		(0)-ADDRESS OF PREVIOUS ABSTR. PROGRAM
008A	0000	LREG	DATA 0		(1)-RETURN ADDRESS

PEJ	DATA SECTION
164	
165	
166	
167	
168	
169	MNEM0 DATA MNEM1
170	MNEM1 BSS 2
171	MNEM2 BSS 3
172	MNEM3 DATA 1
173	MNEM3 BSS 3
174	MNEM4 DATA 2
175	MNEM4 BSS 2
176	MNEM5 BSS 2
177	MNEMPTR DATA MNEM1
178	MNEM10 DATA MNEM2
179	MNEM10 DATA MNEM3
180	MNEM10 DATA MNEM4
181	
182	
183	IBUFF0 BSS 1
184	IBUFF1 BSS 2
185	GRLOC1 BSS 1
186	IGRUF0 BSS 1
187	AGRUF0 BSS 1
188	LORH BSS 1
189	NOVDP BSS 1
190	LINE0 BSS 1
191	LPERP EQU 38
192	PAGENO BSS 1
193	FSTL DATA 0

P 008B 008C
 008C 008E
 0091 00AC
 0092 00AC
 0095 00AC
 0096
 0098
 0099 008C
 P 009A 008E
 P 009B 0092
 P 009C 0096
 009D
 009E
 00A0
 00A1
 00A2
 00A3
 00A4
 00A5
 00A6 0026
 00A7 0000

MNEMONIC STRING 1
 MNEMONIC STRING 2
 MNEMONIC STRING 3
 MNEMONIC STRING 4
 POINTER USED TO POINT MNEM0 TO NEXT STRG
 PTR TO INST BUFFER
 MEM LOC 1
 POINTER TO INST. VD.
 ADDR. WORD
 BITS=0 FOR 'L', -1 FOR 'H'
 NO. OF WORDS PROCESSED
 LINE # WITHIN THE PAGE
 NO. OF LINES / PAGE
 PAGE NO. (FOR 'H')
 00=0 J 1ST 'L' COMMAND

900A OFF LINE ASSEMBLER
CR7 DISASSEMBLER

243	PEJ			
244	*			
245	*	CLUSTER MATRIX	BITS#-31	
246	*			
247		CLSMAT DATA	>C000.# C1	
248		DATA	>3FFF.>C000 C2	
249		DATA	>0000.>3C00 C3	

NEXT CLUSTER

250	*				
251	*				
252	*				
253	*				
254		NCLMAT	DATA 2	ALT	2
255		DATA	DATA 1		1
256		DATA	DATA 1		3
257		DATA	DATA 1		4
258		DATA	DATA 1		5
259		DATA	DATA 1		6
260		DATA	DATA 1		7
261		DATA	DATA 1		8
262		DATA	DATA 1		9
263		DATA	DATA 1		10
264		DATA	DATA 1		11
265		DATA	DATA 3		12
266		DATA	DATA 3		13
267		DATA	DATA 3		14
268		DATA	DATA 3		15
269		DATA	DATA 3		16
270		DATA	DATA 1		17
271		DATA	DATA 1		18
272		DATA	DATA 1		19
273		DATA	DATA 1		20
274		DATA	DATA 1		21
275		DATA	DATA 1		

V/M PREDICATE MATRIX

276	*				
277	*				
278	*				
279	*				
280		VNPMAT	DATA >0000	ALT	1
281		DATA	>0000		2
282		DATA	>0000		3
283		DATA	>1000		4
284		DATA	>2000		5
285		DATA	>3000		6
286		DATA	>4000		7
287		DATA	>5000		8
288		DATA	>6000		9
289		DATA	>7000		10
290		DATA	>8000		11
291		DATA	>9000		12
292		DATA	>A000		13
293		DATA	>B000		14
294		DATA	>C000		15
295		DATA	>D000		

988A OFF LINE ASSEMBLER

SR7 DISASSEMBLER
 #004 #000 296
 #005 #000 297
 #006 #200 298
 #007 #000 299
 #008 #100 300
 #009 #400 301
 302
 303
 304
 305

MASK FOR PREDICATE MATRIX

MPRMAT
 #000 #400
 #001 #400
 #002 #400
 #003 #400
 #004 #400
 #005 #400
 #006 #400
 #007 #400
 #008 #400
 #009 #400
 #010 #400
 #011 #400
 #012 #400
 #013 #400
 #014 #400
 #015 #400
 #016 #400
 #017 #400
 #018 #400
 #019 #400
 #020 #400
 #021 #400

ACT

DATA >#000
 DATA >#000
 DATA >#200
 DATA >#000
 DATA >#100
 DATA >#400

1
2
3
4
5
6
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21

ACTION MATRIX

APTR00
 #100 #100
 #101 #100
 #102 #100
 #103 #100
 #104 #100
 #105 #100
 #106 #100
 #107 #100
 #108 #100
 #109 #100
 #110 #100
 #111 #100
 #112 #100
 #113 #100
 #114 #100
 #115 #100
 #116 #100
 #117 #100
 #118 #100
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 #120 #100
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 #127 #100
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 #165 #100
 #166 #100
 #167 #100
 #168 #100
 #169 #100
 #170 #100
 #171 #100
 #172 #100
 #173 #100
 #174 #100
 #175 #100
 #176 #100
 #177 #100
 #178 #100
 #179 #100
 #180 #100

988A OFF LINE ASSEMBLER
CR7 DISASSEMBLER
P 8185 8189 352

DATA ACLS21

SHEET 11

98A OFF LINE ASSEMBLER
GR7 DISASSEMBLER

SHEET 12

353	PEJ				
354					
355					
356					
P 0106	010C		RECOGNIZE OP-CODE		
P 0107	01A4		INITIALIZATION		
P 0108	035C		RECOGNIZE OP-CODE & SET PRED.		
P 0109	0000				
361					
362					
363					
P 010A	0378		ALL WORDS PROCESSED		
P 010B	0000		WRITE TO LP		
365					
366					
367					
368					
P 010C	023E		FORMAT #		
P 010D	0271		FORMAT ADDR & CONTENT IN DEC,HEX,OCT		
P 010E	033C		PLACE MNE STRS IN BUFF, O.P. TO CONS		
P 010F	0353				
P 0110	0000				
374					
375					
376					
377	0015		FORMAT 1		
378	0000				
P 0111	01C5		CONVERT BIN. INT. TO HEX		
P 0112	023E		FORMAT ADDR & CONTENT IN DEC,HEX,OCT		
P 0113	0271		PLACE MNE STRS IN BUFF, O.P. TO CONS		
P 0114	033C				
P 0115	0353				
P 0116	0000				
P 0117	0000				
384					
385					
386					
387	0016		FORMAT 2		
388	01C5				
P 0118	023E		CONVERT BIN. INT. TO HEX		
P 0119	0271		FORMAT ADDR & CONTENT IN DEC,HEX,OCT		
P 011A	033C		PLACE MNE STRS IN BUFF, O.P. TO CONS		
P 011B	0353				
P 011C	0000				
P 011D	0000				
P 011E	0000				
394					
395					
396					
397	0017		FORMAT 3		
398	01EA				
P 011F	0000				
P 0120	01EA		CONVERT REAL BIN. TO HEX		
P 0121	023E		FORMAT ADDR & CONTENT IN DEC,HEX,OCT		
P 0122	0271		PLACE MNE STRS IN BUFF, O.P. TO CONS		
P 0123	033C				
P 0124	0353				
P 0125	0000				
404					
405					

987A OFF LINE ASSEMBLER
GR7 DISASSEMBLER

SHEET 13

E 0126	0018	486	ACLST6	DATA	03F00	
P 0127	01EA	487		DATA	RL2HX	CONVERT REAL BIN. TO HEX
	0019	488		DATA	0003F	
E 0128	01EA	489		DATA	RL2HX	CONVERT REAL BIN. TO HEX
P 0129	023E	410		DATA	FORLN	FORMAT ADDR & CONTENT IN DEC.HEX.OCT
P 012A	0271	411		DATA	MNE2LN	PLACE MNE STRS IN BUFF. O.P. TO CONS
P 012B	033C	412		DATA	SWADV	
P 012C	033C	413		DATA	CHKALL	
P 012D	0353	414		DATA	#	
P 012E	0000	415		DATA	#	
		416				
		417				
		418				
		419	ACLST7	DATA	007FF	- ALT 7 : FORMAT 5
E 012F	0000	420		DATA	RL2HX	CONVERT REAL BIN. TO HEX
P 0130	01EA	421		DATA	FORLN	FORMAT ADDR & CONTENT IN DEC.HEX.OCT
P 0131	023E	422		DATA	MNE2LN	PLACE MNE STRS IN BUFF. O.P. TO CONS
P 0132	0271	423		DATA	SWADV	
P 0133	033C	424		DATA	CHKALL	
P 0134	0353	425		DATA	#	
P 0135	0000	426		DATA	#	
		427				
		428				
		429	ACLST8	DATA	0003F	- ALT 8 : FORMAT 6
E 0136	0000	430		DATA	INT2HX	CONVERT BIN. INT. TO HEX
P 0137	01C5	431		DATA	FORLN	FORMAT ADDR & CONTENT IN DEC.HEX.OCT
P 0138	023E	432		DATA	MNE2LN	PLACE MNE STRS IN BUFF. O.P. TO CONS
P 0139	0271	433		DATA	SWADV	
P 013A	033C	434		DATA	CHKALL	
P 013B	0353	435		DATA	#	
P 013C	0000	436		DATA	#	
		437				
		438				
		439	ACLST9	DATA	SWAPB	SWAP AROUND BYTES IN TEXT INST.
P 013D	02F3	440		DATA	07F00	
E 013E	0000	441		DATA	ASCII	EXTRACT ASCII
P 013F	0226	442		DATA	0007F	
E 0140	0000	443		DATA	ASCII	EXTRACT ASCII
P 0141	0226	444		DATA	FORLN	FORMAT ADDR & CONTENT IN DEC.HEX.OCT
P 0142	023E	445		DATA	WRLVCB	PLACE MNE STRS IN BUFF. O.P. TO CONS
P 0143	02F7	446		DATA	SWADV	
P 0144	033C	447		DATA	CHKALL	
P 0145	0353	448		DATA	#	
P 0146	0000	449		DATA	#	
		450				
		451				
		452	ACLST10	DATA	0007F	- ALT 10 : FORMAT 8
E 0147	0000	453		DATA	ASCII	EXTRACT ASCII
P 0148	0226	454		DATA	FORLN	FORMAT ADDR & CONTENT IN DEC.HEX.OCT
P 0149	023E					

980A OFF LINE ASSEMBLER

CR7 DISASSEMBLER
P 0314A 0271
P 0314B 033C
P 0314C 0353
P 0314D 0000

DATA MNE2LN
DATA SVADV
DATA CHKALL
DATA

- ALT 11 : FORMAT 9

014E 0313
P 014E 001E
E 014F 0000
P 0150 0353
P 0151 0000

ACLS11 BSS
DATA ADJPTR
DATA 0FFFF

ADJUST INST & ADDR PTRS FOR DW INST.

CHECK ALL WORDS IN BUFFER PROCESSED

- ALT 12 : FORMAT 10

0152 0313
P 0152 001F
E 0153 0000
P 0154 0353
P 0155 0375
P 0156 0000

ACLS12 BSS
DATA ADJPTR
DATA 07FFF

PICK UP OPERAND FIELD

SET PREDICATE FOR INDIRECT ADDR.

- ALT 13 : FORMAT 11

0157 0313
P 0157 0020
E 0158 0000
P 0159 01C5
E 015A 0000
P 015B 01C5
E 015C 0000
P 015D 0353
P 015E 0000

ACLS13 BSS
DATA ADJPTR
DATA 00030

CONVERT BIN. INT. TO HEX

CONVERT BIN. INT. TO HEX

- ALT 14 : FORMAT 12

015F 0313
P 015F 0015
E 0160 0000
P 0161 01C5
E 0162 0000
P 0163 0353
P 0164 0000

ACLS14 BSS
DATA ADJPTR
DATA 00003

CONVERT BIN. INT. TO HEX

- ALT 15 : FORMAT 13

0165 0313
P 0165 001E

ACLS15 BSS
DATA ADJPTR
DATA 0FFFF

9000 OFF. LINE ASSEMBLER
CR7 DISASSEMBLER
#1000 0000

DATA #

557 *
558 *
559 *
560 *

P #189 #23E
P #18A #271
#1000 0000

ACLS21
DATA FORLN
DATA MNEZLN
DATA #

ALT 21 : 1ST WORD OF DW INST. IS LAST WORD

SHEET 16

564	PEJ			
565	*	ACTION SECTION		
566	*	*****		
567	*			
568	*			
569	*	INITIALIZATION		
570	*			
571	*			
572	INIT	BSS #	BLANK OUT MNEM STRINGS	
573		DLD DDB		
574		VDST MNEM1		
575		DST MNEM2		
576		STA MNEM2+2		
577		DST MNEM3		
578		STA MNEM3+2		
579		DST MNEM4		
580		OSTA OPBUF+21		
581	*		PLACE 'LFCR' AT LAST POSITION	
582	*	OLDA -LFCR		
583				
584		OSTA OPBUF+34		
585		OLDA -MNEM10	INITIALIZE POINTERS	
586		S MNEPTR		
587		OLDA -MNEM1		
588		STA MNEM0		
589		RMO L.P		
590	*		RETURN	
591	AS1RET BSS 1			
592	DDBB DATA :			

988A OFF LINE ASSEMBLER
CR7 DISASSEMBLER

SHEET 10

```

593 PEJ
594 *
595 * -- RECOGNIZE OP-CODE, MNEMONIC
596 * SET PRED FOR FORMAT TYPE
597 *
598 * INPUT : A5CNT,CBUFF0
599 * OUTPUT : MNEM1, P5/.../P12,MNEM0
600 *
601 REOPCO BSS #
602 *
603 *
604 *
605 *
606 *
607 *
608 *
609 *
610 *
611 *
612 *
613 *
614 *
615 *
616 *
617 *
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619 *
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621 *
622 *
623 *
624 *
625 *
626 *
627 *
628 *
629 *
630 *
631 *
632 *
633 *

```

REOPCO BSS #
 *LDX NOINS #
 A52.0 LDA #IGBUFF0
 AND #S+1.X
 DATA GRMASK
 CPA #S+1.X
 DATA GRINST
 SNE
 BRU A52.2
 BIX A52.0
 SHBO P4,SVECTR
 RMO L.P
 LDA #S+1.X
 DATA GRFORM
 CRA 4
 STA SVECTR
 RAD X.X
 DLD #S+1.X
 DATA GRMNE
 @DST MNEM1
 DST *MNEM0
 IMO MNEPTR
 LDA *MNEPTR
 STA MNEM0
 RMO L.P
 SABOIN SABO #

RECOGNIZE OP-CODE
 NO. OF INSTRUCTIONS IN REPERTOIRE (-VE)
 GR7 INSTRUCTION
 KEEP OP-CODE
 OP-CODE MATCHED ?
 YES
 NO - TRY NEXT
 NO VALID OP-CODE FOUND
 RETURN
 VALID OP-CODE
 GET FORMAT #
 CHANGE FORMAT# TO A5 PRED
 GET MNEMONIC INTO MNEM1
 DOUBLE INDEX
 GET MNEMONICS FOR OP-CODE
 PLACE IT IN 1ST STRING
 POINT TO NEXT MNEM STRG
 RETURN

980A OFF LINE ASSEMBLER

CR7 DISASSEMBLER

01E1 0518 684
 01E2 010E 685
 01E3 7C00 686
 01E4 05.16R 687
 01E5 089 VALUE 688
 01E6 690 ZSUPP 689

LDA
 STA
 BRU

*MNEPTR
 MNEMO
 *A5.15R

A5.16R BSS 1
 VALUE BSS 1
 ZSUPP BSS 1

VALUE OF CURRENT WORD IN BUFFER
 -1: SUPPRESS ZERO

988A OFF LINE ASSEMBLER
CR7 DISASSEMBLER

SHEET 21

Address	Instruction	PEU	Description
691			CONVERT REAL VALUE IN ADDRESS WORD, SPECIFIED BY 'OPEFLD', INTO HEX ASCII
692			INPUT : OPEFLD, ACBUFO, MNEMO
693			OUTPUT : MNEM(N), MNEMO
694			
695			
696			
697			
698			
699			
700			
701			
702			
703			
704			
705			
706			
707			
708			
709			
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739			
740			
741			

988A OFF LINE ASSEMBLER

GR7 DISASSEMBLER	742	BRU	A5.162	+VE	
	743	SMBO	#,NEG#	-VE	
P	744	ADD	NMASK		SET ALL BITS TO THE LEFT TO 1
	745	RIV	A.A		TAKE ITS MAGNITUDE
	746	ADD	-1		
	747	LDE	--1		SUPPRESS LEADING ZEROS
	748	BRL	-S+1		CONVERT +VE VALUE TO HEX ASCII
	749	DATA	BIZHAS		
E	750	TMBO	#,NEG#		TEST FOR SIGN
	751	BRU	A5.164	+VE #	
	752	LDX	NEGSGN	-VE #	PLACE '-' SIGN
	753	STX	"MNM#		POINT TO NEXT WORD WITHIN STRING
	754	IMO	MNM#		PLACE IN MNE STRG
	755	DST	"MNM#		
	756				POINT TO NEXT STRING
	757	IMO	MNEPTR		
	758	LDA	"MNEPTR		
	759	STA	MNM#		
	760	BRU	"A5.16R		RETURN
	761				
	762	FDPOS	BSS 1		FIELD POSITION (# OF BITS FROM BIT16)
	763	NMASK	BSS 1		NEG MASK
	764	RSBIT	BSS 1		REL SIGN BIT
	765	NEGSGN	DATA >ADAB		NEGATIVE SIGN
	766	NEG#	BSS 1		B#-1 : NEG
	767	LRA#	LRA #		
	768	TABO#	TABO #		
	769	A5.16R	BSS 1		
	770	SABZ#	SABZ #		
	771				
	772				
	773				
	774				
	775				
	776				
	777				
	778				
	779				
	780				
	781				
	782				
	783				
	784				
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	820				
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	822				
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	824				
	825				

988A OFF LINE ASSEMBLER
 CR7 DISASSEMBLER

SHEET 23

771	PEJ		
772	--		TAKE ASCII FROM FIELD SPECIFIED BY 'OPEFLD'
773			AND PLACE IT IN APPROPRIATE MNE STRG
774			INPUT 'OPEFLD,MNEMO,IGBUF@
775			OUTPUT 'MNEM(N)
776			
777			
778			
779	BSS	#	GET INSTRUCTION
780	LDA	'IGBUF@	ISOLATE ASCII
781	AND	'OPEOFL	
782	OPEOFL	DATA	OPEFLD
783	RMO	A,M	GET MNE STRG
784	LDE	'MNEM@	BLANK OUT THE POSITION FIRST
785	LDA	'OPEOFL	
786	RIV	A,A	
787	RAM	E,A	
788	RAD	M,A	PLACE ASCII IN
789	STA	'MNEM@	... THE SAME MNEH STRG
790	RMO	L,P	
791	--		ASSEMBLE INDIRECT BIT
792			
793			
794	IMDBIT	#	'AGBUF@
795	LDA	'AGBUF@	(-LDA 'GIBUFF@)
796	TABO	#	INDIRECT BIT - 1 7
797	RMO	L,P	NO
798	LDA	\$+1	YES - PLACE '..' IN FRONT OF MNEMONICS
799	DATA	'..'	
800	@STA	OPBUF+2#	
801	RMO	L,P	RETURN
802			

980A OFF LINE ASSEMBLER
GR7 DISASSEMBLER

PEU	FORMAT ADDRESS & CONTENT OF ADDRESS WORD	INPUT : IPVAL2.ASCNT.ACBUF0	OUTPUT : CM0LN
803			
804			
805			
806			
807			
808			
809			
810	AFORLN 0LDA ACBUF0		
811	0STA GBUF00		
812	BRU FORLNS		
813			
814	BSS #		
815	0LDA IGBUF0		
816	0STA GBUF00		
817	FORLNS RMO L.A		SAVE RETURN
818	STA A5.19R		
819			
820			
821	0LDA IPVAL2		FORMAT ADDRESS CURRENT GR7 ADDR
822	ADD *NOWDP0		* OF BYTES PROCESSED
823	ADD *NOWDP0		
824	DATA NOWDP		ADJUSTMENT
825	RDE A.A		
826	RDE A.A		
827	STA ADDRESS		
828			
829	BRL *S+1		CONVERT TO DEC ADDR
830	DATA B12DAS		
831	0DST OPBUF+2.		
832	0STX OPBUF+1		
833			
834	LDA ADDRESS		CONVERT TO HEX ADDR
835	BRL *S+1		
836	DATA B12HAS		
837	0DST OPBUF+5		
838			
839	LDA ADDRESS		CONVERT TO OCTAL ASCII
840	BRL *S+1		
841	DATA B12OAS		
842	0DST OPBUF+9		
843	0STX OPBUF+8		
844			

980A OFF LINE ASSEMBLER
GR7 DISASSEMBLER

SHEET 25

#25E	040E	845	LDA	*GBUF00
#25F	7400	847	BRL	*S+1
	0001	848	DATA	B12HAS
E	#260	849	0DST	OPBUF+13
	0400			
P	#262	850	LDA	*GBUF00
	0061	851	BRL	*S+1
	#263	852	DATA	B12DAS
	7400			
	#264	853	0STX	OPBUF+16
	0003	854	0DST	OPBUF+17
	0000			
E	#265	855	BRU	*A5.19R
	9400	856		
P	#266	857	A5.19R	BSS I
	0064	858	ADDRSS	BSS I
	#267	859	GBUF00	BSS I
	0400			
P	#269			
	0065			
	#26A			
	7C00			
	#268			
	#26C			
	#26D			

FORMAT CONTENT

CONVERT TO HEX ASCII

CONVERT TO OCTAL ASCII

RETURN

CURRENT GR7 ADDRESS
EITHER IGBUF0 OR AGBUF0

988A OFF LINE ASSEMBLER
GR7 DISASSEMBLER

860
861
862
863
864
865
866

#26E DB77
P #26F #883
#27B C657

PEJ

SET PREDICATE P7

BSS #
SMBO 7,SVECTR
RMO L,P

SHEET 26

980A OFF LINE ASSEMBLER
CR7 DISASSEMBLER

SHEET 27

867	PE3		
868	--	PLACE MNEMONICS FOR INST. INTO 'CMDLN'	
869		AND OUTPUT TO CONSOLE	
870		INPUT : MNEM1,...,MNEM4	
871		OUTPUT : CMDLN	
872			
873			
874			
875	MNE2LN BSS		SAVE RETURN
876	RMO L,A		
877	STA MNERET		
878			
879		PACK MNEM2,...,MNEM4 INTO ONE CONTIGUOUS STRING	
880			
881	PCKSTR BSS		RESET POINTERS
882	DLN -B		
883	DST TPTR		
884	RMO B,S		
885			
886		OLD MNEM1	PLACE OPCODE MNEM IN OP BUFFER
887		ODST OPBUF+21	
888			
889		OLD MNEM2	
890		ODST OPBUF+24	
891		OLD MNEM2+2	
892		ODST OPBUF+26	
893			
894		OLD MNEM3	
895		ODST OPBUF+28	
896		OLD MNEM3+2	
897		ODST OPBUF+30	
898			
899		OLD MNEM4	
900		ODST OPBUF+32	
901		OLDM -OPBUF+24	START OF STRING
902	PCKS BRL	GETCHA	GET CHAR. POINTED BY FPTR TO (A)
903			
904		CPA -> B	BLANK ?
905	SEQ		
906	BRU PCKS#2		NO - MOVE IT
907	BRU PCKS#1		YES
908			

908A OFF LINE ASSEMBLER
CR7 DISASSEMBLER

#2C7	###	959	DATA 2		
#2C8	###	960	DATA #		
#2C9	###	961	DATA OPBUF	REC #	
#2CA	###	962	DATA #	WORD#	
#2CB	###	963	DATA #	WORD COUNT	
#2CC	###	964	DATA 35		
		965			
		966	OUTPUT LINE TO CONSOLE		
#2CD	###	967	DATA #	CONSOLE	
#2CE	###	968	DATA 2	WRITE	
#2CF	###	969	DATA 7#	CHAR COUNT	
#2D0	###	970	DATA OPBUF	BUFFER	
		971			
#2D1		972	HMERET BSS 1		
		973			
		974			
		975			
		976	GETCHA RMO M,B		
#2D2	C536	977	LDA FPTR	-OFFSET IN WORD	
#2D3	###	978	LRA 1		
#2D4	C841	979	RAD A,B		
#2D5	C886	980	LDA #,BR	GET WORD CONTAINING CHAR.	
#2D6	###	981	TMBO 15,FPTR	ISOLATE CHAR. IN RH BYTE	
#2D7	D83F	982			
#2D8	###	983	LRA 8		
#2D9	C848	984	@AND ->###		
#2DA	###	985	RMO L,P		
#2DB	###	986			
#2DC	C557	987			
		988	PUTCHA RMO A,E	(E)=CHAR.	
#2DD	C536	989	LDA TPTR	START OF STRINGS	
#2DE	###	990	LRA 1	OFFSET IN WORD	
#2DF	C841	991	RAD A,B		
#2E0	C886	992	LDA #,BR	DESTN WORD	
#2E1	###	993	TMBO 15,TPTR	INTO WHICH BYTE ?	
#2E2	D83F	994			
#2E3	###	995	BRU PCKS#5	LH	
#2E4	###	996	@AND ->###	RH	
#2E5	7804	997			
#2E6	3800	998	RAD A,E		
#2E7	FF00	999	BRU PCKS#8		
#2E8	C881	1000	@AND ->###		
#2E9	7804				
#2EA	3800				
#2EB	###				
#2EC	CA28	1001	CRE 8		
#2ED	C881	1002	RAD A,E		
#2EE	9900	1003	STE #,BR	PLACE CHAR. IN BYTE	
#2EF	C557	1004	RMQ L,P		
		1005			
#2F0	###	1006	TPTR DATA #	TO POINTER	
#2F1	###	1007	FPTR DATA #	FROM POINTER	
#2F2	###	1008	BSS 1	CHAR TO BE MOVED	

986A OFF LINE ASSEMBLER
CR7 DISASSEMBLER

SHEET 30

1009	PEJ		
1010			
1011			
1012			
1013	SWAPB	SWAP AROUND 2 ASCII IN 'TEXT' INSTRUCTION	
1014	BSS		
1015	LDA	IGBUFO	GET INSTRUCTION IN BUFFER
1016	CRA	B	SWAP BYTES
1017	STA	IGBUFO	
1018	RMO	L,P	

02F3 0524
02F4 CAB8
02F5 0524
02F6 C557

388A OFF LINE ASSEMBLER
CR7 DISASSEMBLER

SHEET 31

1018	PEJ		
1019			
1020			
1021			
1022			
1023			
1024			
1025			
1026			
1027	WRLVCB	BSS	#
1028	RMO	L,A	
1029	STA	MNERET	
1030			
1031			
1032			
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1038			
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1046			
1047	L00LP		
1048			
1049			

02F7 C550
 02F8 00D8
 02F9 B400
 02FA 0331
 02FB 17F6
 02FC A500
 02FD 0076
 02FE C322
 02FF 40FC
 0300 B400
 0301 009C
 0302 A400
 0303 0059
 0304 B400
 0305 008E
 0306 A400
 0307 006C
 0308 D02F
 0309 00A3
 030A 7004
 030B 1000
 030C 02CD
 030D 0CA0
 030E 70C2
 030F 1000
 0310 02C6
 0311 CA00
 0312 70BE

INPUT : MMEM1,MMEM2
 OUTPUT : CMDLN
 SAVE RETURN
 BLANK OUT MMEM FIELDS IN O/P BUFFER
 LDY. --1/7
 0DST OPBUF+34,X
 R1N X,X
 B1X S-3
 0DLD MMEM1
 PLACE OPCODE MMEM IN OP BUFFER
 0DST OPBUF+21
 0DLD MMEM2
 0DST OPBUF+24
 TMBZ 15,LORH
 BRU L00LP
 0LDM -OPCPRB
 SVC #
 BRU *MNERET
 0LDM -LPSPRB
 SVC #
 BRU *MNERET

OUTPUT TO LP SPOOL
 OUTPUT LINE BUFFER TO CONSOLE

980A OFF LINE ASSEMBLER
CR7 DISASSEMBLER

SHEET 32

1050	PEJ			
1051	--	SET ADDRESS POINTER ONE LOCATION AFTER		
1052		INSTRUCTION POINTER (FOR 2-WORD INST.)		
1053				
1054				
1055	ADJPTR	LDA IGBUF0		
1056		RIN A.A		
1057		TMBO 15.LORH	FOR 'L' OR 'H' ?	
1058		BRU L100	'L'	
1059		RMO L.E	'H' - SAVE RETURN	
1060		STE ADJRET		
1061		0BRL BUMPA	ADJUST ACBUF0	
1062		BRU -ADJRET		
1063	L100	STA ACBUF0		
1064		RMO L.P		
1065				
1066	ADJRET BSS 1			

0313	0124
0314	0300
0315	003F
0316	00A3
0317	7005
0318	C551
0319	8005
031A	7A00
031B	0309
031C	7C02
031D	8125
031E	C557
031F	

988A OFF LINE ASSEMBLER
CR7 DISASSEMBLER

1867	PEJ		
1868			
1869			
1870			
1871	OPADVA	0DLD	D888 BLANK OUT MNEM FIELDS
1872	LDX	--14	
1873	0DST	OPBUF+34.X	
1874	RIN	X.X	
1875	BIX	S-3	
1876			
1877	TMBZ	15.LORH	FOR WHICH COMMAND ?
1878	BRU	L#15#	'H'
1879	0LDM	-OPCPRB	'L'
1880	BRU	L#155	
1881			
1882	L#15#	0LDM	-LPSPRB
1883	L#155	SVC	#
1884		RMO	L,P
1885		DATA	.
1886		D888	
1887			
1888			
1889			
1890			
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1899			
1900			

988A OFF LINE ASSEMBLER
CR7 DISASSEMBLER

SHEET 36

1168 PEJ
1161 *
1162 *
1163 *
1164 INDIRT BSS #
1165 SHBO 6,SVECTR
1166 RMO L,P

-- SET PREDICATE FOR INDIRECT ADDR.

#375 DB76
P #376 #883
#377 C557

988A OFF LINE ASSEMBLER
 GR7 DISASSEMBLER

SHEET 37

1167	PEJ		
1168	--	WRITE LP SPOOL TO LINE PRINTER IF 'H' COMMAND	
1170	BSS		
1171	TMBO	15,LORH	
1172			
1173	RMO	L.P	'L' - CLOSE LP SPOOL
1174	QLDM	-PRBC	'H' - CLOSE LP SPOOL
1175	SVC	8	
1176	RMO	L.R	
1177			
1178			
1179	PRBA0		
1180	DATA	>###3	OP-CODE
1181	DATA	'LP'	DEV MNEM.
1182	DATA	>###1	CATEGORY 1
1183	DATA	'FUP/G'	LISTING IDENT.
1184	DATA	###	
1185			
1186			
1187	PRBC		
1188	DATA	>###4	OP-CODE
1189	DATA	###	
1190			
1191			
1192			
1193			
1194			
1195			
1196			
1197			
1198			
1199			
1200			
1201			
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1213			
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1234			
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1250			

980A OFF LINE ASSEMBLER
CR7 DISASSEMBLER

SHEET 38

038C	0021	PEJ		
038D	0400	--	OUTPUT 'ILLEGAL OP CODE'	
038E	0069	BSS		PLACE '*****' IN PALCE OF INST.
038F	0020	OLD	ASTERK	
0390	0400	0DST	OPBUF+21	
0391	006C	OLD	ILLEGL	PLACE 'ILLEGAL OP CODE'
0392	001F	0DST	OPBUF+24	
0393	0400	OLD	ILLEGL+2	
0394	006E	0DST	OPBUF+26	
0395	001E	OLD	ILLEGL+4	
0396	0400	0DST	OPBUF+28	
0397	0070	LDA	ILLEGL+6	
0398	001D	0STA	OPBUF+30	
0399	0400	TMBZ	15,LORH	
039A	0072	BRU	ILL03	OUTPUT TO LP SPOOL
039B	002F	0LDM	-OPCPRB	OUTPUT LINE BUFFER TO CONSOLE
039C	00A3	BRU	ILL05	
039D	0803	0LDM	-LPSPRB	
039E	1800	SVC		ERASE IT IN O/P BUFFER
039F	02CD	OLD	BLANK	
03A0	0802	0DST	OPBUF+24	
03A1	1800	0DST	OPBUF+26	
03A2	02C6	0DST	OPBUF+28	
03A3	0A00	0STA	OPBUF+30	
03A4	0012	RMO	L.P	
03A5	0400	ASTERK	DATA '-----'	
03A6	006C	ILLEGL	DATA 'ILLEGAL OP CODE'	
03A7	0400			
03A8	006E			
03A9	0400			
03AA	0070			
03AB	0400			
03AC	0072			
03AD	0557			
03AE	ADAD			
03AF	ADAD			
03B0	C9CC			
03B1	CCC5			
03B2	C7C1			
03B3	CCAF			
03B4	CFDB			
03B5	C3CF			
03B6	C4C5			
03B7	00A0	BLANK	DATA	BLANKS TO EARSE 'ILLEGAL CODE'
03B8	00A0			

98MA OFF LINE ASSEMBLER
CR7 DISASSEMBLER

1221	PEJ				
1222		BUMP ADDRESS POINTER - RESET TO START OF HBUF1			
1223		BRING NEXT 38 WORDS TO NEXT HBUF IF NECESSARY			
1224					
1225	BUMPA		OVER TO BUFFER 2 7		
1226	LDA	ACBUF0			
1227	RIM	A,A			
1228	OCPA	=HBUF1+38			
1229	SEO				
1230	BRU	L17#			
1231	STA	ACBUF0	YES - BRING NEXT 38 WORDS INTO HBUF2		
1232					
1233			BRING NEXT 34 WORDS INTO BUFFER 2		
1234					
1235	LDA	PAGENO			
1236	MPY	=38*2			
1237	RMO	E,A			
1238	ADD	GRLOC1	BYTE ADDR FOR NEXT PAGE		
1239	STA	G12+1	...IN 'G1'		
1240	OLDM	=RPRB2			
1241	SVC	#			
1242	INO	PAGENO	BUMP PAGE NO.		
1243	ODLD	BHBUF2	DESTN BYTE ADDR		
1244	RMO	A,M	...IN (M)		
1245	RMO	E,S	... & (S)		
1246	ODLD	BHBUF	SRCE BYTE ADDR IN (A), (E)		
1247	LDX	=38*2	CHAR COUNT		
1248	MVC		MOVE CHARACTERS		
1249	BRU	L21#			
1250					
1251	L17#	OCPA =HBUF2+38			
1252	SEO				
1253	BRU	L18#	NO		
1254	OLDA	=HBUF1	RESET TO HBUF1		
1255	STA	ACBUF0	YES		
1256					
1257			BRING NEXT 38 WORDS INTO HBUF1		
1258	LDA	PAGENO			
1259	MPY	=76			
1260	RMO	E,A			
1261	ADD	GRLOC1	BYTE STARTING ADDR FOR NEXT PAGE		
1262	STA	G1+1			
1263	OLDM	=RPRB			
1264					

980A OFF LINE ASSEMBLER
GR7 DISASSEMBLER

ADDRESS	OPERAND	OPERATION	START BYTE ADDR	WORD COUNT	CHAR COUNT	CHAR COUNT FOR 'GI'	INCREMENT PAGE NO.
E 03E0	0000						
03E1	CAE8	SVC					
03E2	5129	IMO					
03E3	C557	RMO L,P					
03E4	7802	BRU L218					
03E5	8400	QSTA					
03E6	00A2	ACBUFO					
03E7	C557	RMO L,P					
03E8	0002						
03E9	0001	DATA 2					
03EA	0006	DATA 1					
03EB	03EE	DATA 6					
03EC	0058	DATA GI2					
03ED	03F1	DATA 12+38*2					
03EE	4749	DATA R12					
03EF		DATA >4749					
03F0	0026	BSS 1					
03F1	5249	DATA 38					
03F2		DATA >5249					
03F3		BSS 1					
03F4	0000	DATA 0					
03F5		DATA 0					
03F7		BSS 2					
041D	0000	BSS 38					
07EE		BYTE HBUF					

R7 DISASSEMBLER

#463 AJC1
#464 CAC4
#465 D2A5
#466 AJA8
#467 C1C4
#468 C4D2
#469 AJA8
#470 AJA8
#471 D6C1
#472 CC05
#473 CSA8
#474 A8D6
#475 C1CC
#476 D5C5
#477 BA8D

1326 HD3PRL DATA >#203
1327 DATA 2
1328 DATA 5
1329 DATA HD3
1330 DATA 5
1331 DATA 5
1332 DATA 5
1333 DATA 4#
1334 HD3PRB DATA 5
1335 DATA 2
1336 DATA 4#
1337 DATA 5+1
1338 DATA

: CONSOLE
WRITE
CHAR COUNT

-----,LFCR

1339 END
1345

988A OFF LINE ASSEMBLER
GR7 DISASSEMBLER

SHEET 43

A	A5.16R	0224	0000
R	A5.19R	0268	0019
R	A5RET	0058	0268
R	ABLET	0000	0019
R	ACLS10	0147	0157
R	ACLS11	014E	0167
R	ACLS16	016A	0176
R	ACLS21	0189	018A
R	ACLS24	0118	0196
R	ACLS29	013D	011F
R	ADJRET	0352	0126
R	APTR00	0000	010A
R	ASSTR3	0006	012C
R	ASTERK	03AE	012F
R	B003F	0019	012F
R	B3F00	0018	012F
R	BHBUF	041D	012F
R	BLANK	03B7	012F
R	CHKALL	0353	012F
R	DISAS0	0049	012F
R	FORLN	023E	012F
R	GBUFF	0012	012F
R	GRFORM	0023	012F
R	HBUF	03E7	012F
R	HD1PRL	0427	012F
R	HD3PRB	0469	012F
R	ILL03	03A1	012F
R	INCPV	035C	012F
R	IPVAL2	0008	012F
R	IPVAL7	0010	012F
R	L0055	032F	012F
R	L210	03E7	012F
R	LPRB	041F	012F
R	MNE2LN	0271	012F
R	MNEM4	0096	012F
R	MNERET	02D1	012F
R	NEG0	0221	012F
R	NOVDP0	0248	012F
R	OPCPRB	02CD	012F
R	P0	0000	012F
R	P13	0000	012F
R	P4	0004	012F
R	P9	0009	012F
R	PCKS00	0290	012F
R	PCKSTR	0273	012F
R	PUTCHA	02DD	012F
R	RPRB	0026	012F
R	SABZ0	0225	012F
R	SWADV	033C	012F
R	VALUE	01E5	012F
R	VTC0	0051	012F
R	ZSUPP	01E6	012F
R	A5.15R	01E4	01E4
R	A51.2	0045	0045
R	A6	0000	0000
R	ACLS13	0157	0157
R	ACLS18	0176	0176
R	ACLS21	018A	018A
R	ACLS26	0126	0126
R	ADDRS	026C	026C
R	AGBUF	00A2	00A2
R	ASSTR5	0008	0008
R	ASSTR6	0008	0008
R	B0003	0015	0015
R	B00FF	0017	0017
R	B7FFF	001F	001F
R	BREG	0002	0002
R	CLSMAT	0008	0008
R	DVADV	0349	0349
R	FPTR	02F1	02F1
R	GETCHA	02D2	02D2
R	GRLOC1	00A0	00A0
R	HBUF2	0029	0029
R	HD2PRB	0448	0448
R	IBUFF	009E	009E
R	ILLCDE	038C	038C
R	INDIRY	0375	0375
R	IPVAL4	000D	000D
R	L0012	02A8	02A8
R	L007	0038	0038
R	L160	0373	0373
R	LINENO	00A5	00A5
R	LRA0	0222	0222
R	MMEM10	0099	0099
R	MMEM0	01DF	01DF
R	MPRMAT	00DA	00DA
R	NMASK	021E	021E
R	NPAGE	0426	0426
R	OPEFLD	0014	0014
R	P10	000A	000A
R	P15	000F	000F
R	P6	0006	0006
R	PACTOF	0081	0081
R	PCKS05	02EA	02EA
R	PRBA0	037F	037F
R	R12	03F1	03F1
R	RSBIT	021F	021F
R	SHBUF1	0351	0351
R	SWAP8	02F3	02F3
R	VLP	0378	0378
R	XB	0003	0003
R	A5.162	020F	020F
R	A51RET	01A1	01A1
R	ABLET	0000	0000
R	ACLS14	015F	015F
R	ACLS19	0177	0177
R	ACLS22	010C	010C
R	ACLS27	012F	012F
R	ADPTR	0313	0313
R	A1N2HX	01C0	01C0
R	ASSTR1	0004	0004
R	ASSTR6	0009	0009
R	B0007	0018	0018
R	B00FF	0016	0016
R	B0000	0052	0052
R	B12HAS	0001	0001
R	BUMPA	03B9	03B9
R	D000	0331	0331
R	E	0001	0001
R	FSTL	00A7	00A7
R	GRMASK	0021	0021
R	HD1	0432	0432
R	HD2PRL	0444	0444
R	IBUFF0	009D	009D
R	ILLEGL	0380	0380
R	INIT	018C	018C
R	IPVAL5	000E	000E
R	L0015	02BA	02BA
R	L001P	030F	030F
R	L170	03D2	03D2
R	LORH	00A3	00A3
R	LREG	000A	000A
R	MMEM2	0098	0098
R	MMEMS	0098	0098
R	NCLMA0	0085	0085
R	NOINS	0020	0020
R	OPADVA	0320	0320
R	OVER	0350	0350
R	P11	000B	000B
R	P2	0002	0002
R	P7	0007	0007
R	PAGENO	00A6	00A6
R	PCKS08	0300	0300
R	PRBC	0380	0380
R	RL2HX	01EA	01EA
R	S	0004	0004
R	SVC	CAE0	CAE0
R	TABO0	0223	0223
R	VRLVCB	02F7	02F7
R	YNPMA0	0006	0006
R	A5.164	020F	020F
R	A52.0	01A6	01A6
R	ACLS10	0147	0147
R	ACLS15	0165	0165
R	ACLS20	0100	0100
R	ACLS23	0111	0111
R	ACLS28	0136	0136
R	ADJRET	031F	031F
R	APTR00	0000	0000
R	ASSTR2	0005	0005
R	ASSTR7	000A	000A
R	B0038	0020	0020
R	B00FF	001A	001A
R	B0000	001E	001E
R	B12OAS	0003	0003
R	BWAR	02F2	02F2
R	FDPOS	01A2	01A2
R	GBUF00	026D	026D
R	G12	03EE	03EE
R	GRMNE	0024	0024
R	HD1PRB	042E	042E
R	HD3	046D	046D
R	IGBUF0	00A1	00A1
R	IN2HX0	01C9	01C9
R	INT2HX	01C5	01C5
R	IPVAL6	000F	000F
R	L0017	02C2	02C2
R	L0150	032D	032D
R	L180	03E5	03E5
R	LPERP	0026	0026
R	M	0003	0003
R	MNEM3	0092	0092
R	MNEPTR	0098	0098
R	NCLMAT	00AE	00AE
R	NOVDP	00A4	00A4
R	OPBUF	0054	0054
R	P	0007	0007
R	P12	000C	000C
R	P3	0003	0003
R	P8	0008	0008
R	PALT	007F	007F
R	PCKS10	02A3	02A3
R	PROGNO	007D	007D
R	RL2HX0	01EC	01EC
R	SABOIN	018F	018F
R	SVECTR	0083	0083
R	TRTR	02F0	02F0
R	VTC	0025	0025
R	YNPMT	00C4	00C4

V40155 NO ERRORS NO WARNINGS
19/03/84 16:05:18

V998AS 19/03/84 16:05:18

900A OFF LINE ASSEMBLER
DISASSEMBLER - PART 2

SHEET 1

DISASSEMBLER - PART 2 (ACTIONS)
T1980A

K. VO

ISSUE #
REVISION #

10T V90
HED DISASSEMBLER - PART 2

THIS MODULE CONTAINS A NUMBER OF ACTIONS OF
DISASSEMBLER PROGRAM.

DEF OREFLD,80003,807FF,801FF,83F00,8003F,80FFF,80007
DEF 87F00,8007F,80FFF,87FFF,80038
DEF GRINST,GRMASK,GRFORM,GRMNE,NOINS

REF ABLENT,IPVAL0

- 1
- 2
- 3
- 4
- 5
- 6
- 7
- 8
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- 10
- 11
- 12
- 13
- 14
- 15
- 16
- 17
- 18
- 19

SBMA OFF LINE ASSEMBLER
 DISASSEMBLER - PART 2

SHEET 2

21	***	PEJ	
22	***	EQU	1
23	A	EQU	2
24	E	EQU	3
25	X	EQU	4
26	M	EQU	5
27	S	EQU	6
28	L	EQU	7
29	B	EQU	1
30	P	EQU	3
31	BR	OPD	>CAEB,3
32	XB	OPD	>BA8D
33	SVC	EQU	1
34	LFCR	EQU	2
35	*	EQU	3
36	*	EQU	4
37	PB	EQU	5
38	P1	EQU	6
39	P2	EQU	7
40	P3	EQU	8
41	P4	EQU	9
42	P5	EQU	10
43	P6	EQU	11
44	P7	EQU	12
45	P8	EQU	13
46	P9	EQU	14
47	P10	EQU	15
48	P11	EQU	
49	P12	EQU	
50	P13	EQU	
51	P14	EQU	
52	P15	EQU	
53		EQU	

888A OFF LINE ASSEMBLER
DISASSEMBLER - PART 2

SHEET 3

54	*	PEJ			
55	*				
56	*				
57	*				
58	*				
59		OPEL	BSS	1	OPERAND FIELD
60		MNEM0	DATA	MNEM1	POINTER TO MNE STRG
61		MNEM1	BSS	2	1ST MNE. STRING (OP-CODE)
62		MNEM2	BSS	1	2ND
63		MNEM3	BSS	1	3RD
64		MNEM4	BSS	1	4TH
65		MNEMS	BSS	1	
66					
67		IBUFF0	BSS	1	POINTER TO INST. BUFFER
68		IBUFF	BSS	2	BUFFER OF ASSEMBLED INSTRUCTION
69		NOIN0	BSS	1	# OF WORDS FOR INSTRUCTION (1 OR 2)
70		NOINS	DATA	NOINST	# OF INSTRUCTIONS IN REPERTOIRE
71		GRLOC1	BSS	1	MEMORY LOCATION 1

DATA SECTION

P 8881
 8882
 8884
 8885
 8886
 8887
 8887
 8888
 888A
 888B
 888C
 FFD6

SMA OFF LINE ASSEMBLER
DISASSEMBLER - PART 2

SHEET 4

PEJ	GRINB	INSTRUCTION	GR7 INSTRUCTION MASKS
72	BSS	DATA >8000	
73	DATA	>8200	
74	DATA	>8240	
75	DATA	>8280	
76	DATA	>82C0	
77	DATA	>8400	
78	DATA	>8440	
79	DATA	>8480	
80	DATA	>84C0	
81	DATA	>8600	
82	DATA	>8840	
83	DATA	>8880	
84	DATA	>88C0	
85	DATA	>8A00	
86	DATA	>8C40	
87	DATA	>8C80	
88	DATA	>8E00	
89	DATA	>8E40	
90	DATA	>8E80	
91	DATA	>8EC0	
92	DATA	>1000	
93	DATA	>1040	
94	DATA	>1080	
95	DATA	>10C0	
96	DATA	>1100	
97	DATA	>2000	
98	DATA	>2040	
99	DATA	>2080	
100	DATA	>20C0	
101	DATA	>3000	
102	DATA	>4000	
103	DATA	>4800	
104	DATA	>5000	
105	DATA	>5800	
106	DATA	>6000	
107	DATA	>6800	
108	DATA	>7000	
109	DATA	>7800	
110	DATA	>8000	
111	DATA	>8040	
112	DATA	>C000	
113	DATA	>C040	
114	DATA	>9F80	
115	DATA	>8000	
116	DATA	>8E40	
117	DATA	>8DC0	
118	DATA	>8E80	
119	BSS		
120	NOINST EQU	GRINB-GRINST # OF INSTRUCTIONS	
121	NOINST EQU	NOINST*2	
122			
123			
124	DATA	>FFC0	1 HALT
125	DATA	>FFC0	
126	DATA	>FFC0	
127	DATA	>FFC0	

980A OFF LINE ASSEMBLER
DISASSEMBLER - PART 2

003A	FFC0	128	DATA >FFC0
003B	FFC1	129	DATA >FFC1
003C	FFC2	130	DATA >FFC2
003D	FFC3	131	DATA >FFC3
003E	FFC4	132	DATA >FFC4
003F	FFC5	133	DATA >FFC5
0040	FFC6	134	DATA >FFC6
0041	FFC7	135	DATA >FFC7
0042	FFC8	136	DATA >FFC8
0043	FFC9	137	DATA >FFC9
0044	FFCA	138	DATA >FFCA
0045	FFCB	139	DATA >FFCB
0046	FFCC	140	DATA >FFCC
0047	FFCD	141	DATA >FFCD
0048	FFCE	142	DATA >FFCE
0049	FFCF	143	DATA >FFCF
004A	F800	144	DATA >F800
004B	F801	145	DATA >F801
004C	F802	146	DATA >F802
004D	F803	147	DATA >F803
004E	F804	148	DATA >F804
004F	F805	149	DATA >F805
0050	F806	150	DATA >F806
0051	F807	151	DATA >F807
0052	F808	152	DATA >F808
0053	F809	153	DATA >F809
0054	F80A	154	DATA >F80A
0055	F80B	155	DATA >F80B
0056	F80C	156	DATA >F80C
0057	F80D	157	DATA >F80D
0058	C0C0	158	DATA >C0C0
0059	C0C1	159	DATA >C0C1
005A	C0C2	160	DATA >C0C2
005B	FFC0	161	DATA >FFC0
005C	FFC1	162	DATA >FFC1
005D	FFC2	163	DATA >FFC2
005E	FFC3	164	DATA >FFC3
005F	FFC4	165	DATA >FFC4
0060	FFC5	166	DATA >FFC5
0061		167	GRMASK BSS
		168	*
		169	*
		170	*
0061	0000	171	DATA #
0062	0001	172	DATA 1#
0063	0002	173	DATA 13
0064	0003	174	DATA 1#
0065	0004	175	DATA 13
0066	0005	176	DATA 1#
0067	0006	177	DATA 9
0068	0007	178	DATA 13
0069	0008	179	DATA #
006A	0009	180	DATA #
006B	000A	181	DATA 1#
006C	000B	182	DATA 1
006D	000C	183	DATA 1

GR 7 INSTRUCTION FORMATS

38	CHAR	HALT
39	TEXT	
40	INIT (GR8)	
41	CLRM (")	
42	MODE (")	

5 JPRZ

10 IZPR

15 JMPR

20 L00Z

25 DRXR

30 MVYA

35 MVSR

IZPR

980A. OFF LINE ASSEMBLER
DISASSEMBLER - PART 2

```

006E 000E 184
006F 0003 185
0070 0008 186
0071 000C 187
0072 0009 188
0073 0000 189
0074 0002 190
0075 0005 191
0076 0005 192
0077 0005 193
0078 0005 194
0079 0005 195
007A 0005 196
007B 0005 197
007C 0005 198
007D 0005 199
007E 0005 200
007F 0005 201
0080 0002 202
0081 0002 203
0082 0004 204
0083 0004 205
0084 0004 206
0085 0006 207
0086 0008 208
0087 0009 209
0088 0000 210
0089 0000 211
008A 0000 212
008B 0000 213
008C 0000 214
008D 0000 215
008E 0000 216
008F 0000 217
0090 0000 218
0091 0000 219
0092 0000 220
0093 0000 221
0094 0000 222
0095 0000 223
0096 0000 224
0097 0000 225
0098 0000 226
0099 0000 227
009A 0000 228
    
```

LDDZ

MVYA

CHAR
TEXT
INIT (CR8)
CLRHM (")
MODE (")

GRFORM BSS

GR7 INSTRUCTION MNEMONICS

```

0088 0000 218
0089 0000 219
008A 0000 220
008B 0000 221
008C 0000 222
008D 0000 223
008E 0000 224
008F 0000 225
0090 0000 226
0091 0000 227
0092 0000 228
0093 0000 229
0094 0000 230
0095 0000 231
0096 0000 232
0097 0000 233
0098 0000 234
0099 0000 235
009A 0000 236
009B 0000 237
009C 0000 238
009D 0000 239
009E 0000 240
009F 0000 241
00A0 0000 242
00A1 0000 243
    
```

887A OFF LINE ASSEMBLER
DISASSEMBLER - PART 2

SHEET 7

88A2	D6C4				
88A3	D2C5	229	DATA 'RESO'		
88A4	D3C4		DATA 'ADDI'		
88A5	C1C4	230	DATA 'JMPR'	15	
88A6	C4C9				
88A7	CACD	231			
88A8	D8D2				
88A9	CCC4	232	DATA 'LDRI'		
88AA	D2C9		DATA 'LDDI'		
88AB	CCC4	233	DATA 'LODDZ'		
88AC	C4C9				
88AD	CCC4	234	DATA 'LDSP'		
88AE	D3D8		DATA 'VATE'		
88AF	D7C1	235	DATA 'LDDZ'		
88B0	D4C5	236	DATA 'LDDZ'	28	
88B1	CCC4				
88B2	C4DA	237	DATA 'LOOP'		
88B3	CCC4				
88B4	C4D8	238	DATA 'LDXA'		
88B5	CCC4		DATA 'LDXR'		
88B6	D8C1	239	DATA 'LDXR'		
88B7	CCC4				
88B8	D8D2	240	DATA 'DRXA'		
88B9	C4D2		DATA 'DRXR'		
88BA	D8C1	241	DATA 'DRXR'	25	
88BB	C4D2				
88BC	D8D2	242	DATA 'DRYA'		
88BD	C4D2		DATA 'DRYR'		
88BE	D9C1	243	DATA 'DRYR'		
88BF	C4D2		DATA 'MVXA'		
88C0	D9D2	244	DATA 'MVXR'		
88C1	CDD6	245	DATA 'MVXR'		
88C2	D8C1				
88C3	CDD6	246	DATA 'MVYA'	38	
88C4	D8D2		DATA 'MVYR'		
88C5	CDD6	247	DATA 'MVYR'		
88C6	D9C1				
88C7	CDD6	248	DATA 'LDKX'		
88C8	D9D2		DATA 'DRKY'		
88C9	CCC4	249	DATA 'DRSR'		
88CA	C8D8		DATA 'MVSR'		
88CB	C4D2	250	DATA 'PPLR'		
88CC	C8D9				
88CD	C4D2	251	DATA 'LDTI'		
88CE	D3D2		DATA 'CHAR'		
88CF	CDD6	252	DATA 'TEXT'		
88D0	D3D2		DATA 'INIT'		
88D1	D8D8	253			
88D2	CCD2				
88D3	CCC4	254			
88D4	D4C9				
88D5	C3C8	255			
88D6	C1D2				
88D7	D4C5				
88D8	D8D4				
88D9	C9CE	256			48 (GR8)

987A OFF LINE ASSEMBLER
DISASSEMBLER - PART 2

987A	C9D4			
987B	C3CC	257	DATA 'CLRM'	41 (')
987C	D2CD			
987D	CDCF	258	DATA 'MODE'	42 (' ')
987E	C4C5			
987F		259	GRMME BSS	

SHEET 8

900A OFF LINE ASSEMBLER
DISASSEMBLER - PART. 2

Address	Label	Instruction	Comments
260		PEJ	
261			
262			
263			
264			
265	BSS	BSS	
266	OLDA	OLDA ->B7FF	
267	STA	STA *OPEFLO	OPERAND FIELD
268	RMO	RMO L.P	
269			
270			
271			
272	A9.4	A9.4 BITS 5-15	
273	BSS	BSS	
274	OLDA	OLDA ->B7FF	
275	STA	STA *OPEFLO	
276	RMO	RMO L.P	
277			
278			
279	--	-- BITS 7-15	
280	OLDA	OLDA ->B7FF	
281	STA	STA *OPEFLO	
282	RMO	RMO L.P	
283			
284			
285			
286			
287	--	-- BITS 2-7	
288	OLDA	OLDA ->3F00	
289	STA	STA *OPEFLO	
290	RMO	RMO L.P	
291			
292			
293	--	-- BITS 10-15	
294	OLDA	OLDA ->B7FF	
295	STA	STA OPEFLO	
296	RMO	RMO L.P	
297			
298			
299	--	-- BITS 8ITS 4-15	
300	OLDA	OLDA ->B7FF	
301	STA	STA *OPEFLO	
302	RMO	RMO L.P	
303			
304			
305			
306	--	-- BITS 13-15	
307			
308			

980A OFF LINE ASSEMBLER

DISASSEMBLER - PART 2

80F8	8000	0LDA	->8007		
80F9	8007	STA	"OPEFLO		
80FA	8405	RMO	L.P		
80FB	C557				
80FC	8000	--	BITS 1-7		
80FD	7F00	0LDA	->7F00		
80FE	8401	STA	"OPEFLO		
80FF	C557	RMO	L.P		
8100	8000	OPEFLO	DATA OPEFLD		
8101	8000	--	BITS 9-15		
8102	807F	0LDA	->807F		
8103	84FC	STA	"OPEFLO		
8104	C557	RMO	L.P		
8105	8000	--	BITS 0-15		
8106	FFFF	0LDA	->FFFF		
8107	84F8	STA	"OPEFLO		
8108	C557	RMO	L.P		
8109	8000	--	BITS 1-15		
810A	7FFF	0LDA	->7FFF		
810B	84F4	STA	"OPEFLO		
810C	C557	RMO	L.P		
810D	8000	0LDA	->8038		
810E	8038	STA	"OPEFLO		
810F	84F0	RMO	L.P		
8110	C557				
8111	8001	MAXPOS	DATA >1		MAX POSITIVE VALUES
8112	8003	DATA	>3		2 BITS
8113	8007	DATA	>7		FIELD
8114	800F	DATA	>F		3
8115	801F	DATA	>1F,		4
8116	803F	DATA	>3F,		5
8117	807F	DATA	>7F,		6
8118	80FF	DATA	>FF,		7
8119	81FF	DATA	>1FF,		8
					9
					10

980A OFF LINE ASSEMBLER
DISASSEMBLER - PART 2

Address	Hex	Label	Field	Value	Field	Value
011A	03FF	DATA	>3FF		11	
011B	07FF	DATA	>7FF		12	
011C	0FFF	DATA	>FFF		13	
011D	1FFF	DATA	>1FFF		14	
011E	3FFF	DATA	>3FFF		15	
011F	7FFF	DATA	>7FFF		16	
0120	FFFF	DATA	->1	MIN NEGATIVE VALUES		
0121	FFF0	DATA	->3	2 BITS FIELD		
0122	FFF9	DATA	->7		3	
0123	FFF1	DATA	->F		4	
0124	FFE1	DATA	->1F		5	
0125	FFC1	DATA	->3F		6	
0126	FF71	DATA	->8F		7	
0127	FF01	DATA	->FF		8	
0128	FE01	DATA	->1FF		9	
0129	FC01	DATA	->3FF		10	
012A	F801	DATA	->7FF		11	
012B	F001	DATA	->FFF		12	
012C	E001	DATA	->1FFF		13	
012D	C001	DATA	->3FFF		14	
012E	8001	DATA	->7FFF		15	
012F	0000	DATA	->7FFF		16	
0130	0000	DATA	->7FFF			

FIELD POSITION, # OF BITS FROM RIGHT

FLOPOS BSS 1
18UFO0 DATA 18UFF
END

980A OFF LINE ASSEMBLER
DISASSEMBLER - PART 2

R A	0000	R ABLENT	0000
R	0038	003F	00EF
R	00FF	00F4	00EB
R	0037	E	0001
R	0008	R GRLOC1	0007
R	0003	R IBUFF0	0007
R	0005	R MAXNEG	0120
R	0002	R MMEM3	0006
R	0005	R MMEM4	0006
R	0002	R NOINST	FFD6
R	0007	R P0	0000
R	000C	R P12	0000
R	0003	R P3	0000
R	0008	R P8	0004
R	0003	R X8	0009

R B	007F	0006
R	07F0	0101
R	FLDPOS	00FC
R	GRMASK	012F
R	IPVAL0	0061
R	MAXPOS	0111
R	MMEM0	0001
R	NOVRDS	000A
R	P1	0000
R	P14	000E
R	P5	0005
R	S	0004

R	0003	0003
R	01FF	00E7
R	07FF	0109
R	GRFORM	000B
L	GRMNE	00DF
L	MMEM1	0005
R	MMEMS	0002
R	OPEFL0	0007
R	P10	0100
R	P15	000A
R	P6	000F
R	SVC	0006
R		CAEB

R	0007	0007
R	07FF	00E3
R	0FFF	0105
R	GRINB	000D
R	IBUF00	0130
R	LFCR	0A0D
R	MMEM2	0004
R	NOINS	0008
R	OPEFLD	0000
R	P11	0000
R	P2	0002
R	P7	0007
R	X	0002

SHEET 12

V9004S 19/03/84 NO ERRORS NO WARNINGS
16:05:00

VD#88S 07/03/84 16:03:32

980A ON LINE ASSEMBLER
ROUTINES

SHEET 1

SUBROUTINES
T198AA

K. VO

ISSUE #
REVISION #

VAP

IDT
HED
DEF

B12OAS, VTC, HAS2B1, CC
B12DAS, B12HAS, B12OAS, VTC, HAS2B1, CC

- 1
- 2
- 3
- 4
- 5
- 6
- 7
- 8
- 9
- 10
- 11
- 12

980A ON LINE ASSEMBLER
ROUTINES

SHEET 2

13	****	PEJ	B	1	
14	****	EQU	2		
15	****	EQU	3		
16	****	EQU	4		
17	****	EQU	5		
18	****	EQU	6		
19	****	EQU	7		
20	****	EQU	1		
21	****	EQU	3		
22	****	OPD	>CAES,3		
23	****	EQU	>8ABD		
24	****				
25	****				
26	****				
27	****				
28	****				

980A ON LINE ASSEMBLER
ROUTINES

Address	Code	Comments
29	PEJ	
30		
31		
32		
33		CONVERT FROM BINARY TO DEC ASCII LEADING ZERO(S) SUPPRESSED
34		
35		ENTRY : (A)-BIN VALUE
36		
37		RETURN : (X) 1ST MS DIGIT
38		(A),(E)=4 LS ASCII DIGITS
39		
40		
41	B12DAS	LDX --5
42	LDE --B	
43	OLDM ->BBB	
44	COMP	
45	CPL	DCONST,X
46	SLE	
47	BRU	MXUNIT
48	SUB	DCONST,X
49	RIN	E,E
50	BRU	COMP
51		
52	MXUNIT	RAD M,E
53	STE	ASBUF,X
54	LDE --B	
55	BIX	COMP
56	LDX --5	
57	OLDA ->BBB	
58		
59	SUPZER	CPA ASBUF,X
60	SEQ	
61	BRU	PACK
62	SNO	X
63	BRU	PACK
64	OLDE ->BBB	
65	STE	ASBUF,X
66	BIX	SUPZER
67		
68	PACK	LDX ASBUF-5
69		LDA ASBUF-2
70		LLA 8
71		ADD ASBUF-1
72	RMO	A,E
73	LDA	ASBUF-4
74	LLA	8
75	ADD	ASBUF-3
76	RMO	L,P
77	ASBUFR	BSS 6
78	ASBUF	BSS 6
79	ASBUF	BSS 6
80		
81		DATA 10000
82		
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986A ON LINE ASSEMBLER
ROUTINES

002A	03E9	02	DATA	1000
002B	0064	03	DATA	1001
002C	000A	04	DATA	10
002D	0001	05	DATA	1
002E		06	DCONST	BSS
002F		07	REGD	BSS
		08	VSRETN	BSS

SHEET 4

89	PEO						
90		CONVERT FROM BINARY TO HEX ASCII					
91			ENTRY :	(A)-BIN VALUE			
92				(E)-1 LEADING ZERO(S) SUPPRESSED			
93			RETURN :	(A)-2 MS ASCII DIGITS			
94				(E)-2 LS ASCII DIGITS			
95				(M), (X) DESTROYED			
96							
97							
98							
99							
100	BIZHAS	BSS		SAVE ZERO SUPPRESSION			
101	101	STE ZSUPPR					
102	102	LDX --4					
103	103	LDE --8					
104	104	OLDM -->8888					
105							
106		HCOMP					
107		CPL	HCONST,X	CONVERSION UNIT			
108		SLE	B100	UNIT .LE. VALUE ?			
109		BRU	HCONST,X	NO			
110		SUB	HCONST,X	YES - SUBSTRACT			
111		RIM	E.E	AND COUNT			
112		BRU	HCOMP	KEEP GOING			
113		REX	E.A				
114		OLDM	-->8888	ASSUME <= 9			
115		CPA	--A	DIG OR CHAR ?			
116		SLE	B105	DIGIT			
117		BRU	--A	CHAR			
118		SUB	-->88C1				
119		OLDM					
120		RAD	M.A	HEX ASCII			
121		REX	E.A				
122		STE	ASBUF,X	RESET (E)			
123		LDE	--8	CONTINUE CONVERSION TO 4 DIGITS			
124		B1X	HCOMP				
125				SUPPRESS LEADING ZERO(S)			
126							
127							
128		LDA	ZSUPPR	ZERO SUPPRESSION ?			
129		SOO	A				
130		BRU	B108	NO			
131		LDX	--4	YES			
132		OLDA	-->8888				
133		SUPPZE	CPA	ASBUF,X	ZERO ?		
134		SEQ					
135		BRU	B108	NO - STOP SUPPRESSION			
136		SNO	X	YES - LAST DIGIT ?			
137		BRU	B108	YES - LAST ZERO IS NOT SUPPRESSED			
138		OLDE	-->8888	NO - SUPPRESS			
139		STE	ASBUF,X				

380A ON LINE ASSEMBLER
ROUTINES

0057	00F7	140	BIX	SUPPZE	
0058	00CE	141	LDA	ASBUF-2	2 LS DIGITS
0059	C8C8	142	LLA	8	
005A	20CD	143	ADD	ASBUF-1	
005B	C501	144	RMO	A,E	2 MS DIGITS
005C	00C8	145	LDA	ASBUF-4	
005D	C8C8	146	LLA	8	
005E	20C7	147	ADD	ASBUF-3	
005F	C557	148	RMO	L,P	RETURN
0060	1000	149	DATA	>1000	
0061	0100	150	DATA	>1000	
0062	0010	151	DATA	>10	
0063	0001	152	DATA	>1	
0064		153	HCONST	BSS	
0065		154	ZSUPPR	BSS	
		155			
		156			

-1 : ZERO SUPPR. REQUESTED

980A ON LINE ASSEMBLER
ROUTINES

157	PEJ				
158					
159					
160					
161					
162					
163					
164					
165					
166					
167	B120AS	LDX	--6		
168		LDE	=#		
169		OLDM	=>####		
170					
171		OCONST,X			CONVERSION UNIT
172		SLE			UNIT .LE. VALUE ?
173		BRU	BIO##		NO
174		SUB	OCONST,X		YES - SUBSTRACT
175		RIM	E,E		AND COUNT
176		BRU	OOMP		KEEP GOING
177					
178		RAD	M,E		ASCII DIGIT
179		STE	ASBUF,X		
180		LDE	=#		RESET (E)
181		BIX	OOMP		CONTINUE CONVERSION TO 5 DIGITS
182		LDA	ASBUF-6		
183		LLA	B		
184		ADD	ASBUF-5		
185		RMO	A,X		2 LS DIGITS
186		LDA	ASBUF-2		
187		LLA	B		
188		ADD	ASBUF-1		
189		RMO	A,E		2 MIDDLE DIGITS
190		LDA	ASBUF-4		
191		LLA	B		
192		ADD	ASBUF-3		
193		RMO	L,P		RETURN
194					
195		DATA	>####		
196		DATA	#####		
197		DATA	#####		
198		DATA	#####		
199		DATA	#####		
200		DATA	#####		
201		OCONST	BSS		

980A ON LINE ASSEMBLER
ROUTINES

88A6	C898	250	E.A
88A7	2887	251	RESULT
88A8	8886	252	STA RESULT
88B1	5887	253	IMO COUNT
88B2	4888	254	BIX HAS#
88B3	8883	255	LDA RESULT
88B4	C557	256	RMO L.P
88B5	17FF	257	HAS#4
88B6	C557	258	LDX --1
88B7		259	RMO L.P
88B8		261	RESULT BSS 1
88B9		262	TEMPTR BSS 1
		263	COUNT BSS 1

...B-FFFF INTO (A)

SET ERROR ON RETURN

RESULT OF CONVERSION
TEMP POINTER
COUNT OF ASCII

980A ON LINE ASSEMBLER
ROUTINES

SHEET 10

264	PEJ			
265				
266				
267				
268				
269				
270				
271				
272				
273	CC			
274				
275				
276				
277				
278				
279				
280	CC5			
281				
282				
283				
284				
285				
286				
287				
288	CC7			
289				
290				
291				
292				
293				
294				
295				
296				
297	CC10			
298				
299				
300				
301				
302	CCRET			
303	CCNT			
304				
305	TTPTR			

SUBROUTINE TO COUNT NUMBER OF WORDS IN LINE
 A LINE IS TERMINATED (AND INCLUDING) BY CR
 IF CR IS LOCATED IN LH BYTE, A BLANK IS INSERTED BEFORE IT
 ENTRY : (M)= START ADDR OF BUFFER
 RETURN : (A)= # OF WORDS (INCLUDING CR)

BSS #
 RMO L.A
 STA CCRET
 LDA --1
 STA CCNT
 IMO CCNT
 LDA CCNT
 BRL GETCH
 @AND =>@7F
 @CPA =>@80D
 SEQ CC5
 BRU CC5
 TMBZ 15,CCNT
 BRU CC10
 LDA CCNT
 @LDE =>@80D
 LRA 1
 RAD M.A
 STA TTPTR
 STE *TTPTR
 IMO CCNT
 LDA CCNT
 ADD -1
 LRA 1
 BRU *CCRET
 BSS 1
 BSS 1
 BSS 1

RESET CHAR COUNT
 GET CHAR
 CR ?
 NO - CHECK NEXT CHAR
 YES
 IS CR IS LH BYTE ?
 NO
 YES - THEN PLACE BLANK BEFORE CR
 WD OFFSET FROM START OF BUFFER
 FORM POINTER
 PLACE BLANK + CR
 TO COUNT THE BLANK
 CHAR COUNT
 ADJUST
 WORD COUNT
 ..ON RETURN
 RETURN
 CHAR COUNT
 TEMP POINTER

980A ON LINE ASSEMBLER
ROUTINES

SHEET 11

306	PEJ		
307		GET A CHARACTER FROM A STRING	
308		ENTRY : (H) = START ADDR. OF STRING	
309		(A) = BYTE OFFSET WITHIN STRING	
310			
311			
312		RETURN : (A) = THE CHARACTER, RH-JUSTIFIED	
313		ALL OTHER REGISTERS PRESERVED	
314			
315			
316	GETCH		
317	BSS	B	SAVE BYTE OFFSET
318	STA	BOFF	CONVERT TO WORD OFFSET
319	LRA	1	FORM POINTER TO CHARACTER
320	RAD	M,A	
321	STA	TPTR	GET THE CHARACTER
322	LDA	TPTR	... IN RH BYTE
323	TMBO	15,BOFF	
324	LRA	8	
325	QAND	->BOFF	
326	RMO	L,P	RETURN
327	BSS	1	BYTE OFFSET
328	BSS	1	TEMPORARY POINTER

309	8009	
310	800A	
311	C041	
312	800B	
313	C000	
314	800C	
315	800D	
316	800E	
317	800F	
318	8010	
319	8011	
320	8012	
321	8013	
322	8014	
323	8015	
324	8016	
325	8017	
326	8018	
327	8019	
328	801A	

980A ON LINE ASSEMBLER
ROUTINES

SHEET 12

329	PEJ				
330					
331					
332					
333					
334					
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364					
365					

98AA ON LINE ASSEMBLER

ROUTINES			
0116	0004	DATA 4	*,LFCR
0117	0005	DATA	
0118	0006		
0119	0007		
011A	0008	DATA 12	*,LFCR,LFCR
011B	0009	DATA	
011C	0010		
011D	0011		
011E	0012		
011F	0013		
0120	0014		
0121	0015		
0122	0016		
0123	0017		
0124	0018		
0125	0019		
0126	0020		
0127	0021		
0128	0022		
0129	0023		
012A	0024		
012B	0025		
012C	0026		
012D	0027		
012E	0028		
012F	0029		
0130	0030		
0131	0031		
0132	0032		
0133	0033		
0134	0034		
0135	0035		
0136	0036		
0137	0037		
0138	0038		
0139	0039		
013A	0040		
013B	0041		
013C	0042		
013D	0043		
013E	0044		
013F	0045		
0140	0046		
0141	0047		
0142	0048		
0143	0049		
0144	0050		
0145	0051		
0146	0052		
0147	0053		
0148	0054		
366	M1	DATA 4	*,LFCR
367		DATA	
368	*		
369	M15	DATA 12	*,LFCR,LFCR
370		DATA	
371	M11	DATA 8	*,LFCR,LFCR
372		DATA	
373	M12	DATA 22	**INVALID COMMAND *,LFCR
374		DATA	
375	M15	DATA 26	**INVALID ARGUMENT(S) *,LFCR
376		DATA	
377	M16	DATA 18	*,LFCR
378		DATA	**FUP/G TERMINATES*,LFCR
379			END

988A ON LINE ASSEMBLER
ROUTINES

A 8888
B185 8844
B1088 886F
CC5 88BE
COUNT 8889
HASM1 88A1
HCONST 8864
M1 8816
M16 883F
P 8887
RESULT 8887
TEMPTR 8888
X 8882

R ASBUF 8829
B12DAS 8888
BR 8881
CCNT 88D7
E 8881
HASM4 8885
LFCR 8880
M11 8828
NKUMIT 888A
PVTC 88F7
SUPPZE 884F
TTPTR 88D8
ZSUPPR 8864

B B12HAS 8886
CC 8838
CCRET 88BA
GETCH 88D9
HASM281 8885
M 8883
M12 8825
OCOMP 8869
R REG8 882E
SUPZER 8811
R VSRETN 882F

A B188 883B
B12OAS 8865
CC18 88D2
COMP 8884
HASM8 8888
HCOMP 8835
M8 8814
M15 8831
OCONST 8885
REGF2 88FB
SVC CAEM 8887
VTC 8866

SWEET 14

NO ERRORS NO WARNINGS
VD#88 87/83/84 16:04:29